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Digital Principles and Design Donald D. Givone, 2003 Foundations of Digital Logic Design Gideon Langholz, Abraham Kandel, Joe L. Mott, 1998 **Digital Logic Design** Guy Even, Moti Medina, 2012-10-08 This textbook based on the authors fifteen years of teaching is a complete teaching tool for turning students into logic designers in one semester Each chapter describes new concepts giving extensive applications and examples Assuming no prior knowledge of discrete mathematics the authors introduce all background in propositional logic asymptotics graphs hardware and electronics Important features of the presentation are All material is presented in full detail Every designed circuit is formally specified and implemented the correctness of the implementation is proved and the cost and delay are analyzed Algorithmic solutions are offered for logical simulation computation of propagation delay and minimum clock period Connections are drawn from the physical analog world to the digital abstraction The language of graphs is used to describe formulas and circuits Hundreds of figures examples and exercises enhance understanding The extensive website <http://www.eng.tau.ac.il/guyEven> Medina includes teaching slides links to Logisim and a DLX assembly simulator *Digital Logic Design* Brian Holdsworth, 1993 **A Systematic Approach to Digital Logic Design** Frederic J. Mowle, 2000 **Digital Logic Design Principles** Norman Balabanian, Bradley Carlson, 2001 **Digital Logic: Applications And Design**, Logic Design of Digital Systems Donald Leo Dietmeyer, 1988 **LOGIC DESIGN OF DIGITAL SYSTEMS** Donald Leo Dietmeyer, 1971 *Fundamentals of Digital Logic with VHDL Design* Stephen Brown, Zvonko Vranesic, 2022 Fundamentals of Digital Logic with VHDL Design is intended for an introductory course in digital logic design which is a basic course in most electrical and computer engineering programs A successful designer of digital logic circuits needs a good understanding of the classical methods of logic design and a firm grasp of the modern design approach that relies on computer aided design CAD tools The main goals of this book are to teach students the fundamental concepts of classical manual digital design and to illustrate clearly the way in which digital circuits are designed today using CAD tools This title will be available in Connect with the MHeBook but will not have SmartBook at this time *Some General Comments on the Methods of Digital Logic Design* Donald E. Cieslak, 1964 *DIGITAL LOGIC AND COMPUTER DESIGN*, 2001 Solutions manual Gideon Langholz, Abraham Kandel, Joe L. Mott, 1988 *DIGITAL LOGIC DESIGN* Sonali Singh, 2018-06-01 Description The book is an attempt to make Digital Logic Design easy and simple to understand The book covers various features of Logic Design using lots of examples and relevant diagrams The complete text is reviewed for its correctness This book is an outcome of sincere effort and hard work to bring concepts of Digital Logic Design close to the audience of this book The salient features of the book Easy explanation of Digital System and Binary Numbers with lots of solved examples Detailed covering of Boolean Algebra and Gate Level Minimization with proper examples and diagrammatic representation Detailed analysis of different Combinational Logic Circuits Complete Synchronous sequential Logic understanding Deep understanding of Memory and Programmable Logic

Detailed analysis of different Asynchronous Sequential Logic
Table Of Contents Unit 1 Digital System and Binary Numbers
Part 1 Digital System and Binary Numbers Part 2 Boolean Algebra and Gate Level Minimization Unit 2 Combinational Logic Unit 3 Sequential Circuits Unit 4 Memory Programmable Logic and Design Unit 5 Asynchronous Sequential Logic

Digital Logic Design John Passafiume, Michael Douglas, 1991-01-16 The perfect complement to computer architecture and logic texts This widely praised tutorial and lab book gives practice in the fundamentals of digital logic and circuitry with special emphasis on how the machine operates at the gate and register level Presentation employs the TTL family of digital logic due to its wide availability and moderate cost Exercises require the student to perform a simple designs and then implement them on hardware Contains sufficient exercises for a 3 hour lab meeting once a week for a semester

Handbook of Digital Techniques for High-Speed Design ,2007-09 **Digital Principles and Logic Design** Arijit Saha, Nilotpal Manna, 2009-01-28 This text and reference provides students and practicing engineers with an introduction to the classical methods of designing electrical circuits but incorporates modern logic design techniques used in the latest microprocessors microcontrollers microcomputers and various LSI components The book provides a review of the classical methods e g the basic concepts of Boolean algebra combinational logic and sequential logic procedures before engaging in the practical design approach and the use of computer aided tools The book is enriched with numerous examples and their solutions over 500 illustrations and includes a CD ROM with simulations additional figures and third party software to illustrate the concepts discussed in the book *Digital Logic Design* Gideon Langholz, 1988 **Digital Logic Circuit Analysis and Design** ,1995 **Digital Logic and State Machine Design** David J. Comer, 1995

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