

# BUT DC Examples

**Problem-1** For the fixed-bias configuration below, determine

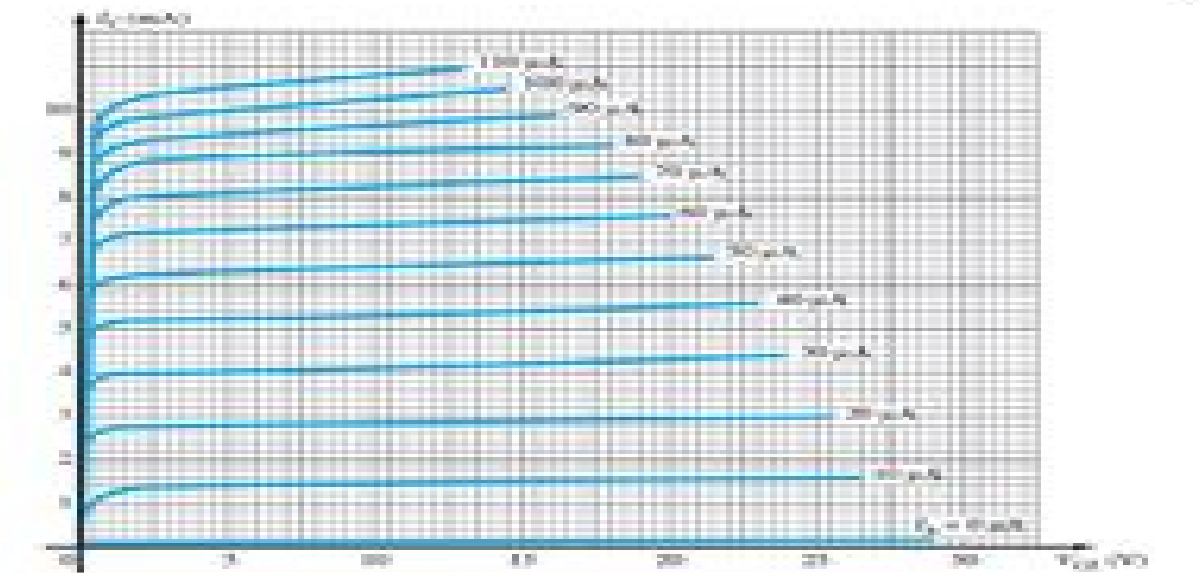
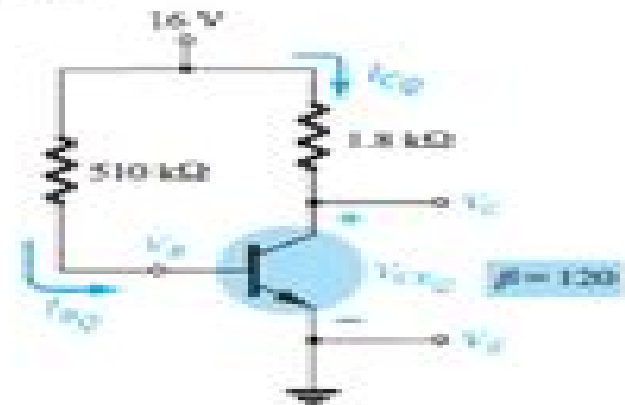
- $I_{BQ}$
- $I_{CQ}$
- $V_{CEQ}$
- $V_{CQ}$
- $V_{BQ}$
- $V_E$

g. Find the saturation current ( $I_{C(sat)}$ )

h. Ignoring the provided value of  $\beta$  (120) draw the load line on the characteristics of figure

i. Find the Q-point and resulting  $I_{CQ}$  and  $V_{CEQ}$

j. What is the  $\beta$  value at this Q-point?



k. If the base resistor is increased to 910 kΩ, find the new Q-point and resulting values of  $I_{CQ}$  and  $V_{CEQ}$

# Basic Engineering Circuit Analysis 10e Irwin Solutions

**J. David Irwin**



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