

```
.model nmos NMOS
.model pmos PMOS
```

7.5

For the gate shown in Fig. P7.5,

- Pull-up transistor ratio is 5/5
- pull-down transistor ratios are 100/5
- $V_{DD} = 1.0$ V
- $\gamma = 0.4$ V^{1/2}
- $|2\phi_F| = 0.6$ V

- Identify the worst case input combination(s) for V_{OL} .
- Calculate the worst case value of V_{OL} . (Assume that all pull-down transistors have the same body bias and initially, that $V_{OL} = 5\% V_{DD}$.)

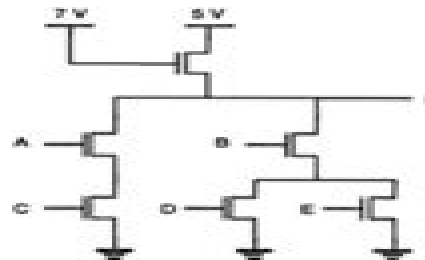


Figure P7.5

SOLUTION:

- The worst case input combinations are those that cause only one current path from output to ground. The combinations are listed in the following table.

$A = 1$	$C = 1$	$B = 0$	
$A = 1$	$C = 1$	$C = 0$	$E = 0$
$B = 1$	$D = 1$	$A = 0$	$E = 0$
$B = 1$	$D = 1$	$C = 0$	$E = 0$
$B = 1$	$E = 1$	$A = 0$	$D = 0$
$B = 1$	$E = 1$	$C = 0$	$D = 0$

- The worst case equivalent circuit can be represented by a simple inverter, with the driver $W/L=10$, load $W/L=1$. Both transistors are operating in linear region in this case because:
Driver: $V_{GS} = 5$, $V_{DS} = 0.25$

Digital Integrated Circuits Solution Manual

Sanjit K. Mitra, Sanjit Kumar Mitra



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Devices and Circuit Fundamentals is Chapter Outline Learning Objectives Key Terms Figure List Chapter Summary Formulas Answers to Examples Self Exams Glossary of Terms defined

Solutions Manual Digital Integrated Circuits CRC Press, 2009-07-10 Any textbook more than five years old simply won't do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization Each chapter includes numerous worked examples case studies and SPICE computer simulations The book's website offers supplementary material and more worked problems Qualifying instructors will have access to a new instructor's manual

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Solutions Manual Kang, 1995-08-01 *Solutions Manual for An Introduction to Digital and Analog Integrated Circuits and Applications* Sanjit K. Mitra, Sanjit Kumar Mitra, 1981 *Analysis and Design of Digital Integrated Circuits* David A. Hodges, Horace G. Jackson, 1988 This is a state of the art treatment of the circuit design of digital integrated circuits It includes coverage of the basic concepts of static characteristics voltage transfer characteristics noise margins fanout power dissipation and dynamic characteristics propagation delay times and the interrelationships among these parameters The authors are regarded as leading authorities in integrated circuits and MOS technology

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Engineering Education, 1990 *Introduction to VLSI Systems* Ming-Bo Lin, 2011-11-28

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technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding of circuit and layout design issues Furthermore engineers can often develop their physical intuition to estimate the behavior of circuits rapidly without relying predominantly on computer aided design CAD tools Introduction to VLSI Systems A Logic Circuit and System Perspective addresses the need for teaching such a topic in terms of a logic circuit and system design perspective To achieve the above mentioned goals this classroom tested book focuses on Implementing a digital system as a full custom integrated circuit Switch logic design and useful paradigms that may apply to various static and dynamic logic families The fabrication and layout designs of complementary metal oxide semiconductor CMOS VLSI Important issues of modern CMOS processes including deep submicron devices circuit optimization interconnect modeling and optimization signal integrity power integrity clocking and timing power dissipation and electrostatic discharge ESD Introduction to VLSI Systems builds an understanding of integrated circuits from the bottom up paying much attention to logic circuit layout and system designs Armed with these tools readers can not only comprehensively understand the features and limitations of modern VLSI technologies but also have enough background to adapt to this ever changing field

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Integrated Circuit Design Xiaokun Yang, 2024-11-20 This textbook seeks to foster a deep understanding of the field by introducing the industry integrated circuit IC design flow and offering tape out or pseudo tape out projects for hands on practice facilitating project based learning PBL experiences Integrated Circuit Design IC Design Flow and Project Based Learning aims to equip readers for entry level roles as IC designers in the industry and as hardware design researchers in academia The book commences with an overview of the industry IC design flow with a primary focus on register transfer level RTL design the automation of simulation and verification and system on chip SoC integration To build connections between RTL design and physical hardware FPGA field programmable gate array synthesis and implementation

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