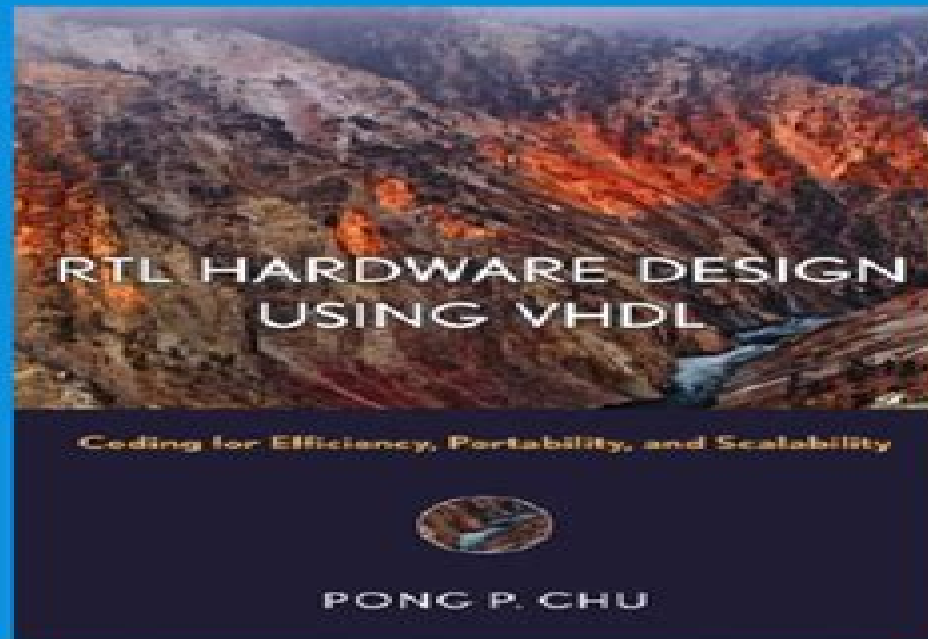


RTL Hardware Design Using VHDL Coding for Efficiency Portability and Scalability 1st Edition Chu



PONG P. CHU

Rtl Hardware Design Using Vhdl Coding For Efficiency Portability And Scalability

Karen Yorav



Rtl Hardware Design Using Vhdl Coding For Efficiency Portability And Scalability:

RTL Hardware Design Using VHDL Pong P. Chu, 2006-04-20 The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient portable and scalable Register Transfer Level RTL digital circuits using the VHDL hardware description language and synthesis software Focusing on the module level design which is composed of functional units routing circuit and storage the book illustrates the relationship between the VHDL constructs and the underlying hardware components and shows how to develop codes that faithfully reflect the module level design and can be synthesized into efficient gate level implementation Several unique features distinguish the book Coding style that shows a clear relationship between VHDL constructs and hardware components Conceptual diagrams that illustrate the realization of VHDL codes Emphasis on the code reuse Practical examples that demonstrate and reinforce design concepts procedures and techniques Two chapters on realizing sequential algorithms in hardware Two chapters on scalable and parameterized designs and coding One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis it also examines the synthesis task from the perspective of the overall development process Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation verification and testing needs and can be easily incorporated into a larger system or reused Discussion is independent of technology and can be applied to both ASIC and FPGA devices With a balanced presentation of fundamentals and practical examples this is an excellent textbook for upper level undergraduate or graduate courses in advanced digital logic Engineers who need to make effective use of today's synthesis software and FPGA devices should also refer to this book

Application-Specific Hardware Architecture Design with VHDL Bogdan Belean, 2017-10-17 This book guides readers through the design of hardware architectures using VHDL for digital communication and image processing applications that require performance computing Further it includes the description of all the VHDL related notions such as language levels of abstraction combinational vs sequential logic structural and behavioral description digital circuit design and finite state machines It also includes numerous examples to make the concepts presented in text more easily understandable

Technische Informatik Günter Kemnitz, 2011-02-04 Das Buch führt den Leser in einer didaktisch ausgefeilten Weise an die Modellierung und den rechnergestützten Entwurf digitaler Schaltungen mit der Hardware Beschreibungssprache VHDL heran Behandelt werden das logische und das Zeitverhalten Laufzeittoleranz Automaten und andere Verhaltensmodelle für sequentielle Schaltungen Synthese Logikoptimierung und Rechenwerke Es folgen eine detaillierte Einführung in VHDL und darauf aufsetzende Beschreibungen und Vorgehens Techniken für die Modellentwicklung sowie ein Exkurs auf die Transistorebene Zum Abschluss demonstrieren drei komplexe Beispielentwürfe die Anwendung des Erlernten Begriffsdefinitionen die Hervorhebung von Kernaussagen zahlreiche Abbildungen Übungsaufgaben mit Musterlösungen und buchbegleitende VHDL Beispiele im Internet unterstützen den Lernprozess

Embedded SoPC Design with Nios

II Processor and VHDL Examples Pong P. Chu, 2011-09-26 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration

FPGA Prototyping by VHDL Examples Pong P. Chu, 2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller

Top-Down Digital VLSI Design Hubert Kaeslin, 2014-12-07 Top Down VLSI Design From Architectures to Gate Level Circuits and FPGAs represents a unique approach to learning digital design Developed from more than 20 years teaching circuit design Doctor Kaeslin s approach follows the natural VLSI design flow and makes circuit design accessible for professionals with a background in systems engineering or digital signal processing It begins with hardware architecture and promotes a system level view first considering the type of intended application and letting that guide your design choices Doctor Kaeslin presents modern considerations for handling circuit complexity throughput and energy efficiency while preserving functionality The book focuses on application specific integrated circuits ASICs which along with FPGAs are increasingly used to develop products with applications in telecommunications IT security biomedical automotive and computer vision industries Topics include field programmable logic algorithms verification modeling hardware synchronous clocking and more Demonstrates a top down approach to digital VLSI design Provides a systematic overview of architecture optimization techniques Features a chapter on field programmable logic devices their technologies and architectures Includes checklists hints and warnings for various design situations Emphasizes design flows that do not overlook important action items and which include alternative options when planning the development of microelectronic

circuits **Hardware and Software: Verification and Testing** Karen Yorav, 2008-02-02 This book constitutes the thoroughly refereed post workshop proceedings of the Third International Haifa Verification Conference HVC 2007 held in Haifa Israel in October 2007 The 15 revised full papers presented together with 4 invited lectures were carefully reviewed and selected from 32 submissions The papers are organized in topical tracks on hardware verification model checking dynamic hardware verification merging formal and testing formal verification for software and software testing

Completion Detection in Asynchronous Circuits Pallavi Srivastava, 2022-11-08 This book is intended for designers with experience in traditional clocked circuit design seeking information about asynchronous circuit design in order to determine if it would be advantageous to adopt asynchronous methodologies in their next design project The author introduces a generic approach for implementing a deterministic completion detection scheme for asynchronous bundled data circuits that incorporates a data dependent computational process taking advantage of the average case delay The author validates the architecture using a barrel shifter as shifting is the basic operation required by all the processors The generic architecture proposed in this book for a deterministic completion detection scheme for bundled data circuits will facilitate researchers in considering the asynchronous design style for developing digital circuits Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology Computer

Information Systems and Industrial Management Khalid Saeed,Václav Snášel,2014-10-25 This book constitutes the proceedings of the 13th IFIP TC 8 International Conference on Computer Information Systems and Industrial Management CISIM 2014 held in Ho Chi Minh City Vietnam in November 2014 The 60 paper presented in this volume were carefully reviewed and selected from 98 submissions They are organized in topical sections named algorithms biometrics and biometrics applications data analysis and information retrieval industrial management and other applications modelling and optimization networking pattern recognition and image processing and various aspects of computer security FPGA Prototyping by SystemVerilog Examples Pong P. Chu,2018-05-30 A hands on introduction to FPGA prototyping and SoC design This is the successor edition of the popular FPGA Prototyping by Verilog Examples text It follows the same learning by doing approach to teach the fundamentals and practices of HDL synthesis and FPGA prototyping The new edition uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP intellectual property cores integrate them into an SoC system on a chip framework realize the system on an FPGA prototyping board and verify the hardware and software operation The examples start with simple gate level circuits progress gradually through the RT register transfer level modules and lead to a functional embedded system with custom I O peripherals and hardware accelerators Although it is an introductory text the examples are developed in a rigorous manner and the derivations follow the strict design guidelines and coding practices used for large complex digital systems The book is completely updated and uses the SystemVerilog language which absorbs the Verilog language It presents the hardware design in the SoC context and introduces the hardware software co design concept Instead of treating examples as isolated entities the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software programmability and develop complex and interesting embedded system projects The new edition Adds four general purpose IP cores which are multi channel PWM pulse width modulation controller I2C controller SPI controller and XADC Xilinx analog to digital converter controller Introduces a music synthesizer constructed with a DDFS direct digital frequency synthesis module and an ADSR attack decay sustain release envelope generator Expands the original video controller into a complete stream based video subsystem that incorporates a video synchronization circuit a test pattern generator an OSD on screen display controller a sprite generator and a frame buffer Provides a detailed discussion on blocking and nonblocking statements and coding styles Describes basic concepts of software hardware co design with Xilinx MicroBlaze MCS soft core processor Provides an overview of bus interconnect and interface circuit Presents basic embedded system software development Suggests additional modules and peripherals for interesting and challenging projects FPGA Prototyping by SystemVerilog Examples makes a natural companion text for introductory and advanced digital design courses and embedded system courses It also serves as an ideal self teaching guide for practicing engineers who wish to learn more about this emerging area of interest Engineering Applications of FPGAs Esteban Tlelo-Cuautle,José de Jesús Rangel-Magdaleno,Luis Gerardo

de la Fraga,2016-05-28 This book offers readers a clear guide to implementing engineering applications with FPGAs from the mathematical description to the hardware synthesis including discussion of VHDL programming and co simulation issues Coverage includes FPGA realizations such as chaos generators that are described from their mathematical models artificial neural networks ANNs to predict chaotic time series for which a discussion of different ANN topologies is included with different learning techniques and activation functions random number generators RNGs that are realized using different chaos generators and discussions of their maximum Lyapunov exponent values and entropies Finally optimized chaotic oscillators are synchronized and realized to implement a secure communication system that processes black and white and grey scale images In each application readers will find VHDL programming guidelines and computer arithmetic issues along with co simulation examples with Active HDL and Simulink The whole book provides a practical guide to implementing a variety of engineering applications from VHDL programming and co simulation issues to FPGA realizations of chaos generators ANNs for chaotic time series prediction RNGs and chaotic secure communications for image transmission

Logic Synthesis for FSM-Based Control Units Alexander Barkalov,Larysa Titarenko,2009-11-25 This book presents the hardware implementation of control algorithms represented by graph schemes of algorithm It includes new methods of logic synthesis and optimization for logic circuits of Mealy and Moore FSMs oriented on both ASIC and FPLD FPGA Prototyping by Verilog Examples Pong P. Chu,2011-09-20 FPGA Prototyping Using Verilog Examples will provide you with a hands on introduction to Verilog synthesis and FPGA programming through a learn by doing approach By following the clear easy to understand templates for code development and the numerous practical examples you can quickly develop and simulate a sophisticated digital circuit realize it on a prototyping device and verify the operation of its physical implementation This introductory text that will provide you with a solid foundation instill confidence with rigorous examples for complex systems and prepare you for future development tasks *Advancing VLSI through Machine Learning* Abhishek Narayan Tripathi,Jagana Bihari Padhy,Indrasen Singh,Shubham Tayal,Ghanshyam Singh,2025-03-31 This book explores the synergy between very large scale integration VLSI and machine learning ML and its applications across various domains It investigates how ML techniques can enhance the design and testing of VLSI circuits improve power efficiency optimize layouts and enable novel architectures This book bridges the gap between VLSI and ML showcasing the potential of this integration in creating innovative electronic systems advancing computing capabilities and paving the way for a new era of intelligent devices and technologies Additionally it covers how VLSI technologies can accelerate ML algorithms enabling more efficient and powerful data processing and inference engines It explores both hardware and software aspects covering topics like hardware accelerators custom hardware for specific ML tasks and ML driven optimization techniques for chip design and testing This book will be helpful for academicians researchers postgraduate students and those working in ML driven VLSI **Embedded Systems** Kiyofumi Tanaka,2012-03-02 Nowadays embedded systems the computer systems that

are embedded in various kinds of devices and play an important role of specific control functions have permitted various aspects of industry Therefore we can hardly discuss our life and society from now onwards without referring to embedded systems For wide ranging embedded systems to continue their growth a number of high quality fundamental and applied researches are indispensable This book contains 19 excellent chapters and addresses a wide spectrum of research topics on embedded systems including basic researches theoretical studies and practical work Embedded systems can be made only after fusing miscellaneous technologies together Various technologies condensed in this book will be helpful to researchers and engineers around the world **A Route to Chaos Using FPGAs** Bharathwaj Muthuswamy,Santo Banerjee,2015-06-18

The purpose of this introductory book is to couple the teaching of chaotic circuit and systems theory with the use of field programmable gate arrays FPGAs As such it differs from other texts on chaos first it puts emphasis on combining theoretical methods simulation tools and physical realization to help the reader gain an intuitive understanding of the properties of chaotic systems Second the medium used for physical realization is the FPGA These devices are massively parallel architectures that can be configured to realize a variety of logic functions Hence FPGAs can be configured to emulate systems of differential equations Nevertheless maximizing the capabilities of an FPGA requires the user to understand the underlying hardware and also FPGA design software This is achieved by the third distinctive feature of this book a lab component in each chapter Here readers are asked to experiment with computer simulations and FPGA designs to further their understanding of concepts covered in the book This text is intended for graduate students in science and engineering interested in exploring implementation of nonlinear dynamical chaotic systems on FPGAs Cognitive Robotics & Control Cecilio Angulo,2020-06-16 Robotics and control are both research and application domains that have been frequently engineered through the use of interdisciplinary approaches like cybernetics Cognition is a particular concept of this approach abstracted from the context of living organisms to that of artificial devices and is concerned with knowledge acquisition and understanding through thought experience and the senses Cognitive robotics and control refer to knowledge processing as much as knowledge generation from problem understanding leading to special forms of architectures that enable systems to behave in an autonomous way The main aim of this book is to highlight emerging applications and address recent breakthroughs in the domain of cognitive robotics and control and related areas Procedures algorithms architectures and implementations for reasoning problem solving or decision making are considered in the domain of robotics and control

Handbook of Signal Processing Systems Shuvra S. Bhattacharyya,Ed F. Deprettere,Rainer Leupers,Jarmo Takala,2013-06-20 Handbook of Signal Processing Systems is organized in three parts The first part motivates representative applications that drive and apply state of the art methods for design and implementation of signal processing systems the second part discusses architectures for implementing these applications the third part focuses on compilers and simulation tools describes models of computation and their associated design tools and methodologies This handbook is an essential tool

for professionals in many fields and researchers of all levels New Technologies in the Oil and Gas Industry Jorge Salgado Gomes, 2012-10-31 Oil and gas are the most important non renewable sources of energy Exploring producing and managing these resources in compliance with HSE standards are challenging tasks New technologies workflows and procedures have to be implemented This book deals with some of these themes and describes some of the advanced technologies related to the oil and gas industry from HSE to field management issues Some new technologies for geo modeling transient well testing and digital rock physics are also introduced There are many more technical topics to be addressed in future books This book is aimed at researchers petroleum engineers geoscientists and people working within the petroleum industry

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