

Chapter 3 SOLUTIONS

4) a) $I_{D1} = \frac{V_{DD} - 2V_{DS1}}{R_{D1} + R_{D2}}$

$= \frac{2.5 - 1.5}{2.5 \text{ k}\Omega} = \frac{1 \text{ V}}{2.5 \text{ k}\Omega}$

$I_{D1} = 276 \mu\text{A}$

b) $I_{D1} = 10^{-8} \text{ A}$, $V_{DS1} = 100 \text{ V}$, $V_{GS1} = 0 \text{ V}$

$I_{D1} = I_{D2} (e^{V_{GS1}/V_T} - 1)$

where $V_{GS1} = \frac{1}{2} V_{DS1} = 26 \text{ V}$ @ $200 \text{ k}\Omega$

⑤ $I_{D1} = \frac{V_{DD} - 2V_{DS1}}{R_{D1} + R_{D2}} = I_{D2} (e^{V_{GS1}/V_T} - 1)$

$= \frac{2.5 - 1.5}{2.5 \text{ k}\Omega} = 10^{-8} (e^{V_{GS1}/V_T} - 1)$

Iterating on this expression we can obtain:

$V_{GS1} = 0.528 \text{ V}$
 $I_{D1} = 5.1 \mu\text{A}$

c) 0.001

c) $I_{D1} = 10^{-8} \text{ A}$, $V_{DS1} = 100 \text{ V}$,
egn. use ⑤ from b and iterate to find

$V_{GS1} = 0.743 \text{ V}$
 $I_{D1} = 263 \mu\text{A}$

$I_{D1} = 10^{-8} \text{ A}$, $V_{DS1} = 50 \text{ V}$;

$V_{GS1} = 0.720 \text{ V}$
 $I_{D1} = 261 \mu\text{A}$

Problem 3.10
a) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

b) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

c) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

d) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

e) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

Problem 3.11
a) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

b) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

c) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

d) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

e) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

f) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

g) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

h) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

i) $V_{GS1} = 0 \text{ V}$
 $V_{DS1} = 100 \text{ V}$
 $I_{D1} = 10^{-8} \text{ A}$
 $V_{GS2} = 0 \text{ V}$
 $V_{DS2} = 100 \text{ V}$
 $I_{D2} = 10^{-8} \text{ A}$

Digital Integrated Circuits Solution Manual Rabaey

**Namit Gupta, Anjali Gupta, Naresh
Purohit, Shiraz Husain**



Digital Integrated Circuits Solution Manual Rabaey:

Solution Manual to Accompany CMOS Digital Integrated Circuits : Analysis and Design, Second Edition

Sung-Mo Kang, Yusuf Leblebici, 1999 **Solutions Manual for Digital Integrated Circuits** Ayers John E, 2003-09

Embedded SoPC Design with Nios II Processor and VHDL Examples Pong P. Chu, 2011-08-29 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio

synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology Transforming Tomorrow: Innovative Solutions and Global Trends in Electrical and Electronics Engineering Namit Gupta, Anjali Gupta, Naresh Purohit, Shiraz Husain, 2025-06-03 The International Conference on Transforming Tomorrow Innovative Solutions and Global Trends in Electrical and Electronics Engineering Pragya 2025 is scheduled to be held on May 5-6, 2025 at Shri Vaishnav Vidyapeeth Vishwavidyalaya Indore Madhya Pradesh India This prestigious event aims to provide a dynamic platform for researchers, academicians, industry professionals and students to exchange knowledge, showcase cutting edge innovations and discuss global trends shaping the future of Electrical and Electronics Engineering Pragya 2025 will feature sessions and presentations on key emerging areas including Robotics, Renewable Energy, Smart Grids, Mechatronics, 5G Communications, Artificial Intelligence and the Internet of Things (IoT). The conference is designed to foster meaningful dialogue, cross disciplinary collaboration and engagement with leading experts from academia and industry. In line with its theme of Transforming Tomorrow, the conference emphasizes clarity, innovation and sustainable development. It will serve as a catalyst for forward looking discussions and solutions that address modern engineering challenges and contribute to building a smarter, greener and more connected world. With a commitment to being Concise, Clear and Cohesive, Pragya 2025 is set to become a significant academic and professional milestone in advancing technological progress and inspiring future innovation across the Electrical and Electronics Engineering spectrum **Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation** Vassilis Paliouras, Johan Vounckx, Diederik Verkest, 2005-08-25 Welcome to the proceedings of PATMOS 2005, the 15th in a series of international workshops. PATMOS 2005 was organized by IMEC with technical co-sponsorship from the IEEE Circuits and Systems Society. Over the years, PATMOS has evolved into an important European event where researchers from both industry and academia discuss and investigate the emerging challenges in future and contemporary applications, design methodologies and tools required for the development of upcoming generations of integrated circuits and systems. The technical program of PATMOS 2005 contained state of the art technical contributions, three invited talks, a special session on hearing aid design and an embedded tutorial. The technical program focused on timing, performance and power consumption as well as architectural aspects with particular emphasis on modeling, design characterization, analysis and optimization in the nanometer era. The Technical Program Committee, with the assistance of additional expert reviewers, selected the 74 papers to be presented at PATMOS. The papers were divided into 11 technical sessions and 3 poster sessions. As is always the case with the PATMOS workshops, the review process was anonymous; full papers were required and several reviews were carried out per paper. Beyond the presentations of the papers, the PATMOS technical program was enriched by a series of speeches offered by world class experts on important

emerging research issues of industrial relevance Prof Jan Rabaey Berkeley USA gave a talk on Traveling the Wild Frontier of Ultra Low Power Design Dr Sung Bae Park S sung gave a presentation on DVL Deep Low Voltage Circuits and Devices Prof

FPGA Prototyping by VHDL Examples Pong P. Chu, 2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller

Building Embedded Systems Changyi Gu, 2016-05-26 Develop the software and hardware you never think about We re talking about the nitty gritty behind the buttons on your microwave inside your thermostat inside the keyboard used to type this description and even running the monitor on which you are reading it now Such stuff is termed embedded systems and this book shows how to design and develop embedded systems at a professional level Because yes many people quietly make a successful career doing just that Building embedded systems can be both fun and intimidating Putting together an embedded system requires skill sets from multiple engineering disciplines from software and hardware in particular Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project Programmers who know software will learn what they need to know about hardware Engineers with hardware knowledge likewise will learn about the software side Whatever your background is Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems He brings knowledge of numerous approaches to embedded systems design including the System on Programmable Chips SOPC approach that is currently growing to dominate the field His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field or even just to do some embedded programming as a side project What You Will Learn Program embedded systems at the hardware level Learn current industry practices in firmware development Develop practical knowledge of embedded hardware options Create tight integration between software and hardware Practice a work flow leading to successful outcomes Build from transistor level to the system level Make sound choices between performance and cost Who This Book Is For Embedded system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware Those who favor the System on a Programmable Chip SOPC approach will in particular benefit from this book Students in both Electrical Engineering and Computer Science can also benefit from this book and the real life industry practice it provides *Solutions Manual to Accompany Analysis and Design of Digital*

Integrated Circuits David A. Hodges, Yu Chen, Horace G. Jackson, 1983 Low Power Design Essentials Jan

Rabaey, 2009-04-21 Low Power Design Essentials contains all the topics of importance to the low power designer The book

lays the foundation with background chapters entitled Advanced MOS Transistors and Their Models and Power Basics These chapters are followed by chapters on the design process including optimization architecture and algorithm level memory run time standby logic and standby memory Chapters on special topics are also included power management and modal design ultra low power and low power design methodology and flows The book concludes with a chapter on case studies as well as a chapter on Projection into the Future These chapters are all based on the extensive amount of teaching that the author has carried out both at universities and companies worldwide All chapters have been drawn up specifically for self study They aim however at different levels of understanding All the chapters start with elementary material but most also contain advanced material

Innovative Computing 2025, Volume 4 Hao-Shang Ma,Hwa-Young Jeong,Yu-Wei Chan,Hsuan-Che Yang,2025-07-12 This book comprises select proceedings of the 7th International Conference on Innovative Computing which was held in Bangkok Thailand Jan 19 23 2025 IC 2025 focusing on cutting edge research carried out in the areas of information technology science and engineering Some of the themes covered in this book are cloud communications and networking high performance computing architecture for secure and interactive IoT satellite communication wearable network and system infrastructure management etc The essays are written by leading international experts making it a valuable resource for researchers and practicing engineers alike

Integrated Circuit and System Design ,2005 *The British National Bibliography* Arthur James Wells,1996

Wireless Sensor and Actor Networks II Ali Miri,2008-05-25 This book constitutes the refereed proceedings of the IFIP Conference on Wireless Sensors and Actor Networks held in Ottawa Canada July 2008 This series publishes state of the art results in the sciences and technologies of information and communication The scope of the series includes foundations of computer science software theory and practice education computer applications in technology communication systems systems modeling and optimization information systems computers and society computer systems technology security and protection in information processing systems artificial intelligence and human computer interaction Proceedings and post proceedings of refereed international conferences in computer science and interdisciplinary fields are featured These results often precede journal publication and represent the most current research The principal aim of the IFIP series is to encourage education and the dissemination and exchange of information about all aspects of computing

CMOS Sigma-Delta Converters Jose M. de la Rosa,Rocio del Rio,2013-03-13 A comprehensive overview of Sigma Delta Analog to Digital Converters ADCs and a practical guide to their design in nano scale CMOS for optimal performance This book presents a systematic and comprehensive compilation of sigma delta converter operating principles the new advances in architectures and circuits design methodologies and practical considerations going from system level specifications to silicon integration packaging and measurements with emphasis on nanometer CMOS implementation The book emphasizes practical design issues from high level behavioural modelling in MATLAB SIMULINK to circuit level implementation in Cadence Design FrameWork II As well as being a comprehensive

reference to the theory the book is also unique in that it gives special importance on practical issues giving a detailed description of the different steps that constitute the whole design flow of sigma delta ADCs The book begins with an introductory survey of sigma delta modulators their fundamentals architectures and synthesis methods covered in Chapter 1 In Chapter 2 the effect of main circuit error mechanisms is analysed providing the necessary understanding of the main practical issues affecting the performance of sigma delta modulators The knowledge derived from the first two chapters is presented in the book as an essential part of the systematic top down bottom up synthesis methodology of sigma delta modulators described in Chapter 3 where a time domain behavioural simulator named SIMSIDES is described and applied to the high level design and verification of sigma delta ADCs Chapter 4 moves farther down from system level to the circuit and physical level providing a number of design recommendations and practical recipes to complete the design flow of sigma delta modulators To conclude the book Chapter 5 gives an overview of the state of the art sigma delta ADCs which are exhaustively analysed in order to extract practical design guidelines and to identify the incoming trends design challenges as well as practical solutions proposed by cutting edge designs Offers a complete survey of sigma delta modulator architectures from fundamentals to state of the art topologies considering both switched capacitor and continuous time circuit implementations Gives a systematic analysis and practical design guide of sigma delta modulators from a top down bottom up perspective including mathematical models and analytical procedures behavioural modeling in MATLAB SIMULINK macromodeling and circuit level implementation in Cadence Design FrameWork II chip prototyping and experimental characterization Systematic compilation of cutting edge sigma delta modulators Complete description of SIMSIDES a time domain behavioural simulator implemented in MATLAB SIMULINK Plenty of examples case studies and simulation test benches covering the different stages of the design flow of sigma delta modulators A number of electronic resources including SIMSIDES the statistical data used in the state of the art survey as well as many design examples and test benches are hosted on a companion website Essential reading for Researchers and electronics engineering practitioners interested in the design of high performance data converters integrated in nanometer CMOS technologies mixed signal designers

Solutions Manual Digital Integrated Circuits CRC Press,2009-07-10 Any textbook more than five years old simply won't do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization Each chapter includes numerous worked examples case studies and SPICE computer simulations The book's website offers supplementary material and more worked problems Qualifying instructors will have access to a new instructor's manual

15th Symposium on Integrated Circuits and Systems Design Ricardo Augusto da Luz Reis,Ney Calazans,2002

Low Power Circuits for Emerging Applications in Communications, Computing, and Sensing Fei

Yuan,2018-12-07 The book addresses the need to investigate new approaches to lower energy requirement in multiple application areas and serves as a guide into emerging circuit technologies It explores revolutionary device concepts sensors and associated circuits and architectures that will greatly extend the practical engineering limits of energy efficient computation The book responds to the need to develop disruptive new system architectures circuit microarchitectures and attendant device and interconnect technology aimed at achieving the highest level of computational energy efficiency for general purpose computing systems Features Discusses unique technologies and material only available in specialized journal and conferences Covers emerging applications areas such as ultra low power communications emerging bio electronics and operation in extreme environments Explores broad circuit operation ex analog RF memory and digital circuits Contains practical applications in the engineering field as well as graduate studies Written by international experts from both academia and industry Digital Integrated Circuits DeMassa,1996-02-01 **Forthcoming Books** Rose Arny,2002

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