DESIGNING PHOTODIODE AMPLIFIER CIRCUITS WITH OPA128

The OPA128 ultra-low bias current operational amplifier achieves its 75fA maximum bias current without compromise. Using standard design techniques, serious performance trade-offs were required which sacrificed overall amplifier performance in order to reach femtoamp (fA = 10⁻¹¹ A) bias currents.

UNIQUE DESIGN MINIMIZES PERFORMANCE TRADE-OFFS

Small-geometry FETs have low bias current, of course, but FET size reduction reduces transconductance and increases noise dramatically, placing a serious restriction on performance when low bias current is achieved simply by making input FETs extremely small. Unfortunately, larger geometries suffer from high gate-to-substrate isolation diode leakage (which is the major contribution to BIFET* amplifier input bias current).

Replacing the reverse-biased gate-to-substrate isolation diode structure of BEFETs with dielectric isolation removes this large leakage current component which, together with a noise-free cascode circuit, special FET geometry, and advanced wafer processing, allows far higher **Diffet** * performance compared to BIFETs.

HOW TO IMPROVE PHOTODIODE AMPLIFIER PERFORMANCE

An important electro-optical application of FET op amps is for photodiode amplifiers. The unequaled performance of the OPA128 is well-suited for very high sensitivity detector designs. A few design tips for photodiode amplifiers may be helpful:

- Photodiode capacitance should be as low as possible. See Figure 1: C, affects not only bandwidth but noise as well. This is because C, and the op amp's feedback resistor form a noise-gain zero (feedback pole).
- Photodiode active area should be as small as possible so that C, is small and R, is high. This will allow a higher signal-to-noise ratio. If a large area is needed, consider using optical "gain" (lens, mirror, etc.) rather than a large area diode. Optical "gain" is essentially noise-free.
- Use as large a feedback resistor as possible (consistent with bandwidth requirements) to minimize noise. This seems paradoxical, but remember, resistor thermal noise increases as:

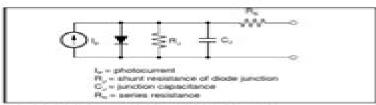


FIGURE 1. Photodiode Equivalent Circuit.

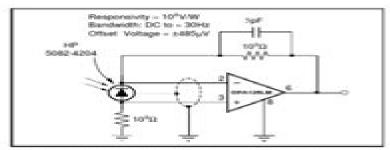


FIGURE 2. High-Sensitivity Photodiode Amplifier.

$$e_{corr} = \sqrt{4k \text{ TBR}}$$

k: Boltzman's constant = 1.38 x 10 = J/K

T: temperature (*K)

B: noise bandwidth (Hz)

R: feedback resistor (Ω)

e noise voltage (Vrms)

while transimpedance gain (signal) increases as:

Signal-to-noise improves by \(\overline{R} \).

 A low bias current op amp is needed to achieve highest sensitivity. Bias current causes voltage offset errors with large-feedback resistors. Wide bandwidth circuits with smaller feedback resistors are less subject to bias current errors, but even in these circuits, bias current must be

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Yi-Tong Ma

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