

12 GHz Super Low Noise FET in Hollow Plastic PKG

DESCRIPTION

- Super Low Noise and High Gain
- Hollow (Air Cavity) Plastic package

FEATURES

- Super Low noise figure and high associated gain:
NF = 0.30 dB TYP., Ga = 13.7 dB TYP.
@V_{DS} = 2 V, I_D = 10 mA, f = 12 GHz

PACKAGE

- Micro-X plastic package



APPLICATIONS

- DBS LNB gain-stage, Mix-stage
- Low noise amplifier for microwave communication systems

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Description
CE3512K2	CE3512K2-C1	Micro-X plastic package	C5	<ul style="list-style-type: none"> - Embossed tape 8 mm wide - Pin 4 (Gate) faces the perforation side of the tape - MOQ 10 kpcs/reel

Rf Low Noise Fet Ce3512k2

Lingsheng Yao

A red circular graphic with a gradient, appearing as a stylized arrow or a partial circle, located to the right of the author's name.

Rf Low Noise Fet Ce3512k2:

Fundamentals of RF Circuit Design Jeremy Everard, 2001-02-08 Transistor and component models at low and high frequencies Two port network parameters Small signal amplifier design and measurement Low noise oscillators Mixers Power amplifiers Real time large signal modelling **RF Design of a Low Noise Amplifier** Rexy J. Geevarghese, 2001

Design of Rf Cmos Low Noise Amplifiers Changgui Lin, Thottam S. Kalkur, Marc Morin, 2010-04 Design of RF CMOS Low Noise Amplifiers presents the results of research on a gain boosted common gate RF low noise amplifier LNA in CMOS technology The book covers noise analysis design techniques prototyping of the LNA as well as broadband modeling for on chip spiral differential inductors Beginning with a technical review of LNA architectures and inductor modeling techniques the authors then introduce a scalable lumped circuit model for octagonal differential inductors The effect of high frequency current crowding i e proximity effect is taken into consideration in the lumped circuit model Subsequently the book offers comprehensive noise analysis of the LNA and discusses design techniques for noise reduction Measurement results for a 2 4GHz CMOS LNA and conclusions are included The book is intended for anyone who is interested in learning essentials of RF CMOS LNA design and basic mathematics of on chip inductor broadband modeling The book is also beneficial to engineers and researchers in CMOS RFIC design especially for WLAN Bluetooth and emerging wireless communication applications

Design of a Low-voltage RF CMOS Low Noise Amplifier Sharifah Fatmadiana Wan Muhamad Hatta, 2009 *Design of an RF Wideband Low-noise Amplifier Using 0.35uM CMOS Technology* Yan Tang, 2002 **RF Low-noise Amplifier,**

Oscillator and Resistive Mixer Design Rakesh Choudari Chaganti, 2010 *Application in Rf Receivers of Cmos Low Noise Amplifiers and Front-end Circuits Design and Implementation*, 2009 *RF Circuits and Applications for Practicing Engineers* Mouqun Dong, 2020-10-31 This comprehensive resource explains the theory of RF circuits and systems and the practice of designing them The fundamentals for linear and low noise amplifier designs including the S and noise parameters and their applications in amplifier designs and matching network designs using the Smith chart are covered Theories of RF power amplifiers and high efficiency power amplifiers are also explained The underpinnings of wireless communications systems as well as passive components commonly used in RF circuits and measurements are discussed RF measurement techniques and RF switches are also presented The book explores stability criteria and the invariant property of lossless networks and includes detailed theoretical treatments The basic concepts and techniques covered in this book are routinely used in today s engineering practice especially from the perspective of printed circuit board PCB based RF circuit design and system integration Intended for practicing engineers and circuit designers this book focuses on practical topics in circuit design and measurement techniques It bridges the gap between academic materials and real circuit designs using real circuit examples and practical tips Readers develop a numerical feel for RF problems as well as awareness of the concepts of design for cost and design for manufacturing which is a critical skill set for today s engineers working in an environment of

commercial product development **Design of CMOS RF Low-noise Amplifiers and Mixer for Wireless Applications** ,
 Xiii 161 leaves ill 30 cm Low-noise RF Design Gerald R. Benitz,1984 **Radio Frequency Integrated Circuits and Systems** Hooman Darabi,2015-04-16 Focusing on the core topics of radio frequency integrated circuits RFICs and system design this textbook provides the in depth coverage and detailed mathematical analyses needed to gain a thorough understanding of the subject Throughout theory is linked to practice with real world application examples practical design guidance is also offered covering the pros and cons of various topologies and preparing students for future work in industry Written for graduate courses on RFICs this uniquely intuitive and practical book will also be of value to practising RFIC and system designers Key topics covered include RF components signals and systems two ports noise distortion low noise amplifiers mixers oscillators power amplifiers and transceiver architectures Lecture slides and a solutions manual for instructors are provided online to complete the course package **A Low-noise Design for the R-F and I-F Components of a 3200 Mcs Receiver** Ralph Bolgiano,1949 Low Noise RF CMOS Receiver Integrated Circuits Sang Hyun Woo,2012
 The objective of this research is to design and implement low noise wideband RFIC components with CMOS technology for the direct conversion architecture This research proposes noise reduction techniques to improve the thermal noise and flicker noise contribution of a low noise amplifier LNA and a mixer Of these techniques the LNA is found to reduce noise boost gain and consume a relatively low amount of power without sacrificing the wideband and linearity advantages of a conventional common gate CG topology The research concludes by investigating the proposed mixer topology which senses and compensates local oscillator LO phase mismatches the dominant cause of flicker noise *Implementation and Improvement for RF Low Noise Amplifiers in Conventional CMOS Technologies* Yo-Chuol Ho,2000 *Low Noise Design Handbook* Gary A. Breed,1994

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Table of Contents Rf Low Noise Fet Ce3512k2

1. Understanding the eBook Rf Low Noise Fet Ce3512k2
 - The Rise of Digital Reading Rf Low Noise Fet Ce3512k2
 - Advantages of eBooks Over Traditional Books
2. Identifying Rf Low Noise Fet Ce3512k2
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Rf Low Noise Fet Ce3512k2
 - User-Friendly Interface
4. Exploring eBook Recommendations from Rf Low Noise Fet Ce3512k2
 - Personalized Recommendations
 - Rf Low Noise Fet Ce3512k2 User Reviews and Ratings
 - Rf Low Noise Fet Ce3512k2 and Bestseller Lists
5. Accessing Rf Low Noise Fet Ce3512k2 Free and Paid eBooks

- Rf Low Noise Fet Ce3512k2 Public Domain eBooks
 - Rf Low Noise Fet Ce3512k2 eBook Subscription Services
 - Rf Low Noise Fet Ce3512k2 Budget-Friendly Options
6. Navigating Rf Low Noise Fet Ce3512k2 eBook Formats
 - ePub, PDF, MOBI, and More
 - Rf Low Noise Fet Ce3512k2 Compatibility with Devices
 - Rf Low Noise Fet Ce3512k2 Enhanced eBook Features
 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Rf Low Noise Fet Ce3512k2
 - Highlighting and Note-Taking Rf Low Noise Fet Ce3512k2
 - Interactive Elements Rf Low Noise Fet Ce3512k2
 8. Staying Engaged with Rf Low Noise Fet Ce3512k2
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Rf Low Noise Fet Ce3512k2
 9. Balancing eBooks and Physical Books Rf Low Noise Fet Ce3512k2
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Rf Low Noise Fet Ce3512k2
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Rf Low Noise Fet Ce3512k2
 - Setting Reading Goals Rf Low Noise Fet Ce3512k2
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Rf Low Noise Fet Ce3512k2
 - Fact-Checking eBook Content of Rf Low Noise Fet Ce3512k2
 - Distinguishing Credible Sources
 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development

- Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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