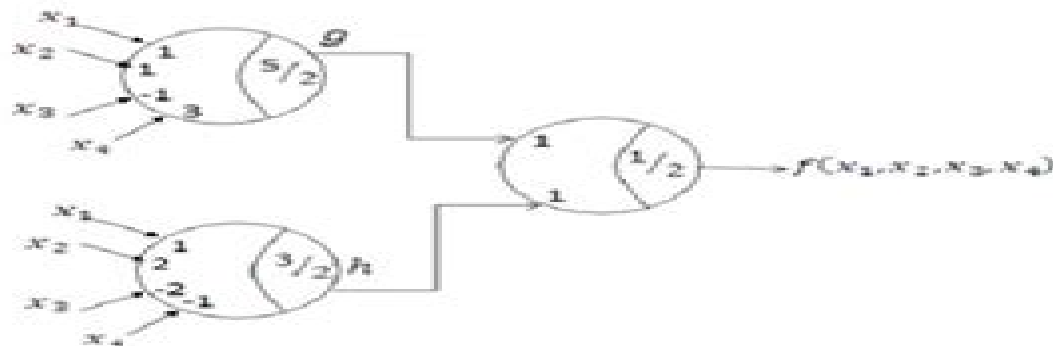


DIGITAL SWITCHING AND LOGIC DESIGN

QUESTION BANK

CHAPTER1: Threshold Logic

- Q1. Is it possible to design EX-OR gate with a single threshold element? Justify your answer.
- Q2. Let $f(x_1, x_2, x_3) = \sum(0,1,3)$. Realize it using a threshold element.
- Q3. What are the various capabilities of threshold logic?
- Q5. Determine whether the function $f(x_1, x_2, x_3, x_4) = \sum(0,1,3,4,5,6,7,12,13)$ is a threshold function, and, if it is, find a weight-threshold vector.
- Q6. By examining the linear inequalities, determine which of the following functions is a threshold function, and for each one that is, find the corresponding weight-threshold vector.
- a) $f_1(x_1, x_2, x_3) = \sum(1,2,3,7)$
 - b) $f_1(x_1, x_2, x_3) = \sum(0,2,4,5,6)$
 - c) $f_1(x_1, x_2, x_3) = \sum(0,3,5,6)$
- Q7. Determine the function $f(x_1, x_2, x_3, x_4)$ realized by the network as shown in fig below.



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Kandel, Gideon Langholz, Joe L Mott, 1998-08-11 This text is intended for a first course in digital logic design at the sophomore or junior level for electrical engineering computer engineering and computer science programs as well as for a number of other disciplines such as physics and mathematics The book can also be used for self study or for review by practicing engineers and computer scientists not intimately familiar with the subject After completing this text the student should be prepared for a second advanced course in digital design switching and automata theory microprocessors or computer organization

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