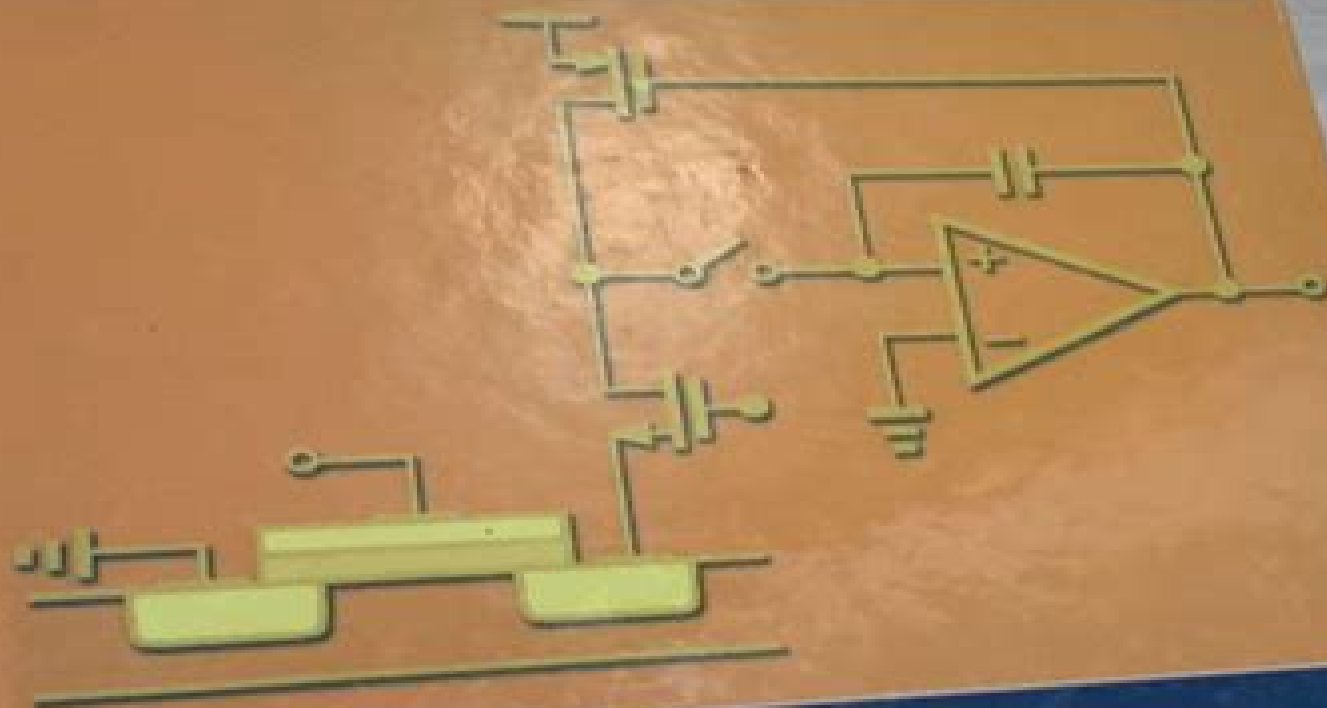


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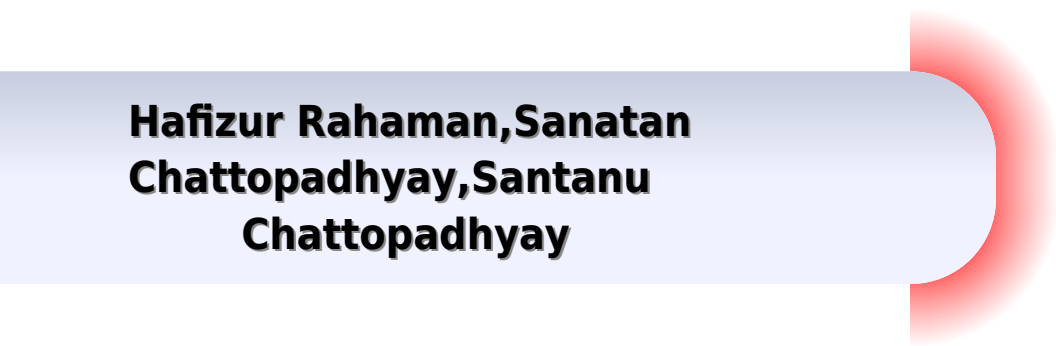
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developed The realized passive baluns rat race and transformer show excellent commonmode rejection ratios exceeding 30 dB and low losses of 2.5 dB On the active side a low gain high linearity single stage common emitter LNA was realized The selected topology allowed finer trade off between linearity and sensitivity of the front end compared to multi stage LNA solutions For the mixer a low voltage supply high linearity low noise double balanced concept was employed It uses AC coupling between the two stages which allowed an independent optimization of transconductance core sizing and bias The transconductance was designed for best noise performance while the core was chosen for maximum linearity A high fidelity two channel receiver was realized using these circuit components which achieved a performance comparable to the published state of the art results in SiGe Single sideband noise figure better than 16.5 dB 1 dB compression point exceeding 12 dBm while consuming moderate 82 mA DC current from a 1.6 V supply for both channels

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Trade-Offs in Analog Circuit Design Chris Toumazou, George S. Moschytz, Barrie Gilbert, 2002-09-30 As the frequency of communication systems increases and the dimensions of transistors are reduced more and more stringent performance requirements are placed on analog circuits This is a trend that is bound to continue for the foreseeable future and while it does understanding performance trade offs will constitute a vital part of the analog design process It is the insight and intuition obtained from a fundamental understanding of performance conflicts and trade offs that ultimately provides the designer with the basic tools necessary for effective and creative analog design Trade offs in Analog Circuit Design which is devoted to the understanding of trade offs in analog design is quite unique in that it draws together fundamental material from and identifies interrelationships within a number of key analog circuits The book covers ten subject areas Design methodology Technology General Performance Filters Switched Circuits Oscillators Data Converters Transceivers Neural

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Progress in VLSI Design and Test Hafizur Rahaman, Sanatan Chattopadhyay, Santanu Chattopadhyay, 2012-06-26 This book constitutes the refereed proceedings of the 16th International Symposium on VLSI Design and Test VDAT 2012 held in Shibpur India in July 2012 The 30 revised regular papers presented together with 10 short papers and 13 poster sessions were carefully selected from 135 submissions The papers are organized in topical sections on VLSI design design and modeling of digital circuits and systems testing and verification design for testability testing memories and regular logic arrays embedded systems hardware software co design and verification emerging technology nanoscale computing and nanotechnology

Proceedings, 2004

Optical Fibers Telecommunications Gary Osborne, 2018-04-13 This book is structured into 12 chapters to facilitate a logical progression of material and to enable straightforward access to topics by providing the appropriate background and theoretical support Chapter 1 gives a short introduction to optical fiber communications by considering the historical development the general system and the major advantages provided by this technology Chapter 2 discuss about the quality of service and telecommunication impairments In Chapter 3 the concept of the optical fiber as a transmission medium is introduced using the simple ray theory approach This is followed by discussion of electromagnetic wave theory applied to optical fibers prior to consideration of lightwave transmission within the various fiber types In particular single mode fiber together with a more recent class of microstructured optical fiber referred to as photonic crystal fiber are covered in further detail The major transmission characteristics of optical fibers are then dealt with in Chapter 4 Again there is a specific focus on the properties and characteristics of single mode fibers including in this third edition enhanced discussion of single mode fiber types polarization mode dispersion nonlinear effects and in particular soliton propagation Chapters 5 and 6 deal with the various transmission and switching techniques Also discuss the different transmission aspects of Voice Telephony Chapter 7 describe the light sources employed in optical fiber communications The other important semiconductor optical source namely the light emitting diode is dealt with in Chapter 7 Chapter 8 discuss about the various design features of Optical Fibers for communication systems Chapter 9 provides a general treatment of the major measurements which may be undertaken on optical fibers in both the laboratory and the field The chapter is incorporated at this stage in the book to enable the reader to

obtain a more complete understanding of optical fiber subsystems and systems prior to consideration of these issues Chapter 10 on optical networks comprises an almost entirely new chapter for the third edition which provides both a detailed overview of this expanding field and a discussion of all the major aspects and technological solutions currently being explored Chapter 11 discusses about the data communications methods Chapter 12 dealt with the telecommunication lasers techniques

High-Frequency Integrated Circuits Sorin Voinigescu, 2013-02-28 A transistor level design intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz this comprehensive text covers high speed RF mm wave and optical fibre circuits using nanoscale CMOS SiGe BiCMOS and III V technologies Step by step design methodologies end of chapter problems and practical simulation and design projects are provided making this an ideal resource for senior undergraduate and graduate courses in circuit design With an emphasis on device circuit topology interaction and optimization it gives circuit designers and students alike an in depth understanding of device structures and process limitations affecting circuit performance

American Book Publishing Record , 2001

Design Of Cmos Rf Integrated Circuits And Systems Kiat Seng Yeo, Manh Anh Do, Chirn Chye Boon, 2010-03-24 This book provides the most comprehensive and in depth coverage of the latest circuit design developments in RF CMOS technology It is a practical and cutting edge guide packed with proven circuit techniques and innovative design methodologies for solving challenging problems associated with RF integrated circuits and systems This invaluable resource features a collection of the finest design practices that may soon drive the system on chip revolution Using this book s state of the art design techniques one can apply existing technologies in novel ways and to create new circuit designs for the future

VLSI Circuits and Systems , 2005

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