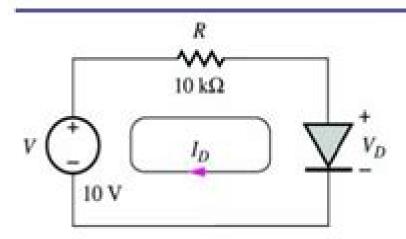
Diode Circuit Analysis: Basics



V and R may represent the Thévenin equivalent of a more complex 2terminal network. The objective of diode circuit analysis is to find the quiescent operating point for the diode.

$$Q$$
-Point = (I_D, V_D)

The loop equation for the diode circuit is:

$$V \bullet I_D R \square V_D$$

This is also called the **load line** for the diode. The solution to this equation can be found by:

- Graphical analysis using the load-line method.
- Analysis with the diode's mathematical model.
- Simplified analysis with the ideal diode model.
- Simplified analysis using the constant voltage drop (CVD) model.

Diode Circuit Analysis

AW Chickering

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