

CMOS Digital Integrated Circuits Analysis and Design 4th Edition Kang

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Exercise Problems

5.1 Design a resistive-load inverter with $R = 2 \text{ k}\Omega$, such that $V_{OL} = 0.05 \text{ V}$. The enhancement-type nMOS driver transistor has the following parameters:

$$\begin{aligned} V_{DD} &= 1.1 \text{ V} \\ V_{TO} &= 0.52 \text{ V} \\ \gamma &= 0 \text{ V}^{1/2} \\ \lambda &= 0 \\ \mu_n C_{ox} &= 216 \text{ } \mu\text{A/V}^2 \end{aligned}$$

- (a) Determine the required aspect ratio, W/L .
- (b) Determine V_{IL} and V_{OH} .
- (c) Determine noise margins NM_L and NM_H .

SOLUTION :

- (a) When $V_{out} = V_{OL}$, $V_{in} = V_{DD} = 1.2 \text{ V}$, the driver transistor operates in linear region. Using Eq.(5.12)

$$I_s = \frac{V_{DD} - V_{OL}}{2K} = \frac{k_n}{2} \frac{W}{L} \left(\frac{1}{1 + \frac{V_{OL}}{E_C L_n}} \right) [2 \cdot (V_{in} - V_{TO}) \cdot V_{OL} - V_{OL}^2]$$

Since V_{OL} is small, $V_{OL}/E_C L_n$ can be ignored. Using (5.17)

$$\frac{1.1 - 0.05}{2K} = \frac{216 \times 10^{-6}}{2} \frac{W}{L} [2 \cdot (1.1 - 0.52) \cdot 0.05 - 0.05^2]$$

Solve for W/L ,

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Solution Manual to Accompany CMOS Digital Integrated Circuits : Analysis and Design, Second Edition Sung-Mo Kang, Yusuf Leblebici, 1999 **MOSFET Technologies for Double-Pole Four-Throw Radio-Frequency Switch** Viranjay M. Srivastava, Ghanshyam Singh, 2013-10-07 This book provides analysis and discusses the design of various MOSFET technologies which are used for the design of Double Pole Four Throw DP4T RF switches for next generation communication systems The authors discuss the design of the DP4T RF switch by using the Double Gate DG MOSFET as well as the Cylindrical Surrounding double gate CSDG MOSFET The effect of HfO₂ high dielectric material in the design of DG MOSFET and CSDG MOSFET is also explored Coverage includes comparison of Single gate MOSFET and Double gate MOSFET switching parameters as well as testing of MOSFETs parameters using image acquisition **Low Power Designs in Nanodevices and Circuits for Emerging Applications** Shilpi Birla, Shashi Kant Dargar, Neha Singh, P. Sivakumar, 2023-11-14 This reference textbook discusses low power designs for emerging applications This book focuses on the research challenges associated with theory design and applications towards emerging Microelectronics and VLSI device design and developments about low power consumptions The advancements in large scale integration technologies are principally responsible for the growth of the electronics industry This book is focused on senior undergraduates graduate students and professionals in the field of electrical and electronics engineering nanotechnology This book Discusses various low power techniques and applications for designing efficient circuits Covers advance nanodevices such as FinFETs TFETs CNTFETs Covers various emerging areas like Quantum Dot Cellular Automata Circuits and FPGAs and sensors Discusses applications like memory design for low power applications using nanodevices The number of options for ICs in control applications telecommunications high performance computing and consumer electronics continues to grow with the emergence of VLSI designs Nanodevices have revolutionized the electronics market and human life it has impacted individual life to make it more convenient They are ruling every sector such as electronics energy biomedicine food environment and communication This book discusses various emerging low power applications using CMOS and other emerging nanodevices

Wireless Security: Models, Threats, and Solutions Randall K. Nichols, Panos C. Lekkas, 2002 Nichols and Lekkas uncover the threats and vulnerabilities unique to the wireless communication telecom broadband and satellite markets They provide an overview of current commercial security solutions available on the open market *High Performance Dynamic Circuits* Ge Yang, 2003 **Simulation Techniques and Solutions for Mixed-Signal Coupling in Integrated Circuits**

Nishath K. Verghese, Timothy J. Schmerbeck, David J. Allstot, 2012-12-06 The goal of putting systems on a chip has been a difficult challenge that is only recently being met Since the world is analog putting systems on a chip requires putting analog interfaces on the same chip as digital processing functions Since some processing functions are accomplished more efficiently in analog circuitry chips with a large amount of analog and digital circuitry are being designed Whether a small

amount of analog circuitry is combined with varying amounts of digital circuitry or the other way around the problem encountered in marrying analog and digital circuitry are the same but with different scope Some of the most prevalent problems are chip package capacitive and inductive coupling ringing on the RLC tuned circuits that form the chip package power supply rails and off chip drivers and receivers coupling between circuits through the chip substrate bulk and radiated emissions from the chip package interconnects To aggravate the problems of designers who have to deal with the complexity of mixed signal coupling there is a lack of verification techniques to simulate the problem In addition to considering RLC models for the various chip package board level parasitics mixed signal circuit designers must also model coupling through the common substrate when simulating ICs to obtain an accurate estimate of coupled noise in their designs Unfortunately accurate simulation of substrate coupling has only recently begun to receive attention and techniques for the same are not widely known Simulation Techniques and Solutions for Mixed Signal Coupling in Integrated Circuits addresses two major issues of the mixed signal coupling problem how to simulate it and how to overcome it It identifies some of the problems that will be encountered gives examples of actual hardware experiences offers simulation techniques and suggests possible solutions Readers of this book should come away with a clear directive to simulate their design for interactions prior to building the design versus a build it and see mentality

The Cumulative Book Index, 1997 A world list of books in the English language

Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today's new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck

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Foundations and Frontiers in Computer, Communication and Electrical Engineering Aritra Acharyya, 2016-05-05 The 3rd International Conference on Foundations and Frontiers in Computer Communication and

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Subject Guide to Books in Print ,1996 **Proceedings** ,2004 **Annual IEEE Semiconductor Thermal Measurement and Management Symposium** ,2003 *CMOS Digital Integrated Circuits* Sung-Mo Kang,Yusuf Leblebici,2003 The fourth edition of CMOS Digital Integrated Circuits Analysis and Design continues the well established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design as well as addressing state of the art technology issues highlighted by the widespread use of nanometer scale CMOS technologies In this latest edition virtually all chapters have been re written the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations and the material has been reinforced with up to date examples The broad ranging coverage of this textbook starts with the fundamentals of CMOS process technology and continues with MOS transistor models basic CMOS gates interconnect effects dynamic circuits memory circuits arithmetic building blocks clock and I O circuits low power design techniques design for manufacturability and design for testability

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