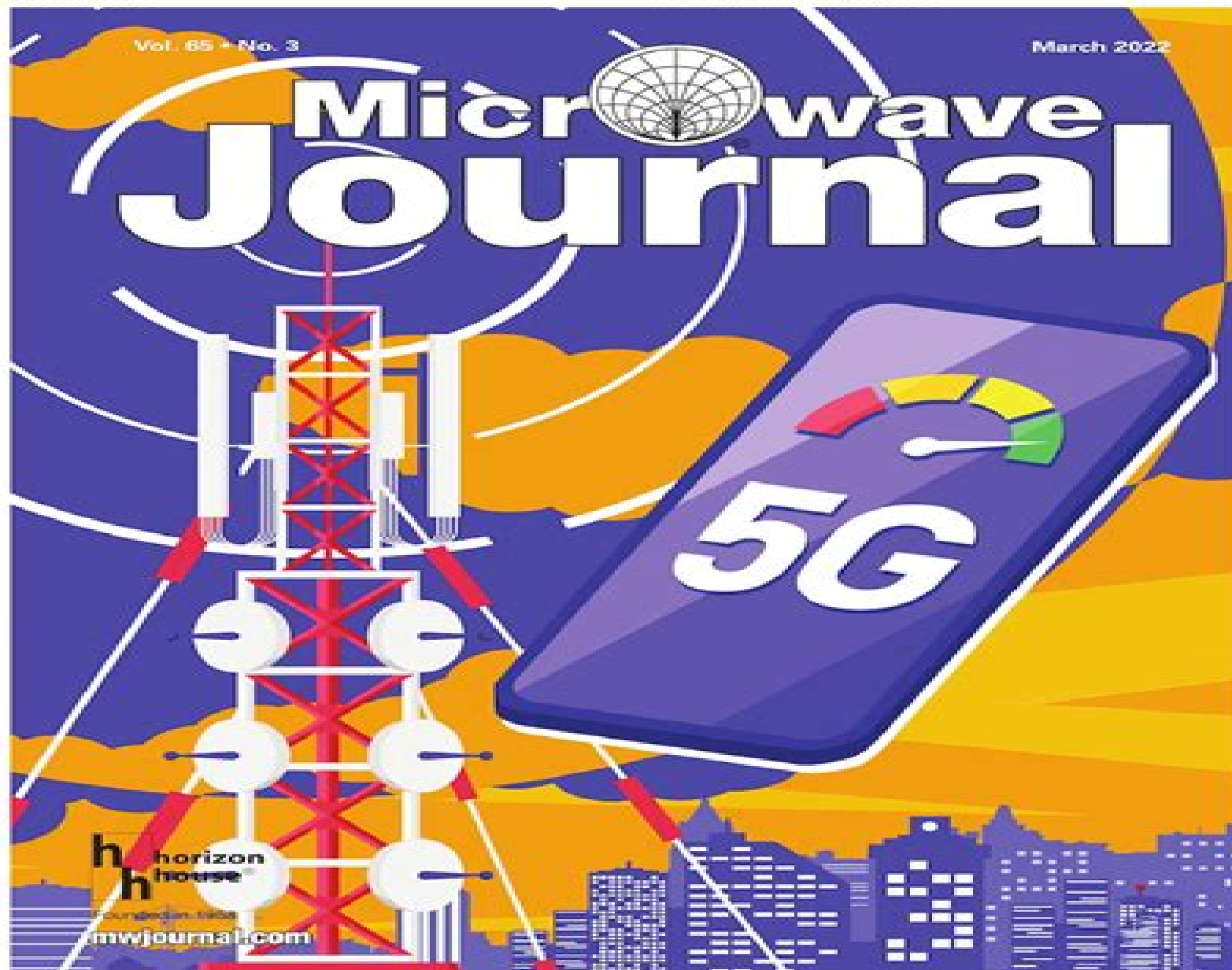




Microwave Journal

JE Gale



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Microwave Journal, 1993 **Introduction to Electronic Defense Systems** Filippo Neri, 2006 This revised edition surveys sophisticated electronic warfare systems with the latest technological advances New material covers current radar techniques with the latest in IR techniques and EW weapons systems and defense equipment It also includes an introduction to Information Operations and Information Warfare Hochfrequenzschaltungstechnik, 1998 **Digital Techniques for Wideband Receivers** James B. Tsui, 2004-06-30 This book is a current comprehensive design guide for your digital processing work with today's complex receiver systems This book brings you up to date with the latest information on wideband electronic warfare receivers the ADC testing procedure frequency channelization and decoding schemes and the operation of monobit receivers **Hochfrequenztechnik 1** Otto Zinke, Heinrich Brunswig, 2013-03-07 *Microwave Journal*, 1968 **Der Mensch im elektromagnetischen Feld. Ergonomie in der Medizin** Hans Jürgen Schmitt, 2013-03-08 RFIC and MMIC Design and Technology I.D. Robertson, S. Lucyszyn, Institution of Electrical Engineers, 2001-11-30 This book gives an in depth account of GaAs InP and SiGe technologies and describes all the key techniques for the design of amplifiers ranging from filters and data converters to image oscillators mixers switches variable attenuators phase shifters integrated antennas and complete monolithic transceivers Applications of Metamaterials Filippo Capolino, 2017-12-19 This book uses the first volume's exploration of theory basic properties and modeling topics to develop readers understanding of applications and devices that are based on artificial materials It explores a wide range of applications in fields including electronics telecommunications sensing medical instrumentation and data storage The text also includes a practical user's guide and explores key areas in which artificial materials have developed It includes experts perspectives on current and future applications of metamaterials to present a well rounded view on state of the art technologies **Microwave and Wireless Synthesizers** Ulrich L. Rohde, 1997-08-25 Over the past decade great strides have been made in the technology of microwave oscillators and synthesizers with digital frequency synthesizers in particular attracting much attention These synthesizers are now being used in virtually all modern signal generators and radio communication equipment Until now however detailed information about their design has been hard to come by much of it scattered through journal articles and most books on the subject have taken a primarily theoretical approach Enter *Microwave and Wireless Synthesizers* the first book to emphasize both practical circuit information from RF to millimeter wave frequencies and up to date theory Based on course material taught by author Ulrich L Rohde at George Washington University and recent work done by the author at Compact Software Inc and Synergy Microwave Corporation this volume is a complete revision and update of Rohde's landmark text *Digital PLL Frequency Synthesizers Theory and Design* While it provides all the necessary theory and formulas it also offers an in depth look at the practical side of the phase lock loop PLL in synthesizers including special loops loop components and practical circuits material that is not available in any other book

Rohde explains loop fundamentals demonstrates the linear approach to oscillator phase noise discusses the digital direct synthesizer technique addresses low noise oscillator design and provides insight into the role and design of crystal oscillators mixers phase frequency discriminators wideband high gain amplifiers programmable dividers and loop filters He goes on to cover conventional multiloop synthesizers and survey existing state of the art microwave synthesizer applications Extensive appendices review the mathematics of useful functions and various applications including even the complex nonlinear theory of noise in large signal systems such as mixers and oscillators Microwave and Wireless Synthesizers allows anyone with a PC running either Windows 3.11 or Windows NT to explore real world design It uses programs for the solution of digital phase lock loop systems tabulates the results and shows how Bode diagrams are determined by the computer's graphic capabilities It also includes examples using commercially available linear and nonlinear CAD programs to provide accurate evaluation and optimization of oscillators and other useful circuits and many practical charts For companies involved in test and communication equipment this book reduces design and research costs by providing a large number of proven circuits and expediting the design process It is also an outstanding senior graduate level textbook for electrical engineering students and an invaluable resource for practicing engineers senior engineers and managers who would like to be able to evaluate new trends and techniques in the field

Eine Integrationstechnik auf Waferebene für Millimeterwellenschaltungen unter Verwendung von Techniken aus der Mikromechanik Peter Abele, 2007-06-06 Der Aufbau von Modulen im Millimeterwellenbereich stellt sowohl an die Kosten als auch an die Strukturgenauigkeit der Aufbautechnik hohe Anforderungen Um die Modulkosten gering zu halten kommt bei der Realisierung der integrierten Schaltungen eine kostengünstige SiGe Technologie zum Einsatz Dabei werden mit dem SiGe1 Prozess von ATMEL Schaltungen für den Frequenzbereich von 24GHz und 32GHz hergestellt Die Schaltungen umfassen Oszillatoren und Mischen mit denen ein Doppler Sensor aufgebaut wird Um die Kosten der integrierten Schaltungen gering zu halten finden konzentrierte passive Strukturen ihren Einsatz Das führt zu sehr kompakten Schaltungen Für die Modellierung der SiGe Transistoren kommt das Mextram Modell zum Einsatz Die Modellierung der passiven Strukturen einschließlich von Antennen wird in der Arbeit ebenfalls beschrieben Für den Aufbau der Module wird eine Integrationstechnik vorgestellt die sich an die MCM Technik anlehnt Diese erlaubt die Herstellung hochwertiger passiver Strukturen wie Mikrostreifenleitungen Koplanarleitungen oder Antennen Dabei findet der Kunststoff BCB Benzocyclobuten vielfach Verwendung Er dient zur Planarisierung als dielektrische Zwischenschicht und Membrane Als Substrat kommt Silizium zur Anwendung In dieses lassen sich durch Techniken aus der Mikromechanik strukturgenau tiefe Graben setzen In diese Graben werden die integrierten Schaltung eingesetzt und mittels BCB wird zwischen dem eingesetzten Schaltkreis und der Substratoberfläche planarisiert Die weitere Prozessierung gleicht der von integrierten Schaltungen Dadurch verbindet diese Integrationstechnik die Flexibilität des hybriden Aufbaus durch die freie Wahl der Komponenten mit der Strukturgenauigkeit der Integrationstechnik Stichwörter Integrationsmechanik Mikromechanik SiGe

MBT **Mikrowellenoszillatoren für die Erzeugung von atmosphärischen Mikroplasmen** Silvio Kühn, 2013-04-15 Die vorliegende Arbeit behandelt den Entwicklungsprozess von Mikrowellenplasmaquellen anhand einer speziellen 2 45 GHz Atmosphärenquelle mit integriertem Solid State Oszillator Dabei regen Mikrowellensignale das Plasma an der Umgebungsluft an Die Entwicklungen berücksichtigen den Trend unkompliziert handhabbare Plasmen für technische Anwendungen ohne aufwendige Vakuumkammern zur Verfügung zu stellen Zusätzlich sind weder Hochspannungsversorgungen noch umfangreiche Generatorsysteme notwendig Die Untersuchungen reichen von den theoretischen Überlegungen bis hin zur praktischen Realisierung eines Prototyps Die Arbeit konzentriert sich zunächst auf die Charakterisierung des atmosphärischen Plasmas und dessen Modellierung Diese Ergebnisse werden in entsprechenden Mikrowellen Modellen berücksichtigt die sowohl für die transiente Simulation bezogen auf den betrachteten Plasmafall als auch in der Harmonic Balance Rechnung nutzbar sind Diese mikrowellentechnischen Plasma Untersuchungen bilden die Grundlage für den Oszillatorentwurf der im Mittelpunkt der Arbeit steht Es werden neue Ansätze im Schaltungssimulator aufgezeigt und verfolgt Das betrifft sowohl generelle Untersuchungen zur Instabilität die durch Einführen einer neuen Methode erfasst werden können als auch das Anwenden einer innovativen transienten Simulationstechnik Die dabei gewonnenen Simulationsergebnisse werden anschließend genutzt um einen Prototyp der Plasmaquelle zu realisieren Im Inbetriebnahmeprozess werden gezielt Messungen am realisierten Aufbau durchgeführt die den Vergleich zur Simulation ermöglichen Abschließend wird das an der Umgebungsluft erzeugte atmosphärische Plasma umfassend charakterisiert *Microwave Receivers and Related Components* James Tsui, 1983 Gallium Arsenide and Related Compounds 1992, Proceedings of the 19th INT Symposium, 28 September-2 October 1992, Karuizawa, Japan Ikegami, 1993-01-01 Bringing together international experts from 16 countries Gallium Arsenide and Related Compounds 1992 focuses on device applications for Gallium Arsenide and related compounds A topic of importance discussed is the first GaAs supercomputer from Fujitsu The book also explores carbon doping and device applications in laser diodes light modulators and amplifiers emphasizing business opportunity in consumer applications such as personal communications and TV tuners It includes an account of the use of scanning tunneling microscopies in GaAs and related compounds This book is ideal for physicists materials scientists and electronics and electrical engineers involved in III V compound research *Microwave Tube Transmitters* L. Sivan, 1994-09-30 This book fulfills the needs of engineers and technicians who specify procure design develop test manufacture operate and service tubes power supply modulators and complete transmitters for radar ECM and communications and broadcast systems The material in the book is also applicable to microwave transmitters for scientific applications **Optoelectronic Integrated Circuit Design and Device Modeling** Jianjun Gao, 2011-09-19 In Optoelectronic Integrated Circuit Design and Device Modeling Professor Jianjun Gao introduces the fundamentals and modeling techniques of optoelectronic devices used in high speed optical transmission systems Gao covers electronic circuit elements such as FET HBT MOSFET as well as design techniques for advanced optical transmitter

and receiver front end circuits The book includes an overview of optical communication systems and computer aided optoelectronic IC design before going over the basic concept of laser diodes This is followed by modeling and parameter extraction techniques of lasers and photodiodes Gao covers high speed electronic semiconductor devices optical transmitter design and optical receiver design in the final three chapters Addresses a gap within the rapidly growing area of transmitter and receiver modeling in OEICs Explains diode physics before device modeling helping readers understand their equivalent circuit models Provides comprehensive explanations for E O and O E conversions done with laser and photodiodes Covers an extensive range of devices for high speed applications Accessible for students new to microwaves Presentation slides available for instructor use This book is primarily aimed at practicing engineers researchers and post graduates in the areas of RF microwaves IC design photonics and lasers and solid state devices The book is also a strong supplement for senior undergraduates taking courses in RF and microwaves Lecture materials for instructors available at www.wiley.com/go/gao

3D Printing Technologies Ajay Kumar, Parveen Kumar, Naveen Sharma, Ashish Kumar Srivastava, 2024-01-29 Additive Manufacturing is a method of manufacturing parts and products directly from design data by adding layers of materials in order to obtain the final shape and size with high accuracy and negligible waste The book covers the latest developments of hybrid and bio inspired 3D Printing the use of Artificial Intelligence and the applications to Industry 4 0 real time defect detection hybrid and bio inspired 3D Printing **Electromagnetic Metrology**, 1975 *Electromagnetic Metrology; Current Awareness Service*, 1976

HF-Leistungsverstärker in modernen Silizium- und Verbindungshalbleitern-Technologien Daniel Gruner, 2012-11-01 Kurzbeschreibung Diese Dissertation beschäftigt sich mit HF Leistungsverstärkern auf Basis moderner Transistortechnologien und verfolgt aktuelle Trends im Bereich Silizium und III V Verbindungshalbleiter Dies umfasst den Entwurf von innovativen HF Leistungstransistoren und Verstärkern Aspekte zur Charakterisierung und Aufbautechnik sowie den Vergleich zum aktuellen Stand von Wissenschaft und Technik Im ersten Teil richtet sich der Fokus auf MMIC Leistungsverstärker für 5 6 GHz WLAN Anwendungen Hierbei wird zunächst auf den Entwurf eines Push Pull PAs in einer 0 25 µm BiCMOS Technologie eingegangen Mit einer Betriebsspannung von lediglich 1 8 V zeigt dieser Verstärker ein breitbandiges Verhalten und erreicht im Frequenzbereich 5 6 2 GHz eine Sättigungsleistung von 24 dBm Der maximale Wirkungsgrad liegt bei 27 % PAE 18 % mit einer Verstärkung von typischerweise 9 dB Um diese Performance weiter zu verbessern wird eine innovative Transformer Combining Topologie vorgestellt Im Vergleich zum Push Pull Verstärker kann diese eine erhöhte Verstärkung 12 dB sowie einen exzellenten PAE Maximalwert von 23 % bei sonst ähnlichen Eigenschaften erzielen Als zweite Zielstellung wird die Steigerung der HF Leistungs Performance innerhalb moderner BiCMOS Technologien verfolgt In diesem Rahmen stehen die Analyse der Entwurf und die Evaluierung von LDMOS Transistoren für Anwendungen bis in den 6 GHz Bereich im Mittelpunkt Der Einfluss von Transistorgröße und Geometrie sowie auch der Stabilitätsnetzwerke wird unter Verwendung HF optimierter LDMOS Strukturen untersucht Die Evaluierung der

Source Load Load Pull Messungen wird mit Hilfe mehrerer hybrider Verstärkermodule durchgeführt Als eine der ersten Veröffentlichungen kann diese Arbeit eine 6 GHz LDMOS Implementation in einer Standard BiCMOS Technologie mit einer Ausgangsleistung von mehr als 1 W und einem Wirkungsgrad über 40 % demonstrieren Der dritte Teil beschäftigt sich mit GaN Doherty Verstärkern für das 6 GHz Band Die Kombination von Doherty Konzept mit Strategien zur Harmonischen Terminierung einem auf Device Referenzebene optimierten Entwurf und der entwickelten Chip Wire Aufbautechnik erlaubt die Realisierung von einem der ersten 6 GHz Doherty Verstärker mit exzellenter Wirkungsgrad Leistungs sowie Linearitäts Performance Unter Verwendung zweier GaN Chip Transistoren kann eine maximale Ausgangsleistung von 41.5 dBm mit einem maximalen Wirkungsgrad von 63 % erreicht werden Im 6 dB Backoff Punkt wird dabei ein exzellenter Wirkungsgrad von 49 % erzielt

Description This work focuses on RF power amplifiers based on modern transistor technologies and investigates current trends in silicon as well as III V semiconductors This includes the design of innovative RF power devices and amplifiers characterization and assembling techniques as well as the comparison to other state of the art achievements The first part focuses on MMIC power amplifiers for WLAN applications in the 5.6 GHz range Based on a 0.25 μ m BiCMOS technology a low voltage push pull power amplifier using integrated transformers is presented At a supply voltage of 1.8 V this fully integrated amplifier achieves a wideband performance showing a maximum output power of 24 dBm across 5.6-6.2 GHz A maximum efficiency of 27 % PAE 18 % is obtained with a small signal gain of typically 9 dB To further improve this performance an innovative transformer combining topology is developed Compared to the push pull amplifier this topology achieves an increased gain of 12 dB and an enhanced maximum PAE of up to 23 % with otherwise similar characteristics showing state of the art performance The RF power enhancement of BiCMOS technologies is a second major aim of this work In this context the analysis design and evaluation of medium voltage LDMOS FETs for wireless applications up to 6 GHz is presented Using RF optimized NLD MOS transistors power devices of different sizes were fabricated in a standard 0.25 μ m BiCMOS technology The influences of the finger geometry and the stabilization networks on the RF performance are studied Power evaluation of the LDMOS transistors is carried out by means of hybrid power amplifier modules As one of the first contributions this work shows a 6 GHz LDMOS implementation in a standard BiCMOS technology that achieves an output power of greater than 1 W with a drain efficiency of more than 40 % In the third part a harmonically tuned GaN Doherty amplifier targeted for vehicular applications at 6 GHz is presented The combination of the Doherty concept with harmonic tuning strategies an optimized design approach and the developed chip wire assembly technology results in one of the first Doherty amplifiers implemented for the 6 GHz frequency range with excellent efficiency output power and linearity performance Using two bare die GaN devices a maximum output power of 41.5 dBm with 63% peak drain efficiency is obtained At 6 dB output power back off an excellent drain efficiency of still 49 % can be achieved

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