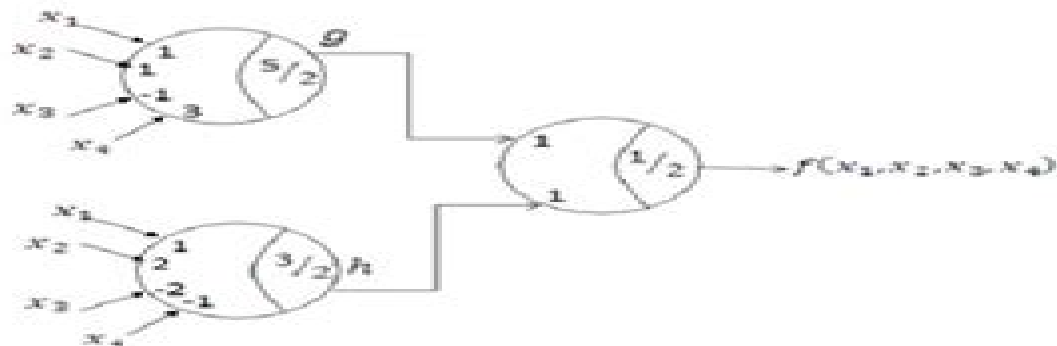


DIGITAL SWITCHING AND LOGIC DESIGN

QUESTION BANK

CHAPTER1: Threshold Logic

- Q1. Is it possible to design EX-OR gate with a single threshold element? Justify your answer.
- Q2. Let $f(x_1, x_2, x_3) = \sum(0,1,3)$. Realize it using a threshold element.
- Q3. What are the various capabilities of threshold logic?
- Q5. Determine whether the function $f(x_1, x_2, x_3, x_4) = \sum(0,1,3,4,5,6,7,12,13)$ is a threshold function, and, if it is, find a weight-threshold vector.
- Q6. By examining the linear inequalities, determine which of the following functions is a threshold function, and for each one that is, find the corresponding weight-threshold vector.
- a) $f_1(x_1, x_2, x_3) = \sum(1,2,3,7)$
 - b) $f_1(x_1, x_2, x_3) = \sum(0,2,4,5,6)$
 - c) $f_1(x_1, x_2, x_3) = \sum(0,3,5,6)$
- Q7. Determine the function $f(x_1, x_2, x_3, x_4)$ realized by the network as shown in fig below.



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Na Vikraman



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is to provide a basic knowledge in Digital Electronics Digital Logic Circuits Digital Principles and System Design for engineering students of degree diploma AMIE courses and a useful reference for these preparing for competitive examinations All the concepts are explained in a simple clear and complete manner to achieve progressive learning Two marks questions and answers Short Long answer questions are provided at the end of fifth chapter This book is divided into five chapters Each chapter is well supported with the necessary illustration practical examples and solved problems

Solutions Manual to Accompany Digital Logic Testing and Simulation Alexander Miczo,1986 Digital Logic Techniques John Stonham,2017-11-22 The third edition of Digital Logic Techniques provides a clear and comprehensive treatment of the representation of data operations on data combinational logic design sequential logic computer architecture and practical digital circuits A wealth of exercises and worked examples in each chapter give students valuable experience in applying the concepts and techniques discussed Beginning with an objective comparison between analogue and digital representation of data the author presents the Boolean algebra framework for digital electronics develops combinational logic design from first principles and presents cellular logic as an alternative structure more relevant than canonical forms to VLSI implementation He then addresses sequential logic design and develops a strategy for designing finite state machines giving students a solid foundation for more advanced studies in automata theory The second half of the book focuses on the digital system as an entity Here the author examines the implementation of logic systems in programmable hardware outlines the specification of a system explores arithmetic processors and elucidates fault diagnosis The final chapter examines the electrical properties of logic components compares the different logic families and highlights the problems that can arise in constructing practical hardware systems *Digital Logic Testing and Simulation* Alexander Miczo,1985-12 The new standard in the field presenting the latest design and testing methods for logic circuits and the development of a BASIC based simulation Offers designers and test engineers unique coverage of circuit design for testability stressing the incorporation of hardware into designs that facilitate testing and diagnosis by allowing greater access to internal circuits Examines various ways of representing a design as well as external testing methods that apply this information

Foundations Of Digital Logic Design Abraham Kandel,Gideon Langholz,Joe L Mott,1998-08-11 This text is intended for a first course in digital logic design at the sophomore or junior level for electrical engineering computer engineering and computer science programs as well as for a number of other disciplines such as physics and mathematics The book can also be used for self study or for review by practicing engineers and computer scientists not intimately familiar with the subject After completing this text the student should be prepared for a second advanced course in digital design switching and automata theory microprocessors or computer organization DIGITAL LOGIC DESIGN Sonali Singh,2018-06-01 Description The book is an attempt to make Digital Logic Design easy and simple to understand The book covers various features of Logic Design using lots of examples and relevant diagrams The complete text is reviewed for its correctness This

book is an outcome of sincere effort and hard work to bring concepts of Digital Logic Design close to the audience of this book. The salient features of the book are: Easy explanation of Digital System and Binary Numbers with lots of solved examples; Detailed covering of Boolean Algebra and Gate Level Minimization with proper examples and diagrammatic representation; Detailed analysis of different Combinational Logic Circuits; Complete Synchronous sequential Logic understanding; Deep understanding of Memory and Programmable Logic; Detailed analysis of different Asynchronous Sequential Logic. Table Of Contents: Unit 1 Digital System and Binary Numbers Part 1 Digital System and Binary Numbers Part 2 Boolean Algebra and Gate Level Minimization Unit 2 Combinational Logic Unit 3 Sequential Circuits Unit 4 Memory Programmable Logic and Design Unit 5 Asynchronous Sequential Logic. *Digital Logic Design* Brian Holdsworth, 1987. New updated and expanded topics in the fourth edition include EBCDIC Grey code practical applications of flip flops linear and shaft encoders memory elements and FPGAs. The section on fault finding has been expanded. A new chapter is dedicated to the interface between digital components and analog voltages. A highly accessible comprehensive and fully up to date digital systems text. A well known and respected text now revamped for current courses. Part of the Newnes suite of texts for HND 1st year modules.

The Essence of Digital Design Barry Wilkinson, 1998. Wilkinson provides a concise introduction to all the fundamental aspects of digital logic design covering state diagrams including those with transitional expressions and programmable logic devices. He also looks at basic fault testing. **A Systematic Approach to Digital Logic Design** Frederic J. Mowle, 1976. Number systems Base R arithmetic Boolean algebra Special boolean functions and basic logic conventions Minimization procedures for boolean function Binary arithmetic units Decimal arithmetic Introduction to sequential circuit design Practical flip flop circuits Binary counters Register design techniques Advanced arithmetic units. **Digital Logic and State Machine Design** David J. Comer, 1984. Fundamentals of Logic Design Charles H. Roth, 1992. *Digital Logic Design* John F. Passafiume, Michael Douglas, 1985. **Handbook of Digital Techniques for High-Speed Design**, 2007-09.

Digital Logic Design MCQs: Multiple Choice Questions and Answers (Quiz & Tests with Answer Keys) Arshad Iqbal, 2016. *Digital Logic Circuit Analysis and Design* Victor Peter Nelson, 1995. For introductory digital logic design or computer engineering courses in electrical and computer engineering or computer science at the sophomore or junior level. Many recent texts place instructors in the difficult position of choosing between authoritative state of the art coverage and an approach that is highly supportive of student learning. This carefully developed text was widely praised by reviewers for both its great clarity and its rigor. The book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language and has abundant coverage of current topics of interest such as programmable devices computer aided design and testability. An unusually large number of illustrations examples and problems help students gain a solid sense of how theory underlies practice.

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