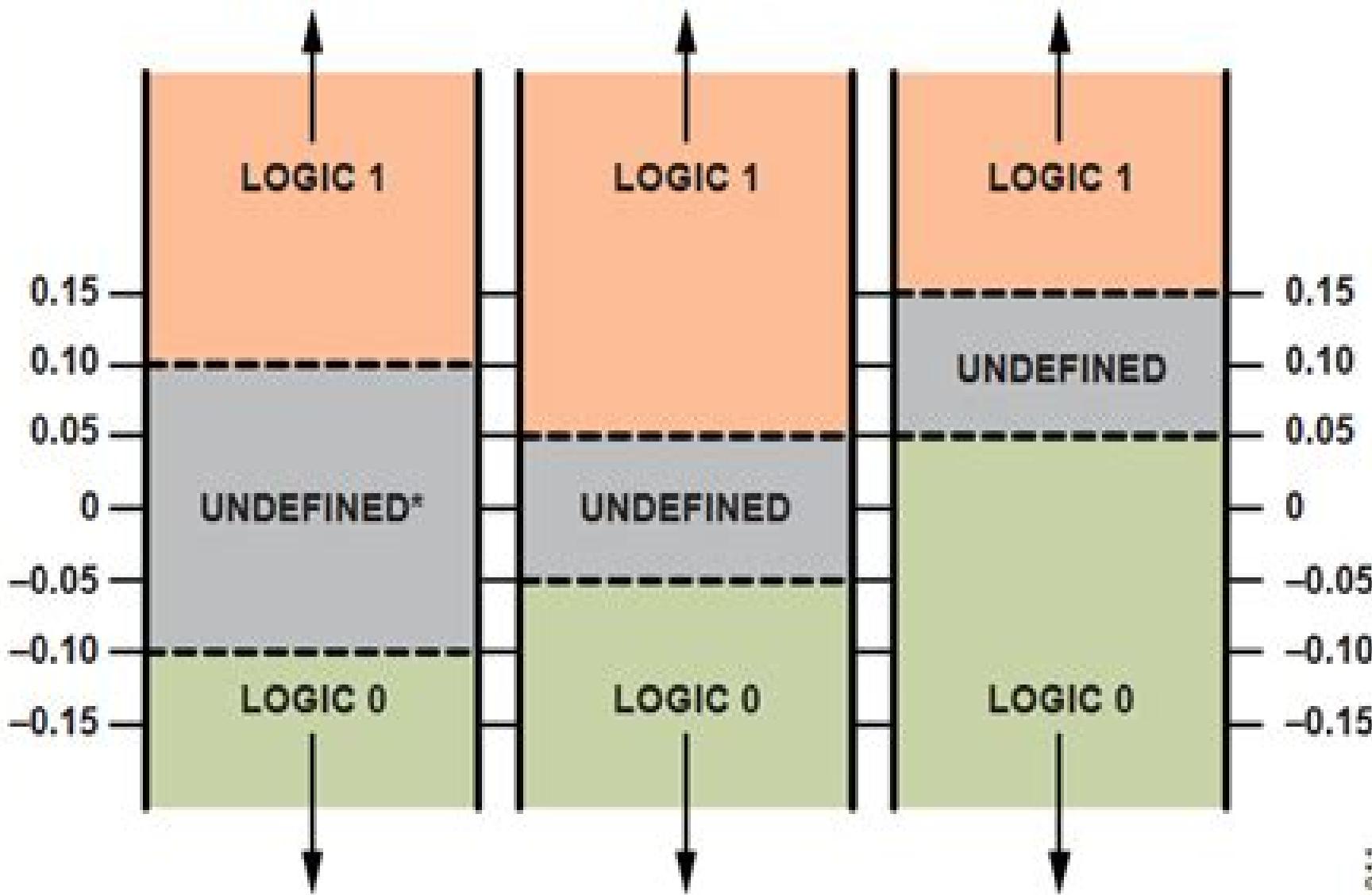


DIFFERENTIAL INPUT VOLTAGE ( $V_{IA} - V_{IB}$ ) [V]

LVDS  
RECEIVER  
OUTPUT

M-LVDS TYPE 1  
RECEIVER  
OUTPUT

M-LVDS TYPE 2  
RECEIVER  
OUTPUT



\*LOGIC 1 FOR LVDS RECEIVERS WITH FAILSAFE

# Lvds And M Lvds Circuit Implementation Guide

**Arturo Cuomo**

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