

Fourth Semester B.E. Degree Examination, Dec.2017/Jan.2018
Microprocessor and Microcontroller

Time: 3 hrs.

Max. Marks: 80

Note: Answer any FIVE full questions, choosing one full question from each module.

Module-1

1.
 - a. Explain the architecture of 8086 microprocessor with a neat diagram along with functions of various blocks. (06 Marks)
 - b. With an example distinguish between physical address, logical address and offset address. If CS = 2000 h, DS = 3000 h, SS = 4000 h, ES = 5000 h, BX = 0020 h, BP = 0030 h. Find physical address for (i) MOV AL, [BP] (ii) MOV CX, [BX]. (04 Marks)
 - c. Explain the following addressing modes of 8086:
 - i) Register indirect
 - ii) Based indexed indirect
 - iii) Direct memory. (06 Marks)

OR

2.
 - a. What are assembler directives? Explain the following assembler directives (i) PROC, (ii) Assume, (iii) PTR. (04 Marks)
 - b. Write assembly language program to add 5 bytes of data stored in data segment. (04 Marks)
 - c. With syntax, explain the following control transfer instructions:
 - i) Conditional transfer
 - ii) Unconditional transfer instruction. (08 Marks)

Module-2

3.
 - a. Explain the syntax of following instructions with an example:
 - i) DAA ii) MUL iii) AND iv) SHR v) CMP vi) AAM (06 Marks)
 - b. Write a program to convert lower case to upper case by reading string from KB and print the converted string at 10th row, 20th column after clearing the screen. (06 Marks)
 - c. Write an ALP to count the number of one's and zero's in a given 8 bit data using rotate instructions. (04 Marks)

OR

4.
 - a. Explain the syntax of following instructions with example: i) AAA, ii) Shl, iii) DIV, iv) RCR. (04 Marks)
 - b. What is an interrupt? Explain various types with an interrupt vector table. (06 Marks)
 - c. Write an ALP to sort a given set of 16 bit numbers in ascending order using any sorting method. (06 Marks)

Module-3

5.
 - a. With an example, explain how to identify over flow and under flow using flags in a flag register for performing arithmetic operation on 16 bit number. (06 Marks)
 - b. Write the syntax of following instruction and explain with an example: (i) CBW, (ii) IDIV, (iii) CMPSB, (iv) Xlat. (04 Marks)
 - c. Design a memory system for 8086 with one 64 KB RAM and one 64 KB ROM at address 30000h and F0000h show the complete design along with memory mapping and draw the final diagram with address decoder. (06 Marks)

Microprocessor Question Papers

Atul P. Godse, Dr. Deepali A. Godse



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electronics and communication computer science and information technology who wish to learn about Microprocessor Microcontroller and crack an interview Table of Contents 1 Number Systems 2 Digital Circuit 3 Microprocessor 8085 4 Peripheral Devices and Interfacing 5 AVR ATmega32 6 Interfacing of Input Output Device 7 Exercise 8 Descriptive Type Questions 9 Multiple Choice Questions **Microprocessor (8085) Lab Manual** G.T. Swamy,2006 Oswaal CBSE Question Bank Class 11 Computer Science, Chapterwise and Topicwise Solved Papers For 2025 Exams Oswaal Editorial Board,2024-02-03 Description of the product 100% Updated Syllabus Question Typologies We have got you covered with the latest and 100% updated curriculum along with the latest typologies of Questions Timed Revision with Topic wise Revision Notes Smart Mind Maps Study smart not hard Extensive Practice with 1000 Questions SAS Questions Sri Aurobindo Society To give you 1000 chances to become a champ Concept Clarity with 500 Concepts Concept Videos For you to learn the cool way with videos and mind blowing concepts NEP 2020 Compliance with Competency Based Questions Artificial Intelligence For you to be on the cutting edge of the coolest educational trends **Oswaal CBSE Question Bank Class 9 Computer Application, Chapterwise and Topicwise Solved Papers For 2025 Exams** Oswaal Editorial Board,2024-02-03 Oswaal CBSE Question Bank Class 9 Computer Application Chapterwise and Topicwise Solved Papers For 2025 Exams **[2000 MCQs] General Computer Knowledge Question Bank** Mocktime Publication, 2000 MCQs General Computer Knowledge Question Bank *Oswaal CBSE Question Bank Class 10 Information Technology, Chapterwise and Topicwise Solved Papers For Board Exams 2025* Oswaal Editorial Board,2024-06-05 DESCRIPTION OF THE PRODUCT 100% Updated with Latest Syllabus Questions Typologies We have got you covered with the latest and 100% updated curriculum Crisp Revision with Topic wise Revision Notes Smart Mind Maps Study smart not hard Extensive Practice with 700 Questions Self Assessment Papers To give you 700 chances to become a champ Concept Clarity with 500 Concepts Concept Videos For you to learn the cool way with videos and mind blowing concepts 100% Exam Readiness with Expert Answering Tips Suggestions for Students For you to be on the cutting edge of the coolest educational trends **Arihant CBSE Term 1 Information Technology (Code 402) Sample Papers Questions for Class 10 MCQ Books for 2021 (As Per CBSE Sample Papers issued on 2 Sep 2021)** Chhirja Yadav,2021-10-12 This year has witness major changes in the field of academics where CBSE s reduced syllabus was a pleasant surprise while the introduction of 2 Term exam pattern was little uncertain for students parents and teachers as well Now more than ever the Sample Papers have become paramount importance of subjects with the recent changes prescribed by the board Give final punch to preparation for CBSE Term 1 examination with the all new edition of Sample Question Papers that is designed as per CBSE Sample Paper that are issued on 02 Sept 2021 for 2021 22 academic session Encouraging with the motto of Keep Practicing Keep Scoring here s presenting Sample Question Paper Computer Applications for Class 10th that consists of 1 10 Sample Papers along with OMR Sheet for quick revision of topics 2 One Day Revision Notes to recall the concepts a day before exam 3 The Qualifiers Chapterwise sets of MCQs to check preparation

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microprocessor It also introduces advanced processors from Intel family SUN SPARC microprocessor and ARM Processor The book teaches you the 8085 architecture instruction set machine cycles and timing diagrams Assembly Language Programming ALP Interrupts interfacing 8085 with support chips memory and peripheral ICs 8255 and 8259 The book explains the features architecture memory addressing operating modes addressing modes of Intel 8086 80286 80386 microprocessors segmentation paging and protection mechanism provided by 80386 microprocessor and the features of 80486 and Pentium Processors It also explains the architecture of SUN SPARC microprocessor and ARM Processor

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Table of Contents Microprocessor Question Papers

1. Understanding the eBook Microprocessor Question Papers
 - The Rise of Digital Reading Microprocessor Question Papers
 - Advantages of eBooks Over Traditional Books
2. Identifying Microprocessor Question Papers
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Microprocessor Question Papers
 - User-Friendly Interface
4. Exploring eBook Recommendations from Microprocessor Question Papers
 - Personalized Recommendations
 - Microprocessor Question Papers User Reviews and Ratings
 - Microprocessor Question Papers and Bestseller Lists

5. Accessing Microprocessor Question Papers Free and Paid eBooks
 - Microprocessor Question Papers Public Domain eBooks
 - Microprocessor Question Papers eBook Subscription Services
 - Microprocessor Question Papers Budget-Friendly Options
6. Navigating Microprocessor Question Papers eBook Formats
 - ePub, PDF, MOBI, and More
 - Microprocessor Question Papers Compatibility with Devices
 - Microprocessor Question Papers Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Microprocessor Question Papers
 - Highlighting and Note-Taking Microprocessor Question Papers
 - Interactive Elements Microprocessor Question Papers
8. Staying Engaged with Microprocessor Question Papers
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Microprocessor Question Papers
9. Balancing eBooks and Physical Books Microprocessor Question Papers
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Microprocessor Question Papers
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Microprocessor Question Papers
 - Setting Reading Goals Microprocessor Question Papers
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Microprocessor Question Papers
 - Fact-Checking eBook Content of Microprocessor Question Papers
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
- Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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