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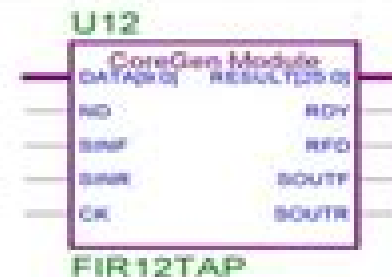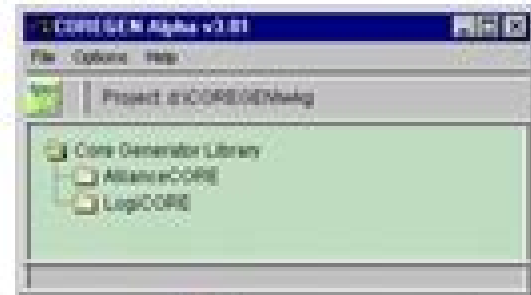
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Hardware Acceleration of Computational Holography Tomoyoshi Shimobaba,Tomoyoshi Ito,2023-07-17 This book explains the hardware implementation of computational holography and hardware acceleration techniques along with a number ofconcrete example source codes that enable fast computation Computational holography includes computer based holographictechnologies such as computer generated hologram and digital holography for which acceleration of wave optics computation is highly desirable This book describes hardware implementations on CPUs Central Processing Units GPUs Graphics Processing Units and FPGAs Field ProgrammableGate Arrays This book is intended for readers involved in holography as well as anyone interested in hardware acceleration **Rapid System Prototyping with FPGAs** R. C. Cofer,Benjamin F. Harding,2006 Reading this guide will take a designer with a basic knowledge of FPGAs to the next level of FPGA implementation Jacket **Handbook of Signal Processing Systems** Shuvra S. Bhattacharyya,Ed F. Deprettere,Rainer Leupers,Jarmo Takala,2010-09-10 It gives me immense pleasure to introduce this timely handbook to the research velopment communities in the eld of signal processing systems SPS This is the rst of its kind and represents state of the arts coverage of research in this eld The driving force behind information technologies IT hinges critically upon the major advances in both component integration and system integration The major breakthrough for the former is undoubtedly the invention of IC in the 50 s by Jack S Kilby the Nobel Prize Laureate in Physics 2000 In an integrated circuit all components were made of the same semiconductor material Beginning with the pocket calculator in 1964 there have been many increasingly complex applications followed In fact processing gates and memory storage on a chip have since then grown at an exponential rate following Moore s Law Moore himself admitted that Moore s Law had turned out to be more accurate longer lasting and deeper in impact than he ever imagined With greater device integration various signal processing systems have been realized for many killer IT applications Further breakthroughs in computer sciences and Internet technologies have also catalyzed large scale system integration All these have led to today s IT revolution which has profound impacts on our lifestyle and overall prospect of humanity It is hard to imagine life today without mobiles or Internets The success of SPS requires a well concerted integrated approach from mul ple disciplines such as device design and application

Software/hardware FPGA-based system for the solution of the 3D heat equation: applications on the non-destructive evaluation of minefield. , , **Field Programmable Logic and Applications** Patrick Lysaght,James Irvine,Reiner Hartenstein,2004-06-22 This book contains the papers presented at the 9th International Workshop on Field ProgrammableLogic and Applications FPL 99 hosted by the University of Strathclyde in Glasgow Scotland August 30 September 1 1999 FPL 99 is the ninth in the series of annual FPL workshops The FPL 99 programme committee has been fortunate to have received a large number of high quality papers addressing a wide range of topics From these 33 papers have been selected for presentation at the workshop and a further 32 papers have been accepted for the poster sessions A

total of 65 papers from 20 countries are included in this volume FPL is a subject area that attracts researchers from both electronic engine ing and computer science Whether we are engaged in research into soft ha ware or hard software seems to be primarily a question of perspective What is unquestionable is that the interaction of groups of researchers from di erent backgrounds results in stimulating and productive research As we prepare for the new millennium the premier European forum for searchers in eld programmable logic remains the FPL workshop Next year the FPL series of workshopswill celebrate its tenth anniversary The contribution of so many overseas researchers has been a particularly attractive feature of these events giving them a truly international perspective while the informal and convivial atmosphere that pervades the workshops have been their hallmark We look forward to preserving these features in the future while continuing to expand the size and quality of the events <u>Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation</u> Lars Svensson,José Monteiro,2009-02-13 This book constitutes the thoroughly refereed post conference proceedings of 18th International Workshop on Power and Timing Modeling Optimization and Simulation PATMOS 2008 featuring Integrated Circuit and System Design held in Lisbon Portugal during September 10 12 2008 The 31 revised full papers and 10 revised poster papers presented together with 3 invited talks and 4 papers from a special session on reconfigurable architectures were carefully reviewed and selected from numerous submissions The papers are organized in topical sections on low leakage and subthreshold circuits low power methods and models arithmetic and memories variability and statistical timing synchronization and interconnect power supplies and switching noise low power circuits reconfigurable architectures circuits and methods power and delay modeling as well as power optimizations addressing reconfigurable architectures **DESIGN OF 4x4 BIT SRAM USING VHDL** Amit Bhattacharyya,2016-02-02 Memory arrays are an essential building block in any digital system The aspects of designing an SRAM are very vital to designing other digital circuits as well The majority of space taken in an integrated circuit is the memory SRAM design consists of key considerations such as increased speed and reduced layout area The hope for this project was to be able to create an efficient and compact SRAM Due to time limitations the goal was to create a working SRAM design and to learn how the SRAM functions Design choices were made and justified appropriately RAM has become a major component in many VLSI Chips due to their large storage density and small access time SRAM has become the topic of substantial research due to the rapid development for low power low voltage memory design during recent years due to increase demand for notebooks laptops IC memory cards and hand held communication devices SRAMs are widely used for mobile applications as both on chip and off c because of their ease of use and low standby leakage The main objective of this paper is evaluating performance in terms of Power consumption delay **FPGA-based Digital Convolution for Wireless Applications** Lei Guan,2017-01-16 This book presents essential perspectives on digital convolutions in wireless communications systems and illustrates their corresponding efficient real time field programmable gate array FPGA implementations FPGAs or generic all

programmable devices will soon become widespread serving as the brains of all types of real time smart signal processing systems like smart networks smart homes and smart cities The book examines digital convolution by bringing together the following main elements the fundamental theory behind the mathematical formulae together with corresponding physical phenomena virtualized algorithm simulation together with benchmark real time FPGA implementations and detailed state of the art case studies on wireless applications including popular linear convolution in digital front ends DFEs nonlinear convolution in digital pre distortion DPD enabled high efficiency wireless RF transceivers and fast linear convolution in massive multiple input multiple output MIMO systems After reading this book students and professionals will be able to Understand digital convolution with inside out information discover what convolution is why it is important and how it works Enhance their FPGA design skills i e enhance their FPGA related prototyping capability with model based hands on examples Rapidly expand their digital signal processing DSP blocks to examine how to rapidly and efficiently create DSP functional blocks on a programmable FPGA chip as a reusable intellectual property IP core Upgrade their expertise as both thinkers and doers minimize close the gap between mathematical equations and FPGA implementations for existing and emerging wireless applications **Information Security and Cryptology** Feng Bao,Moti Yung,Dongdai Lin,Jiwu Jing,2010-11-16 This book constitutes the thoroughly refereed post conference proceedings of the 4th International Conference on Information Security and Cryptology Inscrypt 2009 held in Beijing China in December 2009 The 22 revised full papers and 10 short papers presented were carefully reviewed and selected from 147 submissions The papers are organized in topical sections on cryptanalysis signature and signcryption key exchange private computations cipher design and analysis public key cryptography network and system security hardware security and web security *Aeronautics and Astronautics* Max Mulder,2011-09-12 In its first centennial aerospace has matured from a pioneering activity to an indispensable enabler of our daily life activities In the next twenty to thirty years aerospace will face a tremendous challenge the development of flying objects that do not depend on fossil fuels The twenty three chapters in this book capture some of the new technologies and methods that are currently being developed to enable sustainable air transport and space flight It clearly illustrates the multi disciplinary character of aerospace engineering and the fact that the challenges of air transportation and space missions continue to call for the most innovative solutions and daring concepts **Control Applications in Modern Power Systems** Arvind Kumar Prajapati,Manoj Tripathy,Asheesh K. Singh,Vijay K. Sood,Om P. Malik,2025-03-24 The book titled Control Applications in Modern Power System select proceedings of EPREC 2024 delves into in depth discussions case studies and recent advancements within the burgeoning field of control systems It specifically focuses on areas such as load frequency control wide area monitoring control and instrumentation optimization intelligent control energy management systems and SCADA systems The development of effective control strategies plays a pivotal role in managing reactive power and upholding voltage profiles among other critical aspects Readers stand to gain valuable insights bolstering their

knowledge and expertise in these domains Furthermore this book has the potential to inspire fresh and innovative ideas Whether a newcomer a researcher or a seasoned professional this book serves as an invaluable reference for all for staying abreast of the latest developments in control systems *High Performance Embedded Computing Handbook* David R. Martinez,Robert A. Bond,M. Michael Vai,2018-10-03 Over the past several decades applications permeated by advances in digital signal processing have undergone unprecedented growth in capabilities The editors and authors of High Performance Embedded Computing Handbook A Systems Perspective have been significant contributors to this field and the principles and techniques presented in the handbook are reinforced by examples drawn from their work The chapters cover system components found in today s HPEC systems by addressing design trade offs implementation options and techniques of the trade then solidifying the concepts with specific HPEC system examples This approach provides a more valuable learning tool Because readers learn about these subject areas through factual implementation cases drawn from the contributing authors own experiences Discussions include Key subsystems and components Computational characteristics of high performance embedded algorithms and applications Front end real time processor technologies such as analog to digital conversion application specific integrated circuits field programmable gate arrays and intellectual property based design Programmable HPEC systems technology including interconnection fabrics parallel and distributed processing performance metrics and software architecture and automatic code parallelization and optimization Examples of complex HPEC systems representative of actual prototype developments Application examples including radar communications electro optical and sonar applications The handbook is organized around a canonical framework that helps readers navigate through the chapters and it concludes with a discussion of future trends in HPEC systems The material is covered at a level suitable for practicing engineers and HPEC computational practitioners and is easily adaptable to their own implementation requirements *Smart Cameras* Ahmed Nabil Belbachir,2009-10-20 A smart camera is an integrated machine vision system which in addition to image capture circuitry includes a processor which can extract information from images without need for an external processing unit and interface devices used to make results available to other devices This book provides content on smart cameras for an interdisciplinary audience of professionals and students in embedded systems image processing and camera technology It serves as a self contained single source reference for material otherwise found only in sources such as conference proceedings journal articles or product data sheets Coverage includes the 50 year chronology of smart cameras their technical evolution the state of the art and numerous applications such as surveillance and monitoring robotics and transportation *Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology* Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data

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