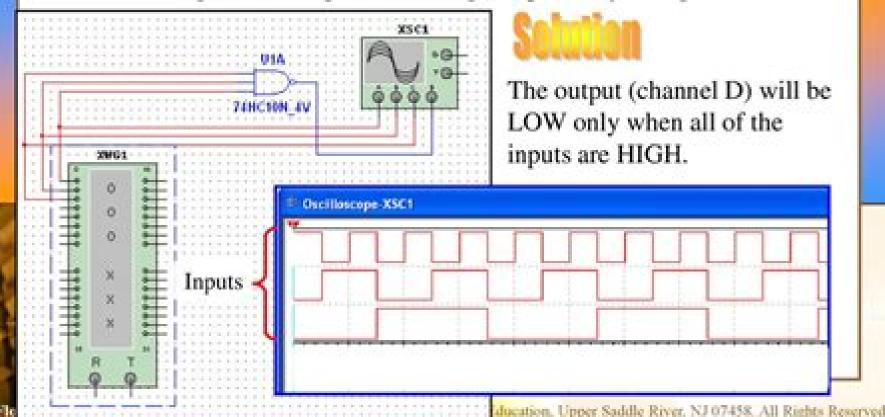
Summary

The NAND Gate



A Multisim circuit is shown. XWG1 is a word generator set in the count up mode. A four-channel oscilloscope monitors the inputs and output. What output signal do you expect to see?



Floyd Multisim Only For Digital Fundamentals

Thomas L. Floyd

Floyd Multisim Only For Digital Fundamentals:

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