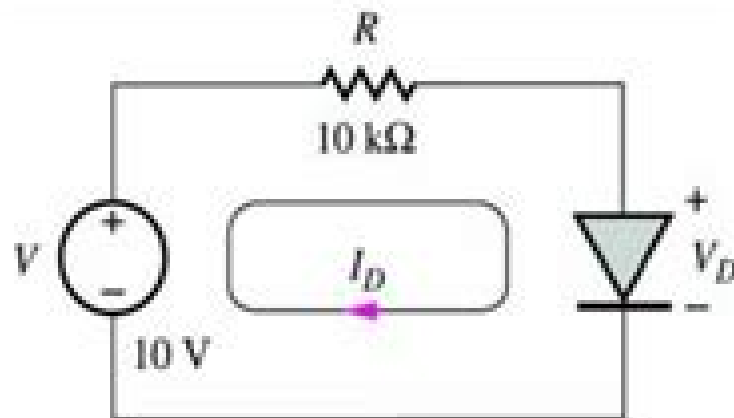


Diode Circuit Analysis: Basics



V and R may represent the Thévenin equivalent of a more complex 2-terminal network. The objective of diode circuit analysis is to find the **quiescent operating point** for the diode.

Q-Point = (I_D, V_D)

The loop equation for the diode circuit is:

$$V - I_D R - V_D = 0$$

This is also called the **load line** for the diode. The solution to this equation can be found by:

- Graphical analysis using the load-line method.
- Analysis with the diode's mathematical model.
- Simplified analysis with the ideal diode model.
- Simplified analysis using the constant voltage drop (CVD) model.

Diode Circuit Analysis

Abraham Pallas



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Electronic Circuit Analysis using LTSpice XVII Simulator Pooja Mohindru,Pankaj Mohindru,2021-08-18 This text discusses simulation process for circuits including clamper voltage and current divider transformer modeling transistor as an amplifier transistor as a switch MOSFET modeling RC and LC filters step and impulse response to RL and RC circuits amplitude modulator in a step by step manner for more clarity and understanding to the readers It covers electronic circuits like rectifiers RC filters transistor as an amplifier operational amplifiers pulse response to a series RC circuit time domain simulation with a triangular input signal and modulation in detail The text presents issues that occur in practical implementation of various electronic circuits and assist the readers in finding solutions to those issues using the software Aimed at undergraduate graduate students and academic researchers in the areas including electrical and electronics and communications engineering this book Discusses

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