# Question Paper Code: 91454



#### B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2019 Fourth/Fifth Semester

Electronics and Communication Engineering

EC 6504 – MICROPROCESSOR AND MICROCONTROLLER (Common to Information Technology, Medical Electronics/Bio Medical Engineering/Computer Science and Engineering)

(Regulations 2013)

(Also Common to PTEC6504 – Microprocessor and Microcontroller for B.E. (Part-Time) for Computer Science and Engineering, Electronics and Communication Engineering – Third/Fourth Semester – Regulations – 2014)

Time: Three Hours

Maximum: 100 Marks

#### Answer ALL questions

#### PART - A

(10×2=20 Marks)

- Calculate the physical address, when segment address is 1085 H and effective address is 4537 H.
- Show how the 2 byte INT instruction can be applied for debugging.
- 3. Differentiate External verses internal Bus.
- Compare closely coupled and loosely coupled configurations.
- 5. What are the handshake signals used in Mode 2 configuration of 8255 ?
- 6. How the DMA operation performed with 8086?

(OR)

- Draw the pin diagram of 8051.
- 8. What is the significance of EA pin?
- 9. Differentiate between timers and counters. Draw the diagram of TCON in 8051.
- Which register is used for serial programming in 8051 ? Illustrate it.

#### PART - B

(5×13=65 Marks)

a) Draw the architecture and explain the functional units of 8086.

(13)

b) Describe the interrupts of 8086 and its types with service routine.

(13)

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## Table of Contents Microprocessor And Its Applications Anna University Question Paper

- 1. Understanding the eBook Microprocessor And Its Applications Anna University Question Paper
  - The Rise of Digital Reading Microprocessor And Its Applications Anna University Question Paper
  - Advantages of eBooks Over Traditional Books
- 2. Identifying Microprocessor And Its Applications Anna University Question Paper
  - Exploring Different Genres
  - o Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
- 3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Microprocessor And Its Applications Anna University Question Paper
  - User-Friendly Interface
- 4. Exploring eBook Recommendations from Microprocessor And Its Applications Anna University Question Paper
  - Personalized Recommendations

- Microprocessor And Its Applications Anna University Question Paper User Reviews and Ratings
- Microprocessor And Its Applications Anna University Question Paper and Bestseller Lists
- 5. Accessing Microprocessor And Its Applications Anna University Question Paper Free and Paid eBooks
  - Microprocessor And Its Applications Anna University Question Paper Public Domain eBooks
  - Microprocessor And Its Applications Anna University Question Paper eBook Subscription Services
  - Microprocessor And Its Applications Anna University Question Paper Budget-Friendly Options
- 6. Navigating Microprocessor And Its Applications Anna University Question Paper eBook Formats
  - ∘ ePub, PDF, MOBI, and More
  - Microprocessor And Its Applications Anna University Question Paper Compatibility with Devices
  - Microprocessor And Its Applications Anna University Question Paper Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Microprocessor And Its Applications Anna University Question Paper
  - Highlighting and Note-Taking Microprocessor And Its Applications Anna University Question Paper
  - Interactive Elements Microprocessor And Its Applications Anna University Question Paper
- 8. Staying Engaged with Microprocessor And Its Applications Anna University Question Paper
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - o Following Authors and Publishers Microprocessor And Its Applications Anna University Question Paper
- 9. Balancing eBooks and Physical Books Microprocessor And Its Applications Anna University Question Paper
  - ∘ Benefits of a Digital Library
  - o Creating a Diverse Reading Collection Microprocessor And Its Applications Anna University Question Paper
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Microprocessor And Its Applications Anna University Question Paper
  - Setting Reading Goals Microprocessor And Its Applications Anna University Question Paper
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Microprocessor And Its Applications Anna University Question Paper
  - Fact-Checking eBook Content of Microprocessor And Its Applications Anna University Question Paper

- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
- 14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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