

**Fundamentals
of
Semiconductor
Theory
and
Device
Physics**

SHYH WANG

SHYH WANG

Fundamentals Of Semiconductor Theory And Device Physics Prentice Hall Series In Electrical And Computer Engineering

Arthur James Wells



Fundamentals Of Semiconductor Theory And Device Physics Prentice Hall Series In Electrical And Computer Engineering:

Fundamentals of Semiconductor Theory and Device Physics Shyh Wang, 1989-01-01 **Fundamentals Of Electronic Materials And Devices: A Gentle Introduction To The Quantum-classical World** Avik Ghosh, 2023-02-02 The Romans built enduring bridges well before Newton came along armed simply with a working knowledge of mechanics and materials. In contrast today's bridge building is an elaborate enterprise involving CAD tools, composite materials and acoustic imaging. When technology is pushed to its limits a working knowledge proves inadequate and an in depth understanding of core physical principles both macroscopic and microscopic top down vs bottom up becomes essential. We find ourselves today at a similar crossroad in semiconductor device technology where a working knowledge of solid state electronics is no longer enough. Faced with the prohibitive cost of computing and the slowdown of chip manufacturing device scaling and the global supply chain the semiconductor industry is forced to explore alternate platforms such as 2 D materials, spintronics, analog processing and quantum engineering. This book combines top down classical device physics with bottom up quantum transport in a single venue to provide the basis for such a scientific exploration. It is essential easy reading for beginning undergraduate and practicing graduate students, physicists unfamiliar with device engineering and engineers untrained in quantum physics. With just a modest pre requisite of freshman maths the book works quickly through key concepts in quantum physics, Matlab exercises and original homeworks to cover a wide range of topics from chemical bonding to Hofstadter butterflies, domain walls to Chern insulators, solar cells to photodiodes, FinFETs to Majorana fermions. For the practicing device engineer it provides new concepts such as the quantum of resistance while for the practicing quantum physicist it provides new contexts such as the tunnel transistor. **Proceedings**, 1996 New Technical Books New York Public Library, 1989 *Analytical and Compact Models (BSIM3v3) for Deep Submicron CMOS* Kai Chen, 1998 *The Physics of Information Technology* Neil Gershenfeld, 2000-10-16 The Physics of Information Technology explores the familiar devices that we use to collect, transform, transmit and interact with electronic information. Many such devices operate surprisingly close to very many fundamental physical limits. Understanding how such devices work and how they can and cannot be improved requires deep insight into the character of physical law as well as engineering practice. The book starts with an introduction to units, forces and the probabilistic foundations of noise and signalling, then progresses through the electromagnetics of wired and wireless communications and the quantum mechanics of electronic, optical and magnetic materials to discussions of mechanisms for computation, storage, sensing and display. This self contained volume will help both physical scientists and computer scientists see beyond the conventional division between hardware and software to understand the implications of physical theory for information manipulation. **Fundamentals of Semiconductor Theory and Device Physics** Wang, 2009-07-07 **Digital MOS Integrated Circuits** Niantzu Wang, 1989 Very Good No

Highlights or Markup all pages are intact Books in Series ,1985 Vols for 1980 issued in three parts Series Authors and Titles Electric Machinery and Transformers Irving L. Kosow,1972 *The Froehlich/Kent Encyclopedia of Telecommunications* Fritz E. Froehlich,Allen Kent,2021-07-28 Introduction to Computer Networking to Methods for Usability Engineering in Equipment Design *FinFET Devices for VLSI Circuits and Systems* Samar K. Saha,2020-07-15 To surmount the continuous scaling challenges of MOSFET devices FinFETs have emerged as the real alternative for use as the next generation device for IC fabrication technology The objective of this book is to provide the basic theory and operating principles of FinFET devices and technology an overview of FinFET device architecture and manufacturing processes and detailed formulation of FinFET electrostatic and dynamic device characteristics for IC design and manufacturing Thus this book caters to practicing engineers transitioning to FinFET technology and prepares the next generation of device engineers and academic experts on mainstream device technology at the nanometer nodes **The British National Bibliography** Arthur James Wells,2006 Books for College Libraries: Psychology, science, technology, bibliography ,1988 **Compact Models for Integrated Circuit Design** Samar K. Saha,2018-09-03 Compact Models for Integrated Circuit Design Conventional Transistors and Beyond provides a modern treatise on compact models for circuit computer aided design CAD Written by an author with more than 25 years of industry experience in semiconductor processes devices and circuit CAD and more than 10 years of academic experience in teaching compact modeling courses this first of its kind book on compact SPICE models for very large scale integrated VLSI chip design offers a balanced presentation of compact modeling crucial for addressing current modeling challenges and understanding new models for emerging devices Starting from basic semiconductor physics and covering state of the art device regimes from conventional micron to nanometer this text Presents industry standard models for bipolar junction transistors BJTs metal oxide semiconductor MOS field effect transistors FETs FinFETs and tunnel field effect transistors TFETs along with statistical MOS models Discusses the major issue of process variability which severely impacts device and circuit performance in advanced technologies and requires statistical compact models Promotes further research of the evolution and development of compact models for VLSI circuit design and analysis Supplies fundamental and practical knowledge necessary for efficient integrated circuit IC design using nanoscale devices Includes exercise problems at the end of each chapter and extensive references at the end of the book Compact Models for Integrated Circuit Design Conventional Transistors and Beyond is intended for senior undergraduate and graduate courses in electrical and electronics engineering as well as for researchers and practitioners working in the area of electron devices However even those unfamiliar with semiconductor physics gain a solid grasp of compact modeling concepts from this book **Control of Electric Machines** Irving L. Kosow,1973 **An Introduction to Condensed Matter Physics for the Nanosciences** Arthur McGurn,2023-04-13 The book provides an accessible introduction to the principles of condensed matter physics with a focus on the nanosciences and device technologies The basics of electronic phononic photonic

superconducting optics quantum optics and magnetic properties are explored and nanoscience and device materials are incorporated throughout the chapters Many examples of the fundamental principles of condensed matter physics are taken directly from nanoscience and device applications This book requires a background in electrodynamics quantum mechanics and statistical mechanics at the undergraduate level It will be a valuable reference for advanced undergraduates and graduate students of physics engineering and applied mathematics Features Contains discussions of the basic principles of quantum optics and its importance to lasers quantum information and quantum computation Provides references and a further reading list to additional scientific literature so that readers can use the book as a starting point to then follow up with a more advanced treatment of the topics covered Requires only a basic background in undergraduate electrodynamics quantum mechanics and statistical mechanics

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Milnes, 2012-12-06 For some time there has been a need for a semiconductor device book that carries diode and transistor theory beyond an introductory level and yet has space to touch on a wider range of semiconductor device principles and applications Such topics are covered in specialized monographs numbering many hundreds but the voluminous nature of this literature limits access for students This book is the outcome of attempts to develop a broad course on devices and integrated electronics for university students at about senior year level The educational prerequisites are an introductory course in semiconductor junction and transistor concepts and a course on analog and digital circuits that has introduced the concepts of rectification amplification oscillators modulation and logic and Switching circuits The book should also be of value to professional engineers and physicists because of both the information included and the detailed guide to the literature given by the references The aim has been to bring some measure of order into the subject area examined and to provide a basic structure from which teachers may develop themes that are of most interest to students and themselves Semiconductor devices and integrated circuits are reviewed and fundamental factors that control power levels frequency speed size and cost are discussed The text also briefly mentions how devices are used and presents circuits and comments on representative applications Thus the book seeks a balance between the extremes of device physics and circuit design

MOSFET Models for VLSI Circuit Simulation Narain D. Arora, 2012-12-06 Metal Oxide Semiconductor MOS transistors are the basic building block of MOS integrated circuits IC Very Large Scale Integrated VLSI circuits using MOS technology have emerged as the dominant technology in the semiconductor industry Over the past decade the complexity of MOS ICs has increased at an astonishing rate This is realized mainly through the reduction of MOS transistor dimensions in addition to the improvements in processing Today VLSI circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0.5 microns are in volume production Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator This

problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models. There is extensive literature available on modeling these short channel devices. However, there is a lot of confusion too. Often it is not clear what model to use and which model parameter values are important and how to determine them. After working over 15 years in the field of semiconductor device modeling, I have felt the need for a book which can fill the gap between the theory and the practice of MOS transistor modeling. This book is an attempt in that direction. **Choice**, 1984

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