

A high-magnification scanning electron micrograph (SEM) of a semiconductor device, likely a microprocessor or memory array. The image shows a complex network of gold-colored metal interconnects and pads on a dark substrate. A black rectangular title box with a thin white border is positioned in the upper right quadrant, containing the title text in a stylized, orange-yellow font. Below the title, the authors' names are printed in a smaller, white, sans-serif font. The background features a dense grid of rectangular structures, possibly memory cells or logic gates, with various sized metal contacts and lines connecting them.

# Fundamentals of Semiconductor Devices

Betty Lise Anderson • Richard L. Anderson

# Fundamentals Of Semiconductor Devices

**B. Jayant Baliga**



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