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Question Paper Code : 91454

B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2019
Fourth/Fifth Semester
Electronics and Communication Engineering
EC 6504 – MICROPROCESSOR AND MICROCONTROLLER
(Common to Information Technology, Medical Electronics/Bio Medical Engineering/Computer Science and Engineering)
(Regulations 2013)
(Also Common to PTEC6504 – Microprocessor and Microcontroller for B.E. (Part-Time) for Computer Science and Engineering, Electronics and Communication Engineering – Third/Fourth Semester – Regulations – 2014)

Time : Three Hours | Maximum : 100 Marks

Answer ALL questions

PART – A (10×2=20 Marks)

1. Calculate the physical address, when segment address is 1085 H and effective address is 4537 H.
2. Show how the 2 byte INT instruction can be applied for debugging.
3. Differentiate External verses internal Bus.
4. Compare closely coupled and loosely coupled configurations.
5. What are the handshake signals used in Mode – 2 configuration of 8255 ?
6. How the DMA operation performed with 8086 ?
7. Draw the pin diagram of 8051.
8. What is the significance of EA pin ?
9. Differentiate between timers and counters. Draw the diagram of TCON in 8051.
10. Which register is used for serial programming in 8051 ? Illustrate it.

PART – B (5×13=65 Marks)

11. a) Draw the architecture and explain the functional units of 8086. (13)

(OR)

b) Describe the interrupts of 8086 and its types with service routine. (13)

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Michael Seilmaier

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