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Fourth Semester B.E. Degree Examination, Dec.2016/Jan.2017 Microprocessors

Time: 3 hrs.

Max. Marks:100

2.30	mer :	이 주변하다.	CARL PRODUCT IN NOTICE
		Note: Answer FIVE full questions, selecting at least TWO questions from each part.	
		PART - A	
1	n.	Discuss the development of Intel 86 family of microprocessors. Briefly is additional features introduced at each stage of development from 8086 to Pentium	i IV. (96 Marks)
	b.	Explain with a near sketch the memory map of a personal computer system. With a neat sketch explain architecture of 8086.	(96 Marks) (98 Marks)
2	m.	Discuss the following Addressing modes of 8086 with example. i) Register indirect ii) Immediate iii) Base plus index. What are the different program memory addressing modes? Explain with example	(06 Marks)
	b.	Calculate the physical address for the following instructions. Assume	Colon barren scale
		DS = 1000H, SS = 7000H, ES = 4000H, BP = 0100H, SI = 0020H, DI BX = 0700H, Values = 0500H. i) MOV AX, [BX] [SI] ii) ADD AL, [BP + 40H] iii) MOV CX, Values [BX] [DI] iv) MOV ES : [1000H], 20H.	- 0200H,
3	a.	Explain the following assembler directives with example. i) ASSUME ii) PUBLIC AND EXTRN	
	b	iii) MACRO AND ENDM iv) MODEL. Write the instruction template (format) for the following instructions.	(10 Marks)
	c	i) MOV AX, DX ii) MOV DX, [BP] 0200H iii) MOV AL, [BX] [DI] What is meant by segment override prefix? Explain with an illustration.	(06 Marks) (04 Marks)
4	a.	Explain the working of following 8086 instructions. i) DAA ii) IMUL iii) REPE CMPSB iv) LOOP.	(08 Marks)
ъ.	Differentiate between 'short', 'near' and 'far' jump instruction with example. Explain with an example, how parameters can be passed to subroutine, using stac	(06 Marks) k.	
			(06 Marks)
		PART - B	
5	20	Differentiate between 'Macros' and Procedures' with an example for each.	(08 Marks)
	b.	Write an ALP to compute the factorial of a given 8-bit number using recursion.	(06 Marks)
	0.,	Write an ALP to sort a given set of N numbers in ascending order using bubble at	(06 Marks)
6	80.0	Illustrate with a neat diagram, the working of 8086 in minimum mode.	(10 Marks)
	b.	Explain the memory read bus cycle of 8086 in minimum mode with a neat diagra	m. (10 Marks)
7	0.	Interface four 8KB RAMs starting with an address of 40000H using 3:8 Deco	
	-	mention the decoding logic and memory map.	(10 Marks) (06 Marks)
	e.	Differentiate between memory mapped I/O and I/O mapped I/O. Write a note on Interrupt driven I/O.	(04 Marks)
8	a.	With a neat sketch explain the functioning of 8255 PPI.	(10 Marks)
		Discuss the control word format of 8255 PPI with a sketch.	(10 Marks)

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