

**Fourth Semester B.E. Degree Examination, Dec.2017/Jan.2018**  
**Microprocessor and Microcontroller**

Time: 3 hrs.

Max. Marks: 80

*Note: Answer any FIVE full questions, choosing one full question from each module.*

**Module-1**

1.
  - a. Explain the architecture of 8086 microprocessor with a neat diagram along with functions of various blocks. (06 Marks)
  - b. With an example distinguish between physical address, logical address and offset address. If CS = 2000 h, DS = 3000 h, SS = 4000 h, ES = 5000 h, BX = 0020 h, BP = 0030 h. Find physical address for (i) MOV AL, [BP] (ii) MOV CX, [BX]. (04 Marks)
  - c. Explain the following addressing modes of 8086:
    - i) Register indirect
    - ii) Based indexed indirect
    - iii) Direct memory. (06 Marks)

**OR**

2.
  - a. What are assembler directives? Explain the following assembler directives (i) PROC, (ii) Assume, (iii) PTR. (04 Marks)
  - b. Write assembly language program to add 5 bytes of data stored in data segment. (04 Marks)
  - c. With syntax, explain the following control transfer instructions:
    - i) Conditional transfer
    - ii) Unconditional transfer instruction. (08 Marks)

**Module-2**

3.
  - a. Explain the syntax of following instructions with an example: i) DAA ii) MUL iii) AND iv) SHR v) CMP vi) AAM (06 Marks)
  - b. Write a program to convert lower case to upper case by reading string from KB and print the converted string at 10<sup>th</sup> row, 20<sup>th</sup> column after clearing the screen. (06 Marks)
  - c. Write an ALP to count the number of one's and zero's in a given 8 bit data using rotate instructions. (04 Marks)

**OR**

4.
  - a. Explain the syntax of following instructions with example: i) AAA, ii) Shl, iii) DIV, iv) RCR. (04 Marks)
  - b. What is an interrupt? Explain various types with an interrupt vector table. (06 Marks)
  - c. Write an ALP to sort a given set of 16 bit numbers in ascending order using any sorting method. (06 Marks)

**Module-3**

5.
  - a. With an example, explain how to identify over flow and under flow using flags in a flag register for performing arithmetic operation on 16 bit number. (06 Marks)
  - b. Write the syntax of following instruction and explain with an example: (i) CBW, (ii) IDIV, (iii) CMPSB, (iv) Xlat. (04 Marks)
  - c. Design a memory system for 8086 with one 64 KB RAM and one 64 KB ROM at address 30000h and F0000h show the complete design along with memory mapping and draw the final diagram with address decoder. (06 Marks)

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**G.T. Swamy**



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Manual G.T. Swamy, 2006

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The book is written for an undergraduate course on the 8086 microprocessor and 8051 microcontroller It provides comprehensive coverage of the hardware and software aspects of 8086 microprocessor and 8051 microcontroller The book is divided into three parts The first part focuses on 8086 microprocessor It teaches you the 8086 architecture instruction set Assembly Language Programming ALP interfacing 8086 with support chips memory and peripherals such as 8251 8253 8255 8259 8237 and 8279 It also explains the interfacing of 8086 with data converters ADC and DAC and introduces a traffic light control system The second part focuses on multiprogramming and multiprocessor configurations numeric processor 8087 I O processor 8089 and introduces features of advanced processors such as 80286 80386 80486 and Pentium processors The third part focuses on 8051 microcontroller It teaches you the 8051 architecture instruction set programming 8051 and interfacing 8051 with external memory It explains timers counters serial port interrupts of 8051 and their programming It also describes the interfacing 8051 with data converters ADC and DAC keyboards LCDs LEDs stepper motors and sensors

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