



Council for Technical Education and Vocational Training

Office of the Controller of Examinations

Sanothimi, Bhaktapur

Regular/Back Exam – 2081 Bhadra/Ashwin

Program:	Diploma in Computer Engineering/ Information Technology	Full Marks: 80
Year/Part:	II/II (2022) © Arjun	Pass Marks: 32
Subject:	Microprocessor and Computer Architecture	Time: 3 hrs.

Candidates are required to give their answers in their own words as far as practicable. The figures in the margin indicate full marks.

Attempt any **TEN** questions.



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1. Define bus. What are the different types of bus in microprocessor? Explain them. [2+6]
2. What is addressing mode? Explain the addressing modes of 8085 with suitable example. [2+6]
3. a. WAP to add the contents of memory location 4500H and 4501H and store the sum at memory location 4502H. [4]
b. WAP to find the 2's complement of the number stored at memory location 4200H and store the complemented number at memory location 4300H. [4]
4. Write an assembly language to multiply two 8 bit numbers 8EH and 2FH. Store the result in memory location C000H and C001H. Also, exchange the result to register C to register B from the accumulator. [8]
5. What are the limitations of 8085-microprocessor? Draw the timing diagram of 8085 OP code fetch cycle. [4+4]
6. Define computer architecture and computer organization. Explain memory hierarchy with a diagram. [8]
7. Differentiate between RISC and CISC. [8]
8. What are different modes of data transfer in computer architecture? Explain. [8]

Cont.

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Table of Contents Diploma 4th Sem Exam Papers Of Microprocessor

1. Understanding the eBook Diploma 4th Sem Exam Papers Of Microprocessor
 - The Rise of Digital Reading Diploma 4th Sem Exam Papers Of Microprocessor
 - Advantages of eBooks Over Traditional Books
2. Identifying Diploma 4th Sem Exam Papers Of Microprocessor
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Diploma 4th Sem Exam Papers Of Microprocessor
 - User-Friendly Interface
4. Exploring eBook Recommendations from Diploma 4th Sem Exam Papers Of Microprocessor
 - Personalized Recommendations
 - Diploma 4th Sem Exam Papers Of Microprocessor User Reviews and Ratings
 - Diploma 4th Sem Exam Papers Of Microprocessor and Bestseller Lists
5. Accessing Diploma 4th Sem Exam Papers Of Microprocessor Free and Paid eBooks
 - Diploma 4th Sem Exam Papers Of Microprocessor Public Domain eBooks
 - Diploma 4th Sem Exam Papers Of Microprocessor eBook Subscription Services
 - Diploma 4th Sem Exam Papers Of Microprocessor Budget-Friendly Options
6. Navigating Diploma 4th Sem Exam Papers Of Microprocessor eBook Formats
 - ePub, PDF, MOBI, and More
 - Diploma 4th Sem Exam Papers Of Microprocessor Compatibility with Devices
 - Diploma 4th Sem Exam Papers Of Microprocessor Enhanced eBook Features
7. Enhancing Your Reading Experience

- Adjustable Fonts and Text Sizes of Diploma 4th Sem Exam Papers Of Microprocessor
- Highlighting and Note-Taking Diploma 4th Sem Exam Papers Of Microprocessor
- Interactive Elements Diploma 4th Sem Exam Papers Of Microprocessor
- 8. Staying Engaged with Diploma 4th Sem Exam Papers Of Microprocessor
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Diploma 4th Sem Exam Papers Of Microprocessor
- 9. Balancing eBooks and Physical Books Diploma 4th Sem Exam Papers Of Microprocessor
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Diploma 4th Sem Exam Papers Of Microprocessor
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Diploma 4th Sem Exam Papers Of Microprocessor
 - Setting Reading Goals Diploma 4th Sem Exam Papers Of Microprocessor
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Diploma 4th Sem Exam Papers Of Microprocessor
 - Fact-Checking eBook Content of Diploma 4th Sem Exam Papers Of Microprocessor
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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