

The background of the slide is a photograph of a circuit board, likely a printed circuit board (PCB), with various components and traces visible. A large, semi-transparent blue rectangular area covers the right side of the image, serving as a backdrop for the title and authors' names.

DIGITAL INTEGRATED CIRCUITS

THOMAS A. DEMASSA
ZACK CICCONE

Digital Integrated Circuits By Thomas A Demassa

J Spring



Digital Integrated Circuits By Thomas A Demassa:

Digital Integrated Circuits Thomas A. DeMassa, Zack Ciccone, 1996 Contains the most extensive coverage of digital integrated circuits available in a single source Provides complete qualitative descriptions of circuit operation followed by in depth analytical analyses and spice simulations The circuit families described in detail are transistor transistor logic TTL STTL and ASTTL emitter coupled logic ECL NMOS logic CMOS logic dynamic CMOS BiCMOS structures and various GASFET technologies In addition to detailed presentation of the basic inverter circuits for each digital logic family complete details of other logic circuits for these families are presented **Digital Integrated Circuits** DeMassa, 1996-02-01 **Low Power Interconnect Design** Sandeep Saini, 2015-06-12 This book provides practical solutions for delay and power reduction for on chip interconnects and buses It provides an in depth description of the problem of signal delay and extra power consumption possible solutions for delay and glitch removal while considering the power reduction of the total system Coverage focuses on use of the Schmitt Trigger as an alternative approach to buffer insertion for delay and power reduction in VLSI interconnects In the last section of the book various bus coding techniques are discussed to minimize delay and power in address and data buses **Bowker's Complete Video Directory**, 2000 *Photonic Materials, Devices, and Applications*, 2005 **Mikroelektronik** Thomas Tille, Doris Schmitt-Landsiedel, 2006-05-06 Das Buch beinhaltet die wichtigsten Themengebiete der Mikroelektronik Der fachlich fundierte und didaktisch sehr gut dargestellte Inhalt ermöglicht das Verständnis für den Aufbau und die Funktionsweise elektronischer Bauelemente der Mikroelektronik und deren Einsatz in schaltungstechnischen Anwendungen Die Verknüpfung physikalischer Halbleitereffekte mit der Funktionsweise elektronischer Bauelemente und integrierter Schaltungen stellt ein Hauptmerkmal des Buches dar Ein weiteres prägnantes Merkmal ist die Vielfalt und Beschreibungsweise der behandelten elektronischen Halbleiterbauelemente wodurch ein umfassendes Bild des Stoffgebietes geliefert wird **The British National Bibliography** Arthur James Wells, 1998 *ASEE Prism*, 1994 *American Book Publishing Record*, 1996 **Bowker's Complete Video Directory 2001**, 2001 *Engineering Education*, 1990 *Forthcoming Books* Rose Army, 1996-06 *Energy Efficient and Reliable Embedded Nanoscale SRAM Design* Bhupendra Singh Reniwal, Pooran Singh, Ambika Prasad Shah, Santosh Kumar Vishvakarma, 2023-11-29 This reference text covers a wide spectrum for designing robust embedded memory and peripheral circuitry It will serve as a useful text for senior undergraduate and graduate students and professionals in areas including electronics and communications engineering electrical engineering mechanical engineering and aerospace engineering Discusses low power design methodologies for static random access memory SRAM Covers radiation hardened SRAM design for aerospace applications Focuses on various reliability issues that are faced by submicron technologies Exhibits more stable memory topologies Nanoscale technologies unveiled significant challenges to the design of energy efficient and reliable SRAMs This reference text investigates the impact of process variation leakage aging soft errors and related

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