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Transactions on High-Performance Embedded Architectures and Compilers IV Per Stenström, 2011-11-15

Transactions on HiPEAC aims at the timely dissemination of research contributions in computer architecture and compilation methods for high performance embedded computer systems. Recognizing the convergence of embedded and general purpose computer systems, this journal publishes original research on systems targeted at specific computing tasks as well as systems with broad application bases. The scope of the journal therefore covers all aspects of computer architecture, code generation, and compiler optimization methods of interest to researchers and practitioners designing future embedded systems. This 4th issue contains 21 papers carefully reviewed and selected out of numerous submissions and is divided in four sections. The first section contains five regular papers. The second section consists of the top four papers from the 4th International Conference on High Performance Embedded Architectures and Compilers HiPEAC 2009 held in Paphos, Cyprus, in January 2009. The third section contains a set of six papers providing a snapshot from the Workshop on Software and Hardware Challenges of Manycore Platforms SHCMP 2008 held in Beijing, China, in June 2008. The fourth section consists of six papers from the 8th IEEE International Symposium on Systems Architectures, Modeling and Simulation SAMOS VIII 2008 held in Samos, Greece, in July 2008.

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Official Gazette of the United States Patent and Trademark Office United States. Patent and Trademark Office,1991 **Modeling, Analysis and Optimization of Network-on-Chip Communication Architectures** Umit Y. Ogras,Radu Marculescu,2013-03-12

Traditionally design space exploration for Systems on Chip SoCs has focused on the computational aspects of the problem at hand However as the number of components on a single chip and their performance continue to increase the communication architecture plays a major role in the area performance and energy consumption of the overall system As a result a shift from computation based to communication based design becomes mandatory Towards this end network on chip NoC communication architectures have emerged recently as a promising alternative to classical bus and point to point communication architectures In this dissertation we study outstanding research problems related to modeling analysis and optimization of NoC communication architectures More precisely we present novel design methodologies software tools and FPGA prototypes to aid the design of application specific NoCs

Advanced Microprocessor and Peripherals Mr. Rohit Manglik,2023-06-23 Explores advanced microprocessor architectures interfacing techniques and peripheral integration for embedded system design

Error Control for Network-on-Chip Links Bo Fu,Paul Ampadu,2011-10-09 This book provides readers with a comprehensive review of the state of the art in error control for Network on Chip NOC links Coverage includes detailed description of key issues in NOC error control faced by circuit and system designers as well as practical error control techniques to minimize the impact of these errors on system performance

Microprocessors O. A. R.

Cornillie,2013-10-22 Microprocessors presents an overview of the state of the art in the field of microprocessors and illustrates with the aid of patents its utilization and application Organized into six parts the book begins with an introduction to the microprocessor microcomputer and software Parts I III focus on program control digital control and electrical motor control Subsequent parts show the medical applications measuring instruments and treatment of data in microprocessors

Highly Integrated Gate Drivers for Si and GaN Power Transistors Achim Seidel,Bernhard Wicht,2021-03-31 This book explores integrated gate drivers with emphasis on new gallium nitride GaN power transistors which offer fast switching along with minimum switching losses It serves as a comprehensive all in one source for gate driver IC design written in handbook style with systematic guidelines The authors cover the full range from fundamentals to implementation details including topics like power stages various kinds of gate drivers resonant non resonant current source voltage source gate drive schemes driver supply gate loop gate driver power efficiency and comparison silicon versus GaN transistors Solutions are presented on the system and circuit level for highly integrated gate drivers Coverage includes miniaturization by higher integration of subfunctions onto the IC buffer capacitors as well as more efficient switching by a multi level approach which also improves robustness in case of extremely fast switching transitions The discussion also includes a concept for robust operation in the highly relevant case that the gate driver is placed in distance to the power transistor All results are widely applicable to achieve highly compact energy efficient and cost effective power electronics solutions

Aachen Conference on Gear Production - Innovations in Gear Technology. 6th - 7th November 2024, Aachen Thomas Bergs,Christian Brecher,2024-12-12 Gears have long been indispensable components in wide range of industries including mechanical engineering automotive engineering and industrial gear manufacturing As a result the transmission technology industry is facing an increasing number of challenges in response to changing market demands In the modern gear transmission industry competition is not only determined by price but also by load carrying capacity operational reliability and noise excitation behavior In the automotive industry reliable transmissions with high power density low weight and minimal noise emissions are required The current trend towards e mobility as well as general ecological and economical challenges to improve resource efficiency lead to increased demands on the entire process chain in gear production The 2024 Aachen Conference on Gear Production ACGP jointly organized by the WZL at RWTH Aachen University and the Research Association for Drive Technology FVA will cover a range of gear production topics including gear design soft and hard machining process and quality control and gear operation The conference will also highlight Manufacturing X and OPC UA as key topics demonstrating how digitalization and system integration enhance flexibility and efficiency in gear production The spectrum of topics ranges from process and tool design in line with requirements to the manufacturing of individual gear geometries and measures for continuous quality assurance The discussion will also explore digitalization and strategies for improving sustainability in gear production and application

InfoWorld ,1998-07-06 InfoWorld is targeted to Senior IT

professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects

Foundations of Computer Technology Alexander John Anderson,2020-10-25 Foundations of Computer Technology is an easily accessible introduction to the architecture of computers and peripherals This textbook clearly and completely explains modern computer systems through an approach that integrates components systems software and design It provides a succinct systematic and readable guide to computers providing a springboard for students to pursue more detailed technology subjects This volume focuses on hardware elements within a computer system and the impact of software on its architecture It discusses practical aspects of computer organization structure behavior and design delivering the necessary fundamentals for electrical engineering and computer science students The book not only lists a wide range of terms but also explains the basic operations of components within a system aided by many detailed illustrations Material on modern technologies is combined with a historical perspective delivering a range of articles on hardware architecture and software programming methodologies and the nature of operating systems It also includes a unified treatment on the entire computing spectrum ranging from microcomputers to supercomputers Each section features learning objectives and chapter outlines Small glossary entries define technical terms and each chapter ends with an alphabetical list of key terms for reference and review Review questions also appear at the end of each chapter and project questions inspire readers to research beyond the text Short annotated bibliographies direct students to additional useful reading

Air Force Journal of Logistics ,1981

Technology Transfer in International Business Tamir Agmon,Mary Ann Young Von Glinow,1991 This is a book on the means by which technological knowledge is transferred from countries that develop it to those that need it but have not yet been able to develop it on their own The focus is on the transfer of technology from Western countries to Asian countries

Chip-Scale Power Supplies for DC-Link and Grid Applications Christoph Rindfleisch,Bernhard Wicht,2024-06-25 This book is a comprehensive single source on the design of chip scale high voltage power supplies for low power DC link and grid applications It is written in handbook style with systematic guidelines and includes implementation examples The authors cover the full range from technology fundamentals to circuit implementation details The book includes guidelines for the application specific selection of the converter topology design guidelines for the inductive components and a detailed description of low power optimized control approaches and subcircuits The authors also include guidelines for the selection and design of high voltage on chip power switches and for the reduction of parasitic effects such as capacitive losses

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Principles of Soldering Giles Humpston,David M. Jacobson,2004

Microprocessor Tech Max Publication Book Review: Unveiling the Magic of Language

In an electronic era where connections and knowledge reign supreme, the enchanting power of language has become much more apparent than ever. Its ability to stir emotions, provoke thought, and instigate transformation is truly remarkable. This extraordinary book, aptly titled "**Microprocessor Tech Max Publication**," written by a highly acclaimed author, immerses readers in a captivating exploration of the significance of language and its profound impact on our existence. Throughout this critique, we will delve into the book's central themes, evaluate its unique writing style, and assess its overall influence on its readership.

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