

1. A logic gate circuit with four inputs A, B, C and D and output F is shown below in Figure 1.

- (a) Determine the output (in terms of Boolean expressions) of *all four logic gates* in the circuit.  
 (b) Complete a truth table for the function F.  
 (c) From the truth table, write out a 'sum of products' expression for the function F.  
 (d) Plot the sum of products function F onto a Karnaugh Map.  
 (e) Directly from the Karnaugh map, write out the minimised function for F.

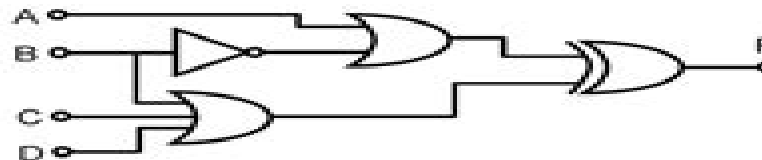


Figure 1

2. Simplify the Boolean expression  $F = A.B + C.( \bar{B} + A.B )$  as far as possible by first finding an equivalent 'sum of products expression' and subsequently utilising a Karnaugh Map.
3. (a) Determine the output (in terms of Boolean expressions) of *all five logic gates* in the circuit shown in Figure 2.

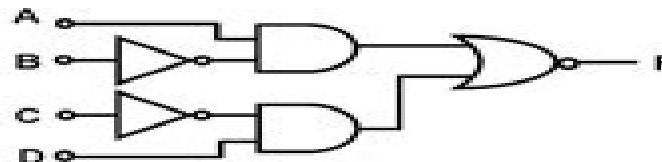


Figure 2

- (b) By utilising Boolean algebra, show that the function F obtained in part (a) above, is also equivalent to  $( \bar{A} + B ).( C + \bar{D} )$ .

# Digital Electronics Lab Exam Questions With Answers

**R Pring**



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### **Table of Contents Digital Electronics Lab Exam Questions With Answers**

1. Understanding the eBook Digital Electronics Lab Exam Questions With Answers
  - The Rise of Digital Reading Digital Electronics Lab Exam Questions With Answers
  - Advantages of eBooks Over Traditional Books
2. Identifying Digital Electronics Lab Exam Questions With Answers
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Digital Electronics Lab Exam Questions With Answers
  - User-Friendly Interface

4. Exploring eBook Recommendations from Digital Electronics Lab Exam Questions With Answers
  - Personalized Recommendations
  - Digital Electronics Lab Exam Questions With Answers User Reviews and Ratings
  - Digital Electronics Lab Exam Questions With Answers and Bestseller Lists
5. Accessing Digital Electronics Lab Exam Questions With Answers Free and Paid eBooks
  - Digital Electronics Lab Exam Questions With Answers Public Domain eBooks
  - Digital Electronics Lab Exam Questions With Answers eBook Subscription Services
  - Digital Electronics Lab Exam Questions With Answers Budget-Friendly Options
6. Navigating Digital Electronics Lab Exam Questions With Answers eBook Formats
  - ePub, PDF, MOBI, and More
  - Digital Electronics Lab Exam Questions With Answers Compatibility with Devices
  - Digital Electronics Lab Exam Questions With Answers Enhanced eBook Features
7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Digital Electronics Lab Exam Questions With Answers
  - Highlighting and Note-Taking Digital Electronics Lab Exam Questions With Answers
  - Interactive Elements Digital Electronics Lab Exam Questions With Answers
8. Staying Engaged with Digital Electronics Lab Exam Questions With Answers
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Digital Electronics Lab Exam Questions With Answers
9. Balancing eBooks and Physical Books Digital Electronics Lab Exam Questions With Answers
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Digital Electronics Lab Exam Questions With Answers
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
11. Cultivating a Reading Routine Digital Electronics Lab Exam Questions With Answers
  - Setting Reading Goals Digital Electronics Lab Exam Questions With Answers
  - Carving Out Dedicated Reading Time



12. Sourcing Reliable Information of Digital Electronics Lab Exam Questions With Answers
  - Fact-Checking eBook Content of Digital Electronics Lab Exam Questions With Answers
  - Distinguishing Credible Sources
13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
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