ARM Assembly Language Guide

ARM is an example of a Reduced Instruction Set Computer (RISC) which was designed for easy instruction pipelining. ARM has a "Load/Store" architecture since all instructions (other than the load and store instructions) must use register operands. ARM has 16 32-bit "general purpose" registers (r0, r1, r2, ..., r15), but some of these have special uses (see ARM Register Conventions table on page 4).

Common A	RM Instructions (and	psuedo-instructions)
Type of Instruction	ARM Assembly Language	Register Transfer Language Description
Memory Access (Load and Store)	LDR r4, Mem	[r4] ← [Mem]; Mem is a global variable label
	STR r4, Mem	[Mem] ← [r4]
	LDR r4, [r3]	[r4] ← [[r3]] ; register indirect
	STR r4, [r3, #4]	[[r3] + 4] ← [r4] ; register indirect with offset
Move	MOV r4, r2	[r4] ← [r2]
	MOV r4, #10	[r4] ← 10; 8-bit literal, but can be shifted
Load Address	ADR r4, Mem	[r4] ← load address of label Mem
Arithmetic Instruction (reg. operands only, except last operand can be an 8-bit integer)	ADD r4, r2, r3	$[r4] \leftarrow [r2] + [r3]$
	MUL r4, r2, r3	[r4] ← [r2] * [r3] (32-bit product)
	SUB r4, r2, r3	[r4] ← [r2] - [r3]
Compare (sets condition codes)	CMP r4, r2	Sets condition codes by r4 - r2
Conditional Branch	BGT LABEL (BGE, BLT, BLE, BEQ, BNE)	Branch to LABEL if r4 > r2 if it follow above CMP
Unconditional Branch	B LABEL	Always Branch to LABEL

A simple ARM assembly language program to sum the elements in an array A is given below: ; ARM Example that sums an array via the algorithm: I SUM = 0(uses r6 for sum) ; for I = 0 to LENGTH - 1 do (uses rl for I) SUM - SUM + ARRAY[I] (uses r3 for address of A[I]) ; end for AREA SUMARRAY, CODE, READONLY ENTRY ; Always needed to indicate where to start pgm LDR r2, LENGTH SUB r2, r2, #1 ; r2 contains (LENGTH-1) MOV r6, #0 MOV r1, #0 ; r6 sum set to 0 ; rl index I set to 0 FOR_INIT ADR 13, ARRAY ; start r3 with address of A[0] ; compare I and (LENGTH-1) FOR_CMP CMP rl, r2 ADD r6, r6, r4 ; update sum with A[I] ; increment I ADD r1, r1, #1 B FOR_CMP ; loop back to for-loop check END_FOR STR r6, SUM ; store result in SUM B STOP STOP AREA SUMARRAY, DATA, READWRITE ALTON 25 (1946) DCD OXFFFFFFF ARRAY DCD 5, 10, 15, 20, 30, 40, 50 DCD 7 LENGTH EMD ; Needed to stop assembly

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Karin Nielsen-Saines

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Arm Assembly Language - An Introduction (Second Edition) J. R. Gibson, 2011 An introductory text describing the ARM assembly language and its use for simple programming tasks

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