Council for Technical Education and Vocational Training

Office of the Controller of Examinations Sanothimi, Bhaktapur

Regular/Back Exam - 2081 Bhadra/Ashwin

Diploma in Computer Engineering/ Information Technology	Full Marks: 80
II/II (2022) © Arjun	Pass Marks: 32
Microprocessor and Computer Architecture	Time: 3 hrs.
	Information Technology II/II (2022) © Arjun Microprocessor and Computer

Candidates are required to give their answers in their own words as far as practicable. The figures in the margin indicate full marks.

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- Define bus. What are the different types of bus in [2+6]1. microprocessor? Explain them.
- What is addressing mode? Explain the addressing modes of [2+6]2. 8085 with suitable example.
- WAP to add the contents of memory location 4500H and [4] 3. 4501H and store the sum at memory location 4502H.
 - b. WAP to find the 2's complement of the number stored at [4] memory location 4200H and store the complemented number at memory location 4300H.
- Write an assembly language to multiply two 8 bit numbers [8] 4. 8EH and 2FH. Store the result in memory location C000H and C001H. Also, exchange the result to register C to register B from the accumulator.
- What are the limitations of 8085-microprocessor? Draw the [4+4]5. timing diagram of 8085 OP code fetch cycle.
- Define computer architecture and computer organization. [8] 6. Explain memory hierarchy with a diagram.
- Differentiate between RISC and CISC. [8] 7.
- [8] What are different modes of data transfer in computer 8. architecture? Explain.

Cont.

Diploma 4th Sem Exam Papers Of Microprocessor

Daniel F McAuley

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