

DESIGNING PHOTODIODE AMPLIFIER CIRCUITS WITH OPA128

The OPA128 ultra-low bias current operational amplifier achieves its 750A maximum bias current without compromise. Using standard design techniques, serious performance trade-offs were required which sacrificed overall amplifier performance in order to reach femtoamp ($I_A = 10^{-15}$ A) bias currents.

UNIQUE DESIGN MINIMIZES PERFORMANCE TRADE-OFFS

Small-geometry FETs have low bias current, of course, but FET size reduction reduces transconductance and increases noise dramatically, placing a serious restriction on performance when low bias current is achieved simply by making input FETs extremely small. Unfortunately, larger geometries suffer from high gate-to-substrate isolation diode leakage (which is the major contribution to BIFET® amplifier input bias current).

Replacing the reverse-biased gate-to-substrate isolation diode structure of BIFETs with dielectric isolation removes this large leakage current component which, together with a noise-free cascode circuit, special FET geometry, and advanced wafer processing, allows far higher *Difet*® performance compared to BIFETs.

HOW TO IMPROVE PHOTODIODE AMPLIFIER PERFORMANCE

An important electro-optical application of FET op amps is for photodiode amplifiers. The unequalled performance of the OPA128 is well-suited for very high sensitivity detector designs. A few design tips for photodiode amplifiers may be helpful:

- *Photodiode capacitance should be as low as possible.* See Figure 1: C_j affects not only bandwidth but noise as well. This is because C_j and the op amp's feedback resistor form a noise-gain zero (feedback pole).
- *Photodiode active area should be as small as possible so that C_j is small and R_j is high.* This will allow a higher signal-to-noise ratio. If a large area is needed, consider using optical "gain" (lens, mirror, etc.) rather than a large area diode. Optical "gain" is essentially noise-free.
- *Use as large a feedback resistor as possible (consistent with bandwidth requirements) to minimize noise.* This seems paradoxical, but remember, resistor thermal noise increases as:

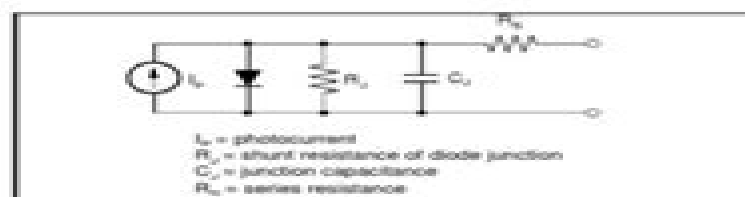


FIGURE 1. Photodiode Equivalent Circuit.

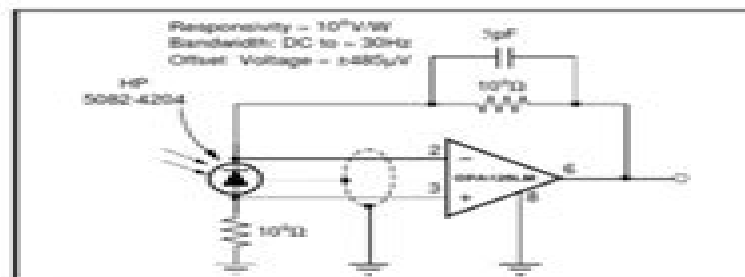


FIGURE 2. High-Sensitivity Photodiode Amplifier.

$$e_{\text{noise}} = \sqrt{4kTBR}$$

k : Boltzman's constant = 1.38×10^{-23} J/K
 T : temperature ($^{\circ}$ K)
 B : noise bandwidth (Hz)
 R : feedback resistor (Ω)
 e_{noise} : noise voltage (Vrms)

while transimpedance gain (signal) increases as:

$$e_{\text{noise}} = i(\text{signal}) R$$

Signal-to-noise improves by \sqrt{R} .

- *A low bias current op amp is needed to achieve highest sensitivity.* Bias current causes voltage offset errors with large-feedback resistors. Wide bandwidth circuits with smaller feedback resistors are less subject to bias current errors, but even in these circuits, bias current must be

Designing Photodiode Amplifier Circuits With Opa128

Anthony S. Fauci



Designing Photodiode Amplifier Circuits With Opa128:

Whispering the Secrets of Language: An Mental Journey through **Designing Photodiode Amplifier Circuits With Opa128**

In a digitally-driven earth wherever displays reign great and quick conversation drowns out the subtleties of language, the profound techniques and psychological subtleties hidden within words frequently get unheard. Yet, nestled within the pages of **Designing Photodiode Amplifier Circuits With Opa128** a captivating fictional value pulsing with organic emotions, lies an extraordinary quest waiting to be undertaken. Published by a skilled wordsmith, this marvelous opus attracts viewers on an introspective journey, delicately unraveling the veiled truths and profound affect resonating within the material of each and every word. Within the psychological depths of the emotional evaluation, we can embark upon a genuine exploration of the book is core styles, dissect their fascinating writing model, and succumb to the strong resonance it evokes heavy within the recesses of readers hearts.

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