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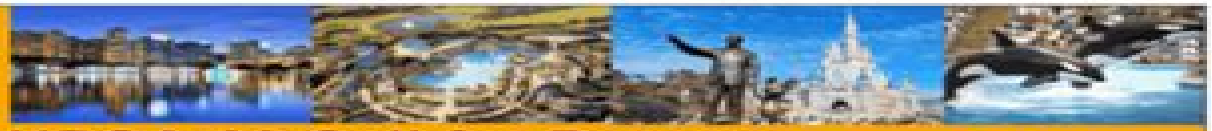

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Authors should submit an abstract by Tuesday, September 3, 2013, 11:59 PM EDT. They should submit the full version of the paper by **Friday, September 6, 2013, 11:59 PM EDT**. **No extensions will be granted**. The full version should be a PDF file that does not exceed 11 pages (plus unlimited pages for references) according to the instructions in http://hpca20.ece.ufl.edu/. Papers that exceed the length limit or that cannot be viewed using Adobe Reader (version 3.0 or higher) may not be reviewed. Papers should be submitted for double-blind review. We anticipate making a Best Paper award; all papers will be evaluated based on their novelty, fundamental insights, and potential for long-term impact. New-idea papers are encouraged. Submission issues should be directed to the program chair at <enright@eecg.toronto.edu>. Workshop and tutorial submissions should be directed to the workshop and tutorial co-chairs (nsatish@umich.edu, jmwu@ustc.edu.cn). HPCA-20 will host an ***Industrial Paper Session*** presenting novel insights from industry (see Call for Industry Papers at http://hpca20.ece.ufl.edu/).

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- Abstract deadline: **September 3, 2013, 11:59 PM EDT**
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Hpca 18 Call For Papers

Ali Iranmanesh,Hossein Sayadi

Hpca 18 Call For Papers:

High Performance Computing and Applications Wu Zhang,Zhangxin Chen,Craig C. Douglas,Weiqin Tong,2010-02-19 This book constitutes the thoroughly refereed post conference proceedings of the Second International Conference on High Performance Computing and Applications HPCA 2009 held in Shangahi China in August 2009 The 71 revised papers presented together with 10 invited presentations were carefully selected from 324 submissions The papers cover topics such as numerical algorithms and solutions high performance and grid computing novel approaches to high performance computing massive data storage and processsing and hardware acceleration **HPCA** ,2001 Topics covered in this text include microarchitecture memory architectures multiprocessor systems code generation techniques energy and thermal management prediction techniques application specific designs performance modelling and analysis and latency tolerance techniques *High Performance Computing and Applications* Jiang Xie,Zhangxin Chen,Craig C. Douglas,Wu Zhang,Yan Chen,2016-07-19 This book constitutes the thoroughly refereed proceedings of the Third International Conference on High Performance Computing and Applications HPCA 2015 held in Shanghai China in July 2015 The 24 revised full papers presented were carefully reviewed and selected from 50 submissions The papers cover the following research areas numerical algorithms and solutions high performance and grid computing novel approached to high performance computing massive data storage and processing hardware acceleration applications *ACM SIGPLAN Notices* ,2006-07 **Complex Networks and Their Applications XI** Hocine Cherifi,Rosario Nunzio Mantegna,Luis M. Rocha,Chantal Cherifi,Salvatore Miccichè,2023-01-03 This book highlights cutting edge research in the field of network science offering scientists researchers students and practitioners a unique update on the latest advances in theory and a multitude of applications It presents the peer reviewed proceedings of the XI International Conference on Complex Networks and their Applications COMPLEX NETWORKS 2022 The carefully selected papers cover a wide range of theoretical topics such as network models and measures community structure network dynamics diffusion epidemics and spreading processes resilience and control as well as all the main network applications including social and political networks networks in finance and economics biological and neuroscience networks and technological networks Data and Applications Security and Privacy XXXIII Simon N. Foley,2019-07-04 This book constitutes the refereed proceedings of the 33rd Annual IFIP WG 11 3 Conference on Data and Applications Security and Privacy DBSec 2019 held in Charleston SC USA in July 2018 The 21 full papers presented were carefully reviewed and selected from 52 submissions The papers present high quality original research from academia industry and government on theoretical and practical aspects of information security They are organized in topical sections on attacks mobile and Web security privacy security protocol practices distributed systems source code security and malware

Efficient Polymorphic Calls Karel Driesen,2012-12-06 The implementation of object oriented languages has been an active topic of research since the 1960s when the first Simula compiler was written The topic received renewed interest in

the early 1980s with the growing popularity of object oriented programming languages such as c and Smalltalk and got another boost with the advent of Java Polymorphic calls are at the heart of object oriented languages and even the first implementation of Simula 67 contained their classic implementation via virtual function tables In fact virtual function tables predate even Simula for example Ivan Sutherland s Sketchpad drawing editor employed very similar structures in 1960 Similarly during the 1970s and 1980s the implementers of Smalltalk systems spent considerable efforts on implementing polymorphic calls for this dynamically typed language where virtual function tables could not be used Given this long history of research into the implementation of polymorphic calls and the relatively mature standing it achieved over time why one might ask should there be a new book in this field The answer is simple Both software and hardware have changed considerably in recent years to the point where many assumptions underlying the original work in this field are no longer true In particular virtual function tables are no longer sufficient to implement polymorphic calls even for statically typed languages for example Java s interface calls cannot be implemented this way Furthermore today s processors are deeply pipelined and can execute instructions out of order making it difficult to predict the execution time of even simple code sequences **Computational Science and Its Applications - ICCSA 2005** Osvaldo Gervasi,2005-04-27 The four volume set LNCS 3480 3483 constitutes the refereed proceedings of the International Conference on Computational Science and Its Applications ICCSA 2005 held in Singapore in May 2005 The four volumes present a total of 540 papers selected from around 2700 submissions The papers span the whole range of computational science comprising advanced applications in virtually all sciences making use of computational techniques as well as foundations techniques and methodologies from computer science and mathematics such as high performance computing and communication networking optimization information systems and technologies scientific visualization graphics image processing data analysis simulation and modelling software systems algorithms security multimedia etc **Proceedings of ... ACM/IEEE-CS Joint Conference on Digital Libraries** ,2007 *Cybersecurity Systems for Human Cognition Augmentation* Robinson E. Pino,Alexander Kott,Michael Shevenell,2014-10-18 This book explores cybersecurity research and development efforts including ideas that deal with the growing challenge of how computing engineering can merge with neuroscience The contributing authors who are renowned leaders in this field thoroughly examine new technologies that will automate security procedures and perform autonomous functions with decision making capabilities To maximize reader insight into the range of professions dealing with increased cybersecurity issues this book presents work performed by government industry and academic research institutions working at the frontier of cybersecurity and network sciences Cybersecurity Systems for Human Cognition Augmentation is designed as a reference for practitioners or government employees working in cybersecurity Advanced level students or researchers focused on computer engineering or neuroscience will also find this book a useful resource A Primer on Hardware Prefetching Babak Falsafi,Thomas F. Wenisch,2022-06-01 Since the 1970 s microprocessor based digital platforms have been

riding Moore s law allowing for doubling of density for the same area roughly every two years However whereas microprocessor fabrication has focused on increasing instruction execution rate memory fabrication technologies have focused primarily on an increase in capacity with negligible increase in speed This divergent trend in performance between the processors and memory has led to a phenomenon referred to as the Memory Wall To overcome the memory wall designers have resorted to a hierarchy of cache memory levels which rely on the principal of memory access locality to reduce the observed memory access time and the performance gap between processors and memory Unfortunately important workload classes exhibit adverse memory access patterns that baffle the simple policies built into modern cache hierarchies to move instructions and data across cache levels As such processors often spend much time idling upon a demand fetch of memory blocks that miss in higher cache levels Prefetching predicting future memory accesses and issuing requests for the corresponding memory blocks in advance of explicit accesses is an effective approach to hide memory access latency There have been a myriad of proposed prefetching techniques and nearly every modern processor includes some hardware prefetching mechanisms targeting simple and regular memory access patterns This primer offers an overview of the various classes of hardware prefetchers for instructions and data proposed in the research literature and presents examples of techniques incorporated into modern microprocessors **AI-Enabled Electronic Circuit and System Design** Ali Iranmanesh,Hossein Sayadi,2025-01-27 As our world becomes increasingly digital electronics underpin nearly every industry Understanding how AI enhances this foundational technology can unlock innovations from smarter homes to more powerful gadgets offering vast opportunities for businesses and consumers alike This book demystifies how AI streamlines the creation of electronic systems making them smarter and more efficient With AI s transformative impact on various engineering fields this resource provides an up to date exploration of these advancements authored by experts actively engaged in this dynamic field Stay ahead in the rapidly evolving landscape of AI in engineering with AI Enabled Electronic Circuit and System Design From Ideation to Utilization your essential guide to the future of electronic systems endif A transformative guide describing how revolutionizes electronic design through AI integration Highlighting trends challenges and opportunities Demystifies complex AI applications in electronic design for practical use Leading insights authored by top experts actively engaged in the field Offers a current relevant exploration of significant topics in AI s role in electronic circuit and system design Editor s bios Dr Ali A Iranmanesh is the founder and CEO of Silicon Valley Polytechnic Institute He has received his Bachelor of Science in Electrical Engineering from Sharif University of Technology SUT Tehran Iran and both his master s and Ph D degrees in Electrical Engineering and Physics from Stanford University in Stanford CA He additionally holds a master s degree in business administration MBA from San Jose State University in San Jose CA Dr Iranmanesh is the founder and chairman of the International Society for Quality Electronic Design ISQED Currently he serves as the CEO of Innovotek Dr Iranmanesh has been instrumental in advancing semiconductor technologies innovative design methodologies and

engineering education He holds nearly 100 US and international patents reflecting his signifi cant contributions to the field Dr Iranmanesh is the Senior life members of EEE senior member of the American Society for Quality co founder and Chair Emeritus of the IEEE Education Society of Silicon Valley Vice Chair Emeritus of the IEEE PV chapter and recipient of IEEE Outstanding Educator Award Dr Hossein Sayadi is a Tenure Track Assistant Professor and Associate Chair in the Department of Computer Engineering and Computer Science at California State University Long Beach CSULB He earned his Ph D in Electrical and Computer Engineering from George Mason University in Fairfax Virginia and an M Sc in Computer Engineering from Sharif University of Technology in Tehran Iran As a recognized researcher with over 14 years of research experience Dr Sayadi is the founder and director of the Intelligent Secure and Energy Efficient Computing iSEC Lab at CSULB His research focuses on advancing hardware security and trust AI and machine learning cybersecurity and energy efficient computing addressing critical challenges in modern computing and cyber physical systems He has authored over 75 peer reviewed publications in leading conferences and journals Dr Sayadi is the CSU STEM NET Faculty Fellow with his research supported by multiple National Science Foundation NSF grants and awards from CSULB and the CSU Chancellor s Office He has contributed to various international conferences as an organizer and program committee member including as the TPC Chair for the 2024 and 2025 IEEE ISQED **Index to IEEE Publications** Institute of Electrical and Electronics Engineers,1997 Detection of Intrusions and Malware, and Vulnerability Assessment Clémentine Maurice,Leyla Bilge,Gianluca Stringhini,Nuno Neves,2020-07-07 This book constitutes the proceedings of the 17th International Conference on Detection of Intrusions and Malware and Vulnerability Assessment DIMVA 2020 held in Lisbon Portugal in June 2020 The 13 full papers presented in this volume were carefully reviewed and selected from 45 submissions The contributions were organized in topical sections named vulnerability discovery and analysis attacks web security and detection and containment The conference was held virtually due to the COVID 19 pandemic **Post-Quantum Cryptography** Thomas Johansson,Daniel Smith-Tone,2023-08-09 This book constitutes the refereed proceedings of the 14th International Workshop on Post Quantum Cryptography PQCrypto 2022 held in College Park MD USA in August 14 18 2023 The 25 full papers presented in this book were carefully reviewed and selected from 51 submissions They are categorized in the following topical sections code based cryptography group action based cryptography isogenye based cryptography lattice based cryptography multivariate cryptography quantum algorithms cryptanalysis and models post quantum protocols side channel cryptanalysis and countermeasures **The Engineering of Large Systems** ,1998-09-25 Since its first volume in 1960 Advances in Computers has presented detailed coverage of innovations in hardware and software and in computer theory design and applications It has also provided contributors with a medium in which they can examine their subjects in greater depth and breadth than that allowed by standard journal articles As a result many articles have become standard references that continue to be of significant lasting value despite the rapid growth taking place in the field This volume is organized

around engineering large scale software systems It discusses which technologies are useful for building these systems which are useful to incorporate in these systems and which are useful to evaluate these systems Biomedical Signal Analysis Rangaraj M. Rangayyan,Sridhar Krishnan,2024-02-19 Biomedical Signal Analysis Comprehensive resource covering recent developments applications of current interest and advanced techniques for biomedical signal analysis Biomedical Signal Analysis provides extensive insight into digital signal processing techniques for filtering identification characterization classification and analysis of biomedical signals with the aim of computer aided diagnosis taking a unique approach by presenting case studies encountered in the authors research work Each chapter begins with the statement of a biomedical signal problem followed by a selection of real life case studies and illustrations with the associated signals Signal processing modeling or analysis techniques are then presented starting with relatively simple textbook methods followed by more sophisticated research informed approaches Each chapter concludes with solutions to practical applications Illustrations of real life biomedical signals and their derivatives are included throughout The third edition expands on essential background material and advanced topics without altering the underlying pedagogical approach and philosophy of the successful first and second editions The book is enhanced by a large number of study questions and laboratory exercises as well as an online repository with solutions to problems and data files for laboratory work and projects Biomedical Signal Analysis provides theoretical and practical information on The origin and characteristics of several biomedical signals Analysis of concurrent coupled and correlated processes with applications in monitoring of sleep apnea Filtering for removal of artifacts random noise structured noise and physiological interference in signals generated by stationary nonstationary and cyclostationary processes Detection and characterization of events covering methods for QRS detection identification of heart sounds and detection of the dicrotic notch Analysis of waveshape and waveform complexity Interpretation and analysis of biomedical signals in the frequency domain Mathematical electrical mechanical and physiological modeling of biomedical signals and systems Sophisticated analysis of nonstationary multicomponent and multisource signals using wavelets time frequency representations signal decomposition and dictionary learning methods Pattern classification and computer aided diagnosis Biomedical Signal Analysis is an ideal learning resource for senior undergraduate and graduate engineering students Introductory sections on signals systems and transforms make this book accessible to students in disciplines other than electrical engineering *Innovations in Computer Science and Engineering* H. S. Saini,Rishi Sayal,A. Govardhan,Rajkumar Buyya,2021-04-23 This book features a collection of high quality peer reviewed research papers presented at the 8th International Conference on Innovations in Computer Science Engineering ICICSE 2020 held at Guru Nanak Institutions Hyderabad India on 28 29 August 2020 It covers the latest research in data science and analytics cloud computing machine learning data mining big data and analytics information security and privacy wireless and sensor networks and IoT applications artificial intelligence expert systems natural language processing image processing computer vision and

artificial neural networks *Euro-Par' 99 Parallel Processing* Patrick Amestoy,Philippe Berger,Michel Dayde,Iain Duff,Valerie Fraysse,Luc Giraud,Daniel Ruiz,2003-05-21 Euro Parisaninternationalconferencededicatedtothepromotionandadvan ment of all aspects of parallel computing The major themes can be divided into the broad categories of hardware software algorithms and applications for p allel computing The objective of Euro Par is to provide a forum within which to promote the development of parallel computing both as an industrial te nique and an academic discipline extending the frontier of both the state of the art and the state of the practice This is particularly important at a time when parallel computing is undergoing strong and sustained development and experiencing real industrial take up The main audience for and participants in Euro Parareseenasresearchersinacademicdepartments governmentlabora ries and industrial organisations Euro Par s objective is to become the primary choice of such professionals for the presentation of new results in their specic areas Euro Par is also interested in applications which demonstrate the e tiveness of the main Euro Par themes There is now a permanent Web site for the series http brahms fmi uni passau de cl europar where the history of the conference is described Euro Par is now sponsored by the Association of Computer Machinery and the International Federation of Information Processing Euro Par 99 The format of Euro Par 99follows that of the past four conferences and consists of a number of topics eachindividually monitored by a committee of four There were originally 23 topics for this year s conference The call for papers attracted 343 submissions of which 188 were accepted Of the papers accepted 4 were judged as distinguished 111 as regular and 73 as short papers **Optical Interconnects for Future Data Center Networks** Christoforos Kachris,Keren Bergman,Ioannis Tomkos,2012-11-07 Optical Interconnects in Future Data Center Networks covers optical networks and how they can be used to provide high bandwidth energy efficient interconnects for future data centers with increased communication bandwidth requirements This contributed volume presents an integrated view of the future requirements of the data centers and serves as a reference work for some of the most advanced solutions that have been proposed by major universities and companies Collecting the most recent and innovative optical interconnects for data center networks that have been presented in the research community by universities and industries this book is a valuable reference to researchers students professors and engineers interested in the domain of high performance interconnects and data center networks Additionally Optical Interconnects in Future Data Center Networks provides invaluable insights into the benefits and advantages of optical interconnects and how they can be a promising alternative for future data center networks

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