

```
.model nmos NMOS
.model pmos PMOS
```

7.5

For the gate shown in Fig. P7.5,

- Pull-up transistor ratio is 5/5
- pull-down transistor ratios are 100/5
- $V_{DD} = 1.0$ V
- $\gamma = 0.4$ V^{1/2}
- $|2\phi_F| = 0.6$ V

- (a) Identify the worst case input combination(s) for V_{OL} .
- (b) Calculate the worst case value of V_{OL} . (Assume that all pull-down transistors have the same body bias and initially, that $V_{OL} = 5\% V_{DD}$.)

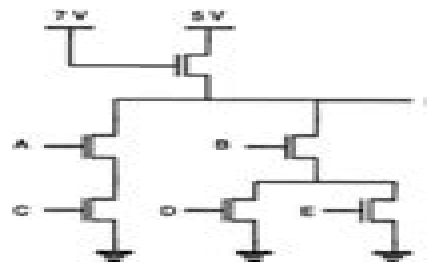


Figure P7.5

SOLUTION:

- (a) The worst case input combinations are those that cause only one current path from output to ground. The combinations are listed in the following table.

$A = 1$	$C = 1$	$B = 0$	
$A = 1$	$C = 1$	$C = 0$	$E = 0$
$B = 1$	$D = 1$	$A = 0$	$E = 0$
$B = 1$	$D = 1$	$C = 0$	$E = 0$
$B = 1$	$E = 1$	$A = 0$	$D = 0$
$B = 1$	$E = 1$	$C = 0$	$D = 0$

- (b) The worst case equivalent circuit can be represented by a simple inverter, with the driver $W/L=10$, load $W/L=1$. Both transistors are operating in linear region in this case because:
 Driver: $V_{GS} = 5$, $V_{DS} = 0.25$

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Kang



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Solutions Manual Digital Integrated Circuits CRC Press, 2009-07-10 Any textbook more than five years old simply won't do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization Each chapter includes numerous worked examples case studies and SPICE computer simulations The book's website offers supplementary material and more worked problems Qualifying instructors will have access to a new instructor's manual

Digital Integrated Circuits DeMassa, 1996-02-01 Gallium Arsenide Digital Integrated Circuit Design Stephen I. Long, 1990 **Digital Integrated Circuit Design** Hubert Kaeslin, 2008-04-28 This practical tool independent guide to designing digital circuits takes a unique top down approach reflecting the nature of the design process in industry Starting with architecture design the book comprehensively explains the why and how of digital circuit design using the physics designers need to know and no more

Solutions Manual Kang, 1995-08-01 *Solutions Manual for An Introduction to Digital and Analog Integrated Circuits and Applications* Sanjit K. Mitra, Sanjit Kumar Mitra, 1981 *Analysis and Design of Digital Integrated Circuits* David A. Hodges, Horace G. Jackson, 1988 This is a state of the art treatment of the circuit design of digital integrated circuits It includes coverage of the basic concepts of static characteristics voltage transfer characteristics noise margins fanout power dissipation and dynamic characteristics propagation delay times and the interrelationships among these parameters The authors are regarded as leading authorities in integrated circuits and MOS technology

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Foundations of Analog and Digital Electronic Circuits Anant Agarwal, Jeffrey Lang, 2005-07-01 Unlike books currently on the market this book attempts to satisfy two goals combine circuits and electronics into a single unified treatment and establish a strong connection with the contemporary world of digital systems It will introduce a new way of looking not only at the treatment of circuits but also at the treatment of introductory coursework in engineering in general Using the concept of abstraction the book attempts to form a bridge between the world of physics and the world of large computer systems In particular it attempts to unify electrical engineering and computer science as the art of creating and exploiting successive abstractions to manage the complexity of building useful electrical systems Computer systems are simply one type of electrical systems Balances circuits theory with practical digital electronics applications Illustrates concepts with real devices Supports the popular circuits and electronics course on the MIT OpenCourse Ware from which professionals worldwide study this new approach Written by two educators well known for their innovative teaching and research and their collaboration with industry Focuses on contemporary MOS technology

Integrated Circuit Design Xiaokun Yang, 2024-11-20 This textbook seeks to foster a deep understanding of the field by introducing the industry integrated circuit IC design flow and offering tape out or pseudo tape out projects for hands on practice facilitating project based learning PBL experiences Integrated Circuit Design IC Design Flow and Project Based Learning aims to equip readers for entry level roles as IC designers in the industry and as hardware design researchers in academia The book commences with an overview of the industry IC design flow with a primary focus on register transfer level RTL design the automation of simulation and verification and system on chip SoC integration To build connections between RTL design and physical hardware FPGA field programmable gate array synthesis and implementation

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