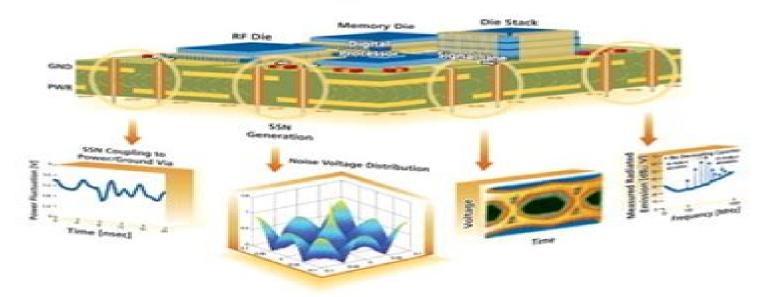


Power Integrity Modeling and Design for Semiconductors and Systems



Madhavan Swaminathan • A. Ege Engin

Power Integrity Modeling And Design For Semiconductors And Systems

Greg Edlund

Power Integrity Modeling And Design For Semiconductors And Systems:

Power Integrity Modeling and Design for Semiconductors and Systems Madhavan Swaminathan, Ege Engin, 2007-11-19 The First Comprehensive Example Rich Guide to Power Integrity Modeling Professionals such as signal integrity engineers package designers and system architects need to thoroughly understand signal and power integrity issues in order to successfully design packages and boards for high speed systems Now for the first time there s a complete guide to power integrity modeling everything you need to know from the basics through the state of the art Using realistic case studies and downloadable software examples two leading experts demonstrate today s best techniques for designing and modeling interconnects to efficiently distribute power and minimize noise The authors carefully introduce the core concepts of power distribution design systematically present and compare leading techniques for modeling noise and link these techniques to specific applications. Their many examples range from the simplest using analytical equations to compute power supply noise through complex system level applications. The authors Introduce power delivery network components analysis high frequency measurement and modeling requirements Thoroughly explain modeling of power ground planes including plane behavior lumped modeling distributed circuit based approaches and much more Offer in depth coverage of simultaneous switching noise including modeling for return currents using time and frequency domain analysis Introduce several leading time domain simulation methods such as macromodeling and discuss their advantages and disadvantages Present the application of the modeling methods on several advanced case studies that include high speed servers high speed differential signaling chip package analysis materials characterization embedded decoupling capacitors and electromagnetic bandgap structures This book s system level focus and practical examples will make it indispensable for every student and professional concerned with power integrity including electrical engineers system designers signal integrity engineers and materials scientists It will also be valuable to developers building software that helps to analyze high speed systems

Power Integrity Modeling and Design for Semiconductors and Systems Madhavan Swaminathan, A. Ege Engin, 2007 The First Comprehensive Example Rich Guide to Power Integrity Modeling Professionals such as signal integrity engineers package designers and system architects need to thoroughly understand signal and power integrity issues in order to successfully design packages and boards for high speed systems Now for the first time there s a complete guide to power integrity modeling everything you need to know from the basics through the state of the art Using realistic case studies and downloadable software examples two leading experts demonstrate today s best techniques for designing and modeling interconnects to efficiently distribute power and minimize noise The authors carefully introduce the core concepts of power distribution design systematically present and compare leading techniques for modeling noise and link these techniques to specific applications Their many examples range from the simplest using analytical equations to compute power supply noise through complex system level applications The authors Introduce power delivery network components analysis high

frequency measurement and modeling requirements Thoroughly explain modeling of power ground planes including plane behavior lumped modeling distributed circuit based approaches and much more Offer in depth coverage of simultaneous switching noise including modeling for return currents using time and frequency domain analysis Introduce several leading time domain simulation methods such as macromodeling and discuss their advantages and disadvantages Present the application of the modeling methods on several advanced case studies that include high speed servers high speed differential signaling chip package analysis materials characterization embedded decoupling capacitors and electromagnetic bandgap structures This book s system level focus and practical examples will make it indispensable for every student and professional concerned with power integrity including electrical engineers system designers signal integrity engineers and materials scientists It will also be valuable to developers building software that helps to analyze high speed systems

Electrical Modeling and Design for 3D System Integration Er-Ping Li,2012-03-19 New advanced modeling methods for simulating the electromagnetic properties of complex three dimensional electronic systems Based on the author's extensive research this book sets forth tested and proven electromagnetic modeling and simulation methods for analyzing signal and power integrity as well as electromagnetic interference in large complex electronic interconnects multilayered package structures integrated circuits and printed circuit boards Readers will discover the state of the technology in electronic package integration and printed circuit board simulation and modeling In addition to popular full wave electromagnetic computational methods the book presents new more sophisticated modeling methods offering readers the most advanced tools for analyzing and designing large complex electronic structures Electrical Modeling and Design for 3D System Integration begins with a comprehensive review of current modeling and simulation methods for signal integrity power integrity and electromagnetic compatibility Next the book guides readers through The macromodeling technique used in the electrical and electromagnetic modeling and simulation of complex interconnects in three dimensional integrated systems The semi analytical scattering matrix method based on the N body scattering theory for modeling of three dimensional electronic package and multilayered printed circuit boards with multiple vias Two and three dimensional integral equation methods for the analysis of power distribution networks in three dimensional package integrations. The physics based algorithm for extracting the equivalent circuit of a complex power distribution network in three dimensional integrated systems and printed circuit boards An equivalent circuit model of through silicon vias Metal oxide semiconductor capacitance effects of through silicon vias Engineers researchers and students can turn to this book for the latest techniques and methods for the electrical modeling and design of electronic packaging three dimensional electronic integration integrated circuits and printed circuit boards **Signal and Power Integrity - Simplified** Eric Bogatin, 2009-07-13 The 1 Practical Guide to Signal Integrity Design Now Updated with Extensive New Coverage This book brings together up to the minute techniques for finding fixing and avoiding signal integrity problems in your design Drawing on his work teaching more than five

thousand engineers world class signal and power integrity expert Eric Bogatin systematically reviews the root causes of all six families of signal integrity problems and shows how to design them out early in the design cycle This edition's extensive new content includes a brand new chapter on S parameters in signal integrity applications and another on power integrity and power distribution network design topics at the forefront of contemporary electronics design Coverage includes A fully up to date introduction to signal integrity and physical design How design and technology selection can make or break the performance of the power distribution network Exploration of key concepts such as plane impedance spreading inductance decoupling capacitors and capacitor loop inductance Practical techniques for analyzing resistance capacitance inductance and impedance Solving signal integrity problems via rules of thumb analytic approximation numerical simulation and measurement Understanding how interconnect physical design impacts signal integrity Managing differential pairs and losses Harnessing the full power of S parameters in high speed serial link applications Ensuring power integrity throughout the entire power distribution path Realistic design guidelines for improving signal integrity and much more Unlike books that concentrate on theoretical derivation and mathematical rigor this book emphasizes intuitive understanding practical tools and engineering discipline Designed for electronics industry professionals from beginners to experts it will be an invaluable resource for getting signal integrity designs right the first time every time **Modeling and Design of Electromagnetic** Compatibility for High-Speed Printed Circuit Boards and Packaging Xing-Chang Wei, 2017-09-19 Modeling and Design of Electromagnetic Compatibility for High Speed Printed Circuit Boards and Packaging presents the electromagnetic modelling and design of three major electromagnetic compatibility EMC issues related to the high speed printed circuit board PCB and electronic packages signal integrity SI power integrity PI and electromagnetic interference EMI The emphasis is put on two essential passive components of PCBs and packages the power distribution network and the signal distribution network This book includes two parts Part one talks about the field circuit hybrid methods used for the EMC modeling including the modal method the integral equation method the cylindrical wave expansion method and the de embedding method Part two illustrates EMC design methods and explores the applications of novel metamaterials and two dimensional materials on traditional EMC problems This book is designed to enhance worthwhile electromagnetic theory and mathematical methods for practical engineers and to train students with advanced EMC applications **Power Integrity** for I/O Interfaces Vishram S. Pandit, Woong Hwan Ryu, Myoung Joon Choi, 2010-10-13 Foreword by Joungho Kim The Hands On Guide to Power Integrity in Advanced Applications from Three Industry Experts In this book three industry experts introduce state of the art power integrity design techniques for today s most advanced digital systems with real life system level examples They introduce a powerful approach to unifying power and signal integrity design that can identify signal impediments earlier reducing cost and improving reliability After introducing high speed single ended and differential I O interfaces the authors describe on chip package and PCB power distribution networks PDNs and signal networks carefully

reviewing their interactions Next they walk through end to end PDN and signal network design in frequency domain addressing crucial parameters such as self and transfer impedance They thoroughly address modeling and characterization of on chip components of PDNs and signal networks evaluation of power to signal coupling coefficients analysis of Simultaneous Switching Output SSO noise and many other topics Coverage includes The exponentially growing challenge of I O power integrity in high speed digital systems PDN noise analysis and its timing impact for single ended and differential interfaces Concurrent design and co simulation techniques for evaluating all power integrity effects on signal integrity Time domain gauges for designing and optimizing components and systems Power signal integrity interaction mechanisms including power noise coupling onto signal trace and noise amplification through signal resonance Performance impact due to Inter Symbol Interference ISI crosstalk and SSO noise as well as their interactions Validation techniques including low impedance VNA measurements power noise measurements and characterization of power to signal coupling effects Power Integrity for I O Interfaces will be an indispensable resource for everyone concerned with power integrity in cutting edge digital designs including system design and hardware engineers signal and power integrity engineers graduate students and Timing Analysis and Simulation for Signal Integrity Engineers Greg Edlund, 2007-10-22 Every day researchers companies call upon their signal integrity engineers to make difficult decisions about design constraints and timing margins Can I move these wires closer together How many holes can I drill in this net How far apart can I place these chips Each design is unique there s no single recipe that answers all the questions Today s designs require ever greater precision but design guides for specific digital interfaces are by nature conservative Now for the first time there s a complete guide to timing analysis and simulation that will help you manage the tradeoffs between signal integrity performance and cost Writing from the perspective of a practicing SI engineer and team lead Greg Edlund of IBM presents deep knowledge and quantitative techniques for making better decisions about digital interface design Edlund shares his insights into how and why digital interfaces fail revealing how fundamental sources of pathological effects can combine to create fault conditions You won t just learn Edlund's expert techniques for avoiding failures you ll learn how to develop the right approach for your own projects and environment Coverage includes Systematically ensure that interfaces will operate with positive timing margin over the product s lifetime without incurring excess cost Understand essential chip to chip timing concepts in the context of signal integrity Collect the right information upfront so you can analyze new designs more effectively Review the circuits that store information in CMOS state machines and how they fail Learn how to time common clock source synchronous and high speed serial transfers Thoroughly understand how interconnect electrical characteristics affect timing propagation delay impedance profile crosstalk resonances and frequency dependent loss Model 3D discontinuities using electromagnetic field solvers Walk through four case studies coupled differential vias land grid array connector DDR2 memory data transfer and PCI Express channel Appendices present a refresher on SPICE modeling and a high level

conceptual framework for electromagnetic field behavior Objective realistic and practical this is the signal integrity resource engineers have been searching for Preface xiii Acknowledgments xvi About the Author xix About the Cover xx Chapter 1 Engineering Reliable Digital Interfaces 1 Chapter 2 Chip to Chip Timing 13 Chapter 3 Inside IO Circuits 39 Chapter 4 Modeling 3D Discontinuities 73 Chapter 5 Practical 3D Examples 101 Chapter 6 DDR2 Case Study 133 Chapter 7 PCI Express Case Study 175 Appendix A A Short CMOS and SPICE Primer 209 Appendix B A Stroll Through 3D Fields 219 Endnotes 233 Index 235 Circuit Oriented Electromagnetic Modeling Using the PEEC Techniques Albert Ruehli, Giulio Antonini, Lijun Jiang, 2017-05-30 Bridges the gap between electromagnetics and circuits by addressing electrometric modeling EM using the Partial Element Equivalent Circuit PEEC method This book provides intuitive solutions to electromagnetic problems by using the Partial Element Equivalent Circuit PEEC method This book begins with an introduction to circuit analysis techniques laws and frequency and time domain analyses The authors also treat Maxwell s equations capacitance computations and inductance computations through the lens of the PEEC method Next readers learn to build PEEC models in various forms equivalent circuit models non orthogonal PEEC models skin effect models PEEC models for dielectrics incident and radiate field models and scattering PEEC models. The book concludes by considering issues like stability and passivity and includes five appendices some with formulas for partial elements Leads readers to the solution of a multitude of practical problems in the areas of signal and power integrity and electromagnetic interference Contains fundamentals applications and examples of the PEEC method Includes detailed mathematical derivations Circuit Oriented Electromagnetic Modeling Using the PEEC Techniques is a reference for students researchers and developers who work on the physical layer modeling of IC interconnects and Packaging PCBs and high speed links Modern VLSI Design Wayne Wolf, 2008-12-21 The Number 1 VLSI Design Guide Now Fully Updated for IP Based Design and the Newest Technologies Modern VLSI Design Fourth Edition offers authoritative up to the minute guidance for the entire VLSI design process from architecture and logic design through layout and packaging Wayne Wolf has systematically updated his award winning book for today s newest technologies and highest value design techniques Wolf introduces powerful new IP based design techniques at all three levels gates subsystems and architecture He presents deeper coverage of logic design fundamentals clocking and timing and much more No other VLSI guide presents as much up to date information for maximizing performance minimizing power utilization and achieving rapid design turnarounds Electromagnetic Bandgap (EBG) Structures Antonio Orlandi, Bruce Archambeault, Francesco de Paulis, Samuel Connor, 2017-05-31 An essential guide to the background design and application of common mode filtering structures in modern high speed differential communication links Written by a team of experts in the field Electromagnetic Bandgap EBG Structures explores the practical electromagnetic bandgap based common mode filters for power integrity applications and covers the theoretical and practical design approaches for common mode filtering in high speed printed circuit boards especially for boards in high data rate systems The authors describe the classic applications of electromagnetic bandgap EBG structures and the phenomena of common mode generation in high speed digital boards The text also explores the fundamental electromagnetic mechanisms of the functioning of planar EBGs and considers the impact of planar EBGs on the digital signal propagation of single ended and differential interconnects routed on top or between EBGs The authors examine the concept design and modeling of EBG common mode filters in their two forms on board and removable They also provide several comparisons between measurement and electromagnetic simulations that validate the proposed EBG filters design approach This important resource Presents information on planar EBG based common mode filters for high speed differential digital systems Provides systematic analysis of the fundamental mechanisms of planar EBG structures Offers detailed design methodology to create EBG filters without the need for repeated full wave electromagnetic analysis Demonstrates techniques for use in practical real world designs Electromagnetic Bandgap EBG Structures Common Mode Filters for High Speed Digital Systems offers an introduction to the background design and application of common mode filtering structures in modern high speed differential communication links a critical issue in high speed and high performance systems

Scientific Computing in Electrical Engineering Andreas Bartel, Markus Clemens, Michael Günther, E. Jan W. ter Maten, 2016-05-09 This book is a collection of selected papers presented at the 10th International Conference on Scientific Computing in Electrical Engineering SCEE held in Wuppertal Germany in 2014 The book is divided into five parts reflecting the main directions of SCEE 2014 1 Device Modeling Electric Circuits and Simulation 2 Computational Electromagnetics 3 Coupled Problems 4 Model Order Reduction and 5 Uncertainty Quantification Each part starts with a general introduction followed by the actual papers The aim of the SCEE 2014 conference was to bring together scientists from academia and industry mathematicians electrical engineers computer scientists and physicists with the goal of fostering intensive discussions on industrially relevant mathematical problems with an emphasis on the modeling and numerical simulation of electronic circuits and devices electromagnetic fields and coupled problems The methodological focus was on model order reduction and uncertainty quantification this book will appeal to mathematicians and electrical engineers it offers a valuable starting point for developers of algorithms programs who want learn about recent advances in other fields as well open problems coming from industry moreover be use representatives industry with an interest new program tools mathematical Outlines and Highlights for Power Integrity Modeling and Design for Semiconductors and Systems by methods Engin, Isbn Cram101 Textbook Reviews, 2009-12 Never HIGHLIGHT a Book Again Virtually all of the testable terms concepts persons places and events from the textbook are included Cram101 Just the FACTS101 studyquides give all of the outlines highlights notes and guizzes for your textbook with optional online comprehensive practice tests Only Cram101 is Textbook Specific Accompanys 9780136152064 Simulation-Driven Modeling and Optimization Slawomir Koziel, Leifur Leifsson, Xin-She Yang, 2016-02-12 This edited volume is devoted to the now ubiquitous use of computational models across

most disciplines of engineering and science led by a trio of world renowned researchers in the field Focused on recent advances of modeling and optimization techniques aimed at handling computationally expensive engineering problems involving simulation models this book will be an invaluable resource for specialists engineers researchers graduate students working in areas as diverse as electrical engineering mechanical and structural engineering civil engineering industrial engineering hydrodynamics aerospace engineering microwave and antenna engineering ocean science and climate modeling and the automotive industry where design processes are heavily based on CPU heavy computer simulations Various techniques such as knowledge based optimization adjoint sensitivity techniques and fast replacement models to name just a few are explored in depth along with an array of the latest techniques to optimize the efficiency of the simulation driven design process High fidelity simulation models allow for accurate evaluations of the devices and systems which is critical in the design process especially to avoid costly prototyping stages Despite this and other advantages the use of simulation tools in the design process is quite challenging due to associated high computational cost The steady increase of available computational resources does not always translate into the shortening of the design cycle because of the growing demand for higher accuracy and necessity to simulate larger and more complex systems For this reason automated simulation driven design while highly desirable is difficult when using conventional numerical optimization routines which normally require a large number of system simulations each one already expensive Interconnect Technologies for Integrated Circuits and Flexible Electronics Yash Agrawal, Kavicharan Mummaneni, P. Uma Sathyakam, 2023-09-21 This contributed book provides a thorough understanding of the basics along with detailed state of the art emerging interconnect technologies for integrated circuit design and flexible electronics It focuses on the investigation of advanced on chip interconnects which match the current as well as future technology requirements The contents focus on different aspects of interconnects such as material physical characteristics parasitic extraction design structure modeling machine learning and neural network based models for interconnects signaling schemes varying signal integrity performance analysis variability reliability aspects associated electronic design automation tools The book also explores interconnect technologies for flexible electronic systems It also highlights the integration of sensors with stretchable interconnects to demonstrate the concept of a stretchable sensing network for wearable and flexible applications This book is a useful guide for those working in academia and industry to understand the fundamentals and application of interconnect technologies An Engineer's Guide to Automated Testing of High-Speed Interfaces, Second Edition Jose Moreira, Hubert Werkmann, 2016-04-30 This second edition of An Engineer s Guide to Automated Testing of High Speed Interfaces provides updates to reflect current state of the art high speed digital testing with automated test equipment technology ATE Featuring clear examples this one stop reference covers all critical aspects of automated testing including an introduction to high speed digital basics a discussion of industry standards ATE and bench instrumentation for digital applications and test and measurement techniques for characterization and production

environment Engineers learn how to apply automated test equipment for testing high speed digital I O interfaces and gain a better understanding of PCI Express 4 100Gb Ethernet and MIPI while exploring the correlation between phase noise and jitter This updated resource provides expanded material on 28 32 Gbps NRZ testing and wireless testing that are becoming increasingly more pertinent for future applications This book explores the current trend of merging high speed digital testing within the fields of photonic and wireless testing **SOC (System-on-a-Chip) Testing for Plug and Play Test Automation** Krishnendu Chakrabarty, 2013-04-17 System on a Chip SOC integrated circuits composed of embedded cores are now commonplace Nevertheless there remain several roadblocks to rapid and efficient system integration Test development is seen as a major bottleneck in SOC design and manufacturing capabilities Testing SOCs is especially challenging in the absence of standardized test structures test automation tools and test protocols In addition long interconnects high density and high speed designs lead to new types of faults involving crosstalk and signal integrity SOC System on a Chip Testing for Plug and Play Test Automation is an edited work containing thirteen contributions that address various aspects of SOC testing SOC System on a Chip Testing for Plug and Play Test Automation is a valuable reference for researchers and students interested in various aspects of SOC testing Digital Communications Test and Measurement Dennis Derickson, Marcus Müller, 2007-12-10 A Comprehensive Guide to Physical Layer Test and Measurement of Digital Communication Links Today s new data communication and computer interconnection systems run at unprecedented speeds presenting new challenges not only in the design but also in troubleshooting test and measurement This book assembles contributions from practitioners at top test and measurement companies component manufacturers and universities It brings together information that has never been broadly accessible before information that was previously buried in application notes seminar and conference presentations short courses and unpublished works Readers will gain a thorough understanding of the inner workings of digital high speed systems and learn how the different aspects of such systems can be tested The editors and contributors cover key areas in test and measurement of transmitters digital waveform and jitter analysis and bit error ratio receivers sensitivity jitter tolerance and PLL CDR characterization and high speed channel characterization in time and frequency domain Extensive illustrations are provided throughout Coverage includes Signal integrity from a measurement point of view Digital waveform analysis using high bandwidth real time and sampling equivalent time oscilloscopes Bit error ratio measurements for both electrical and optical links Extensive coverage on the topic of jitter in high speed networks State of the art optical sampling techniques for analysis of 100 Gbit s signals Receiver characterization clock recovery phase locked loops jitter tolerance and transfer functions sensitivity testing and stressed waveform receiver testing Channel and system characterization TDR T and frequency domain based alternatives Testing and measuring PC architecture communication links PCIexpress SATA and FB DIMM Passive Macromodeling Stefano Grivet-Talocia, Bjorn Gustavsen, 2015-10-30 Offers an overview of state of the art passive macromodeling techniques with an

emphasis on black box approaches This book offers coverage of developments in linear macromodeling with a focus on effective proven methods After starting with a definition of the fundamental properties that must characterize models of physical systems the authors discuss several prominent passive macromodeling algorithms for lumped and distributed systems and compare them under accuracy efficiency and robustness standpoints The book includes chapters with standard background material such as linear time invariant circuits and systems basic discretization of field equations state space systems as well as appendices collecting basic facts from linear algebra optimization templates and signals and transforms The text also covers more technical and advanced topics intended for the specialist which may be skipped at first reading Provides coverage of black box passive macromodeling an approach developed by the authors Elaborates on main concepts and results in a mathematically precise way using easy to understand language Illustrates macromodeling concepts through dedicated examples Includes a comprehensive set of end of chapter problems and exercises Passive Macromodeling Theory and Applications serves as a reference for senior or graduate level courses in electrical engineering programs and to engineers in the fields of numerical modeling simulation design and optimization of electrical electronic systems Stefano Grivet Talocia PhD is an Associate Professor of Circuit Theory at the Politecnico di Torino in Turin Italy and President of IdemWorks Dr Grivet Talocia is author of over 150 technical papers published in international journals and conference proceedings He invented several algorithms in the area of passive macromodeling making them available through IdemWorks Bj rn Gustavsen PhD is a Chief Research Scientist in Energy Systems at SINTEF Energy Research in Trondheim Norway More than ten years ago Dr Gustavsen developed the original version of the vector fitting method with Prof Semlyen at the University of Toronto The vector fitting method is one of the most widespread approaches for model extraction Dr Gustavsen is also an IEEE fellow Handbook of Integrated Circuit Industry Yangyuan Wang, Min-Hwa Chi, Jesse Jen-Chung Lou, Chun-Zhang Chen, 2023-11-27 Written by hundreds experts who have made contributions to both enterprise and academics research these excellent reference books provide all necessary knowledge of the whole industrial chain of integrated circuits and cover topics related to the technology evolution trends fabrication applications new materials equipment economy investment and industrial developments of integrated circuits Especially the coverage is broad in scope and deep enough for all kind of readers being interested in integrated circuit industry Remarkable data collection update marketing evaluation enough working knowledge of integrated circuit fabrication clear and accessible category of integrated circuit products and good equipment insight explanation etc can make general readers build up a clear overview about the whole integrated circuit industry This encyclopedia is designed as a reference book for scientists and engineers actively involved in integrated circuit research and development field In addition this book provides enough guide lines and knowledges to benefit enterprisers being interested in integrated circuit industry Signal Integrity Effects in Custom IC and ASIC Designs Raminderpal Singh, 2001-12-12 offers a tutorial guide to IC designers who want to move to the next level of chip design by unlocking the secrets of signal integrity Jake Buurma Senior Vice President Worldwide Research Development Cadence Design Systems Inc Covers signal integrity effects in high performance Radio Frequency RF IC Brings together research papers from the past few years that address the broad range of issues faced by IC designers and CAD managers now and in the future A Wiley IEEE Press publication

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Power Integrity Modeling And Design For Semiconductors And Systems Introduction

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