

Chapter 3 SOLUTIONS

1) a) $I_D = \frac{V_{in} - 2V_{D0n}}{R_1 + R_2}$

$$= \frac{2.5 - 1.4}{4k\Omega} = \frac{1.1V}{4k\Omega}$$

$$I_D = 275\mu A$$

b) $I_S = 10^{-14} A$, $T = 300K$, $V_{D0n} = 0.7V$

$$I_D = I_S (e^{V_D/\phi_T} - 1)$$

where $\phi_T = \frac{kT}{q} = 26mV @ 300K$

⊛ $I_D = \frac{V_{in} - 2V_D}{R_1 + R_2} = I_S (e^{V_D/\phi_T} - 1)$
 $\frac{2.5 - 2V_D}{4k\Omega} = 10^{-14} (e^{V_D/0.026} - 1)$

iterating on this expression
we can obtain.

$$V_D = 0.628V$$

$$I_D = 311\mu A$$

c) SPICE

exercise 3.1

```
vin in 0 dc 2.5
r1 in 2 2k
d1 2 3 D
r2 3 out 2k
d2 out 0 D
```

```
.options post=2
.op
```

```
.model D D level=1 is=1e-14 n=0.5
.end
```

```
*****
NODE      VOLTAGE
2         1.8750E+00
3         1.2500E+00
OUT       6.2501E-01
```

CURRENT 3.1249E-04

d) $I_S = 10^{-14} A$, $T = 300K$;
again use ⊛ from b and iterate
to find:

$$V_D = 0.743V$$

$$I_D = 253\mu A$$

$I_S = 10^{-14} A$, $T = 350K$;

$$V_D = 0.728V$$

$$I_D = 261\mu A$$

```
exercise 3.1d
vin in 0 dc 2.5
r1 in 2 2k
d1 2 3 D
r2 3 out 2k
d2 out 0 D
```

```
.options post=2
.op
```

```
*.temp 27
*.temp 77
*.model D D level=1 is=1e-14 n=0.5
*.model D D level=1 is=1e-14 n=0.5
.end
```

```
*****
t=27C*
NODE      VOLTAGE
2         1.9890E+00
3         1.2500E+00
IN        2.5000E+00
OUT       7.3892E-01
```

```
ID        2.5554E-04
*****
t=77C*
NODE      VOLTAGE
2         1.7844E+00
3         1.2500E+00
IN        2.5000E+00
OUT       5.3436E-01
```

ID 3.5782E-04

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Rachel Sandford



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Analog and Digital Electronic Circuits R. Prasad, 2021-05-15 This book introduces the foundations and fundamentals of electronic circuits. It broadly covers the subjects of circuit analysis as well as analog and

digital electronics It features discussion of essential theorems required for simplifying complex circuits and illustrates their applications under different conditions Also in view of the emerging potential of Laplace transform method for solving electrical networks a full chapter is devoted to the topic in the book In addition it covers the physics and technical aspects of semiconductor diodes and transistors as well as discrete time digital signals logic gates and combinational logic circuits Each chapter is presented as complete as possible without the reader having to refer to any other book or supplementary material Featuring short self assessment questions distributed throughout along with a large number of solved examples supporting illustrations and chapter end problems and solutions this book is ideal for any physics undergraduate lecture course on electronic circuits Its use of clear language and many real world examples make it an especially accessible book for students unfamiliar or unsure about the subject matter

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Solutions Manual Digital Integrated Circuits CRC Press, 2009-07-10 Any textbook more than five years old simply won't do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization Each chapter includes numerous worked examples case studies and SPICE computer

simulations The book's website offers supplementary material and more worked problems Qualifying instructors will have access to a new instructor's manual FPGA Implementations of Neural Networks Amos R. Omondi, Jagath C.

Rajapakse, 2006-10-04 During the 1980s and early 1990s there was significant work in the design and implementation of hardware neurocomputers Nevertheless most of these efforts may be judged to have been unsuccessful at no time have hardware neurocomputers been in wide use This lack of success may be largely attributed to the fact that earlier work was almost entirely aimed at developing custom neurocomputers based on ASIC technology but for such niche uses this technology was never sufficiently developed or competitive enough to justify large scale adoption On the other hand gate arrays of the period mentioned were never large enough nor fast enough for serious artificial neural network ANN applications But technology has now improved the capacity and performance of current FPGAs are such that they present a much more realistic alternative Consequently neurocomputers based on FPGAs are now a much more practical proposition than they have been in the past This book summarizes some work towards this goal and consists of 12 papers that were selected after review from a number of submissions The book is nominally divided into three parts Chapters 1 through 4 deal with foundational issues Chapters 5 through 11 deal with a variety of implementations and Chapter 12 looks at the lessons learned from a large scale project and also reconsiders design issues in light of current and future technology *Makers of the Microchip* Christophe

Lecuyer, David C. Brock, 2022-10-25 The first years of the company that developed the microchip and created the model for a successful Silicon Valley start up In the first three and a half years of its existence Fairchild Semiconductor developed produced and marketed the device that would become the fundamental building block of the digital world the microchip Founded in 1957 by eight former employees of the Shockley Semiconductor Laboratory Fairchild created the model for a successful Silicon Valley start up intense activity with a common goal close collaboration and a quick path to the market Fairchild's first device hit the market just ten months after the company's founding Fairchild Semiconductor was one of the first companies financed by venture capital and its success inspired the establishment of venture capital firms in the San Francisco Bay area These firms would finance the explosive growth of Silicon Valley over the next several decades This history of the early years of Fairchild Semiconductor examines the technological business and social dynamics behind its innovative products The centerpiece of the book is a collection of documents reproduced in facsimile including the company's first prospectus ideas sketches and plans for the company's products and a notebook kept by cofounder Jay Last that records problems schedules and tasks discussed at weekly meetings A historical overview interpretive essays and an introduction to semiconductor technology in the period accompany these primary documents **Nuclear Science Abstracts**, 1974

Plunkett's Engineering & Research Industry Almanac 2006: The Only Complete Guide to the Business of Research, Development and Engineering Jack W. Plunkett, 2006-05 A complete guide to trends and leading companies in the Engineering and Research business fields design development and technology based research Includes market analysis R D

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steps each with accurate molecular treatment Semiconductor electronics have seen a sustained exponential reduce in size and cost and a similar augment in performance and level of integration over the last thirty years The Silicon Roadmap is laid out for the next ten years After that either economical or physical barriers will pose a huge challenge The former is connected to the difficulty of making a profit in view of the exorbitant costs of building the necessary manufacturing capabilities if present day technologies are extrapolated The latter is a direct consequence of the shrinking device size leading to physical phenomena impeding the operation of current devices The transistor is the building block to a modern processor The current silicon designed transistors are going to hit their physical limit not merely the actualization of Moore s law but also the problems with heat dissipation wire connections and the materials we use to create them Hence nanotechnology helps us to look at new ways information processing at a better speed and measure A promising alternative to the imminent challenges from the CMOS based computing is to focus on other alternatives of nano scale precision Chemically Assembled Electronic Nanotechnology CAEN is a promising technology which uses self alignment to construct electronic circuits from nano scale devices that take advantage of quantum mechanical effects This book is intended as an introduction to the field of nanotechnology for nanoelectronics vendors researchers and students who want to start thinking about the potential opportunities afforded by these emerging scientific developments

New Technologies Constantin Volosencu, 2012-03-30 The book New Technologies Trends Innovations and Research presents contributions made by researchers from the entire world and from some modern fields of technology serving as a valuable tool for scientists researchers graduate students and professionals Some practical applications in particular areas are presented offering the capability to solve problems resulted from economic needs and to perform specific functions The book will make possible for scientists and engineers to get familiar with the ideas from researchers from some modern fields of activity It will provide interesting examples of practical applications of knowledge assist in the designing process as well as bring changes to their research areas A collection of techniques that combine scientific resources is provided to make necessary products with the desired quality criteria Strong mathematical and scientific concepts were used in the applications They meet the requirements of utility usability and safety Technological applications presented in the book have appropriate functions and they may be exploited with competitive advantages The book has 17 chapters covering the following subjects manufacturing technologies nanotechnologies robotics telecommunications physics dental medical technologies smart homes speech technologies agriculture technologies and management

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