SECOND EDITION

DIGITAL INTEGRATED CIRCUITS

A DESIGN PERSPECTIVE

JAN M. RABAEY ANANTHA CHANDRAKASAN BORIVOJE NIKOLIĆ

Unique in approach, this text offers comprehensive coverage of new design methodologies and implementation strategies of digital ICs and brings to the forefront a number of new issues that impact the reliability, cost, performance, and power dissipation of chips now reaching the 100-nm realm. This means increased importance in circuit design of deep submicron transistor effects, interconnect, signal integrity, high-performance and low-power design, timing and clock distribution. In contrast to the first edition, the present text focuses entirely on CMOS ICs. While introducing these changes, the text preserves the spirit and goals of the first edition—that is, it successfully bridges the gap between the circuit and system visions on digital design.

Beginning with solid discussions on the operation of electronic devices and in depth analysis of the nucleus of digital design—the inverter—the text gradually channels this knowledge into the design of more complex modules such as gates, controllers, adders, multipliers, and memories. Actual design examples and layouts are interspersed throughout the text. Besides numerous examples, each chapter also contains a number of problems or brain teasers that provoke thinking and understanding while reading. Fully worked-out design problems are available from the web site.

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The text is intended for use in digital circuit design courses conducted for undergraduate and postgraduate students of electronics.



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Saladukha, 2022-02-10 This book guides readers through the entire complex of interrelated theoretical and practical aspects of the end to end design and organization of production of silicon submicron integrated circuits The discussion includes the theoretical foundations of the operation of field effect and bipolar transistors the methods and peculiarities of the structural and schematic design basic circuit design and system design engineering solutions for bipolar CMOS BiCMOS and TTL integrated circuits standard design libraries and typical design flows Digital Control in Power Electronics, 2nd **Edition** Simone Buso, Paolo Mattavelli, 2022-05-31 This book presents the reader whether an electrical engineering student in power electronics or a design engineer a selection of power converter control problems and their basic digital solutions based on the most widespread digital control techniques. The presentation is primarily focused on different applications of the same power converter topology the half bridge voltage source inverter considered both in its single and three phase implementation This is chosen as the test case because besides being simple and well known it allows the discussion of a significant spectrum of the most frequently encountered digital control applications in power electronics from digital pulse width modulation DPWM and space vector modulation SVM to inverter output current and voltage control ending with the relatively more complex VSI applications related to the so called smart grid scenario This book aims to serve two purposes 1 to give a basic introductory knowledge of the digital control techniques applied to power converters and 2 to raise the interest for discrete time control theory stimulating new developments in its application to switching power converters

Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation Vassilis Paliouras.2005-09-06 This book constitutes the refereed proceedings of the 15th International Workshop on Power and Timing Optimization and Simulation PATMOS 2005 held in Leuven Belgium in September 2005 The 74 revised full papers presented were carefully reviewed and selected from numerous submissions. The papers are organized in topical sections on low power processors code optimization for low power high level design telecommunications and signal processing low power circuits system on chip design busses and interconnections modeling design automation low power techniques memory and register files applications digital circuits and analog and physical design **Electronic Circuits** Mike Tooley, 2019-11-07 Electronics explained in one volume using both theoretical and practical applications Mike Tooley provides all the information required to get to grips with the fundamentals of electronics detailing the underpinning knowledge necessary to appreciate the operation of a wide range of electronic circuits including amplifiers logic circuits power supplies and oscillators The 5th edition includes an additional chapter showing how a wide range of useful electronic applications can be developed in conjunction with the increasingly popular Arduino microcontroller as well as a new section on batteries for use in electronic equipment and some additional updated student assignments The book s content is matched to the latest pre degree level courses from Level 2 up to and including Foundation Degree and HND making this an invaluable reference text for all study levels and its broad coverage is combined with practical case studies based in real

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important with integrated circuits operating at higher frequencies since the inductive impedance is proportional to the frequency The operating frequencies of integrated circuits have increased dramatically over the past decade and are expected to maintain the same rate of increase over the next decade approaching 10 GHz by the year 2012 Also wide wires are frequently encountered in important global nets such as clock distribution networks and in upper metal layers and performance requirements are pushing the introduction of new materials for low resistance interconnect such as copper interconnect already used in many commercial CMOS technologies On Chip Inductance in High Speed Integrated Circuits deals with the design and analysis of integrated circuits with a specific focus on on chip inductance effects It has been described throughout this book that inductance can have a tangible effect on current high speed integrated circuits For example neglecting inductance and using an RC interconnect model in a production 0.25 mum CMOS technology can cause large errors over 35% in estimates of the propagation delay of on chip interconnect It has also been shown that including inductance in the repeater insertion design process as compared to using an RC model improves the overall repeater solution in terms of area power and delay with average savings of 40 8% 15 6% and 6 7% respectively On Chip Inductance in High Speed Integrated Circuitsis full of design and analysis techniques for RLC interconnect These techniques are compared to techniques traditionally used for RC interconnect design to emphasize the effect of inductance emOn Chip Inductance in High Speed Integrated Circuits will be of interest to researchers in the area of high frequency interconnect noise and high performance integrated circuit design Location-Based Services and Geo-Information Engineering Allan Brimicombe, Chao Li,2009-02-17 Location Based Services LBS are the delivery of data and information services where the content of those services is tailored to the current location and context of a mobile user This is a new and fast growing technology sector incorporating GIS wireless technologies positioning systems and mobile human computer interaction Geo Information GI Engineering is the design of dependably engineered solutions to society s use of geographical information and underpins applications such as LBS These are brought together in this comprehensive text that takes the reader through from source data to product delivery This book will appeal to professionals and researchers in the areas of GIS mobile telecommunications services and LBS It provides a comprehensive view and in depth knowledge for academia and industry alike It serves as essential reading and an excellent resource for final year undergraduate and postgraduate students in GIScience Geography Mobile Computing or Information Systems who wish to develop their understanding of LBS

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combinational and sequential circuits controllability and observability deterministic algorithms digital circuit dynamics timing verification built in self test BIST architecture scan cell operations and functional and diagnostic testing The book also discusses the automatic test pattern generation ATPG process the logical determinant theory and joint test action group JTAG interface models Digital Integrated Circuits explores the possibilities of MATLAB s tools in the development of application specific integrated circuit ASIC design systems The book shows how to incorporate Simulink and Stateflow into the process of modern digital design Plunkett's Almanac of Middle Market Companies: Middle Market Research, Statistics & Leading Companies Jack W. Plunkett, 2007-07 Plunkett s Almanac of Middle Market Companies 2008 is designed to be time saving business development tool for professionals marketers sales directors consultants and strategists seeking to understand and reach middle market American companies It will also be of great use to placement recruiting and human resources professionals as well as professionals working in economic development lending and media It covers competitive intelligence market research and business analysis everything you need to identify and develop strategies for middle market corporations Coverage includes all major business sectors from InfoTech to health care to telecommunications and much more We have intentionally omitted retail companies and banks These profiles and details on over 500 middle market firms are pulled from our extensive company and industry databases. We also include a business glossary and a listing of business contacts such as industry associations and government agencies Next we profile hundreds of leading middle market companies Our company profiles include complete business descriptions and up to 27 executives by name and title Purchasers of either the book or PDF version can receive a free copy of the company profiles database on CD ROM enabling key word search and export of key information addresses phone numbers and executive names with titles for every company profiled

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