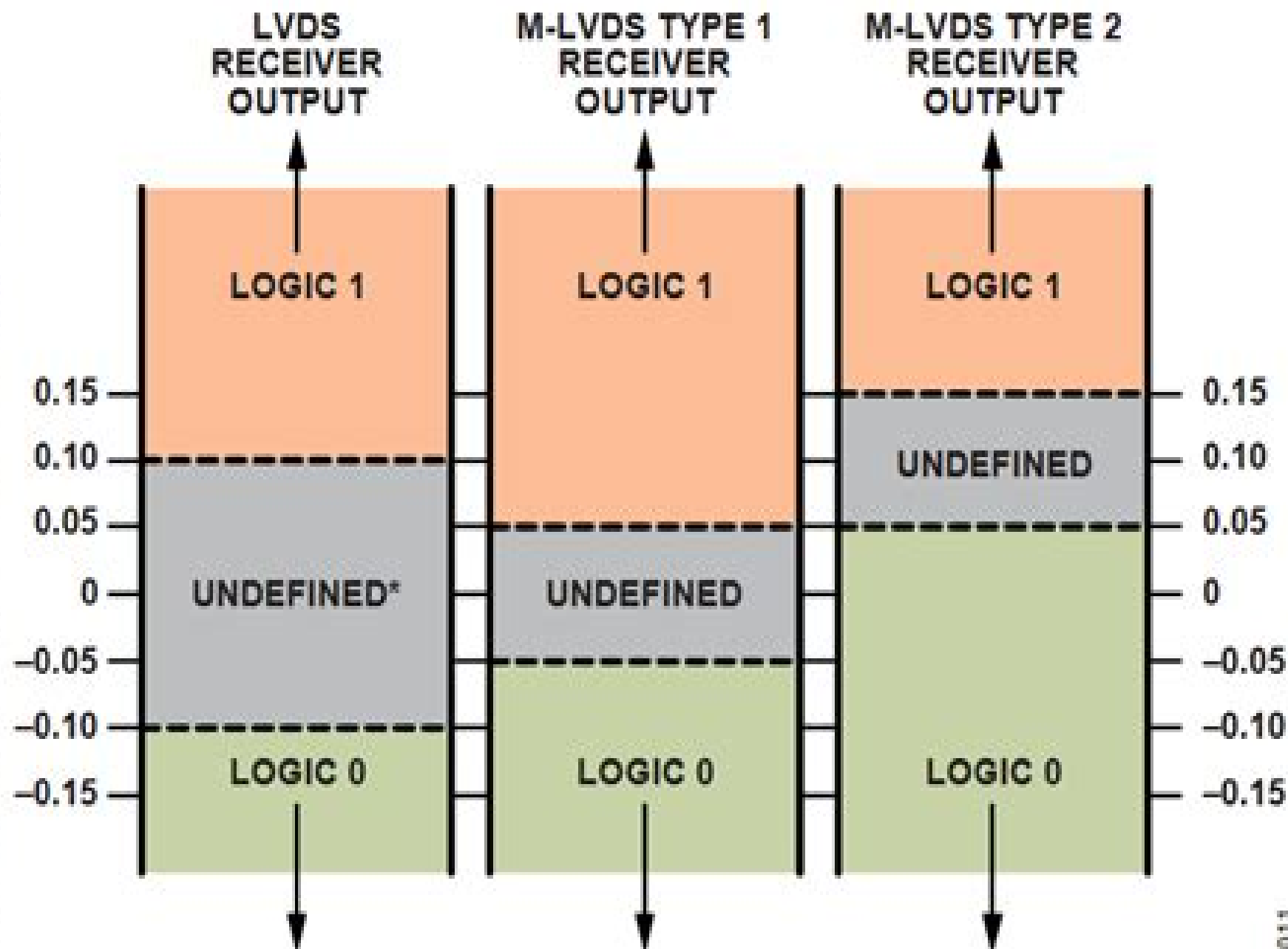


DIFFERENTIAL INPUT VOLTAGE ($V_{IA} - V_{IB}$) [V]



*LOGIC 1 FOR LVDS RECEIVERS WITH FAILSAFE

Lvds And M Lvds Circuit Implementation Guide

**Xingui He, Ertian Hua, Yun Lin, Xiaozhu
Liu**



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