

# DRAM

## Circuit Design

A TUTORIAL

**Brent Keeth • R. Jacob Baker**



IEEE Press Series on  
Integrated Circuits and Systems

Edited by H. Tokumitsu and J. E. Brewer,  
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# Dram Circuit Design A Tutorial Ieee Press Series On Microelectronic Systems

**Institute of Electrical and Electronics  
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## **Dram Circuit Design A Tutorial Ieee Press Series On Microelectronic Systems:**

**DRAM Circuit Design** Brent Keeth,R. Jacob Baker,2001 DRAM Circuit Design teaches readers the introductory level design of DRAM memory chips It focuses on giving readers a reference that can be used to educate students or practicing design engineers in DRAM circuit design      **Digital Electronics** Anil K. Maini,2007-09-27 The fundamentals and implementation of digital electronics are essential to understanding the design and working of consumer industrial electronics communications embedded systems computers security and military equipment Devices used in applications such as these are constantly decreasing in size and employing more complex technology It is therefore essential for engineers and students to understand the fundamentals implementation and application principles of digital electronics devices and integrated circuits This is so that they can use the most appropriate and effective technique to suit their technical need This book provides practical and comprehensive coverage of digital electronics bringing together information on fundamental theory operational aspects and potential applications With worked problems examples and review questions for each chapter Digital Electronics includes information on number systems binary codes digital arithmetic logic gates and families and Boolean algebra an in depth look at multiplexers de multiplexers devices for arithmetic operations flip flops and related devices counters and registers and data conversion circuits up to date coverage of recent application fields such as programmable logic devices microprocessors microcontrollers digital troubleshooting and digital instrumentation A comprehensive must read book on digital electronics for senior undergraduate and graduate students of electrical electronics and computer engineering and a valuable reference book for professionals and researchers      **DRAM Circuit Design** Brent Keeth,R. Jacob Baker,Brian Johnson,Feng Lin,2007-12-04 A modern comprehensive introduction to DRAM for students and practicing chip designers Dynamic Random Access Memory DRAM technology has been one of the greatest driving forces in the advancement of solid state technology With its ability to produce high product volumes and low pricing it forces solid state memory manufacturers to work aggressively to cut costs while maintaining if not increasing their market share As a result the state of the art continues to advance owing to the tremendous pressure to get more memory chips from each silicon wafer primarily through process scaling and clever design From a team of engineers working in memory circuit design DRAM Circuit Design gives students and practicing chip designers an easy to follow yet thorough introductory treatment of the subject Focusing on the chip designer rather than the end user this volume offers expanded up to date coverage of DRAM circuit design by presenting both standard and high speed implementations Additionally it explores a range of topics the DRAM array peripheral circuitry global circuitry and considerations voltage converters synchronization in DRAMs data path design and power delivery Additionally this up to date and comprehensive book features topics in high speed design and architecture and the ever increasing speed requirements of memory circuits The only book that covers the breadth and scope of the subject under one cover DRAM Circuit Design is an invaluable introduction for students in courses on memory circuit

design or advanced digital courses in VLSI or CMOS circuit design It also serves as an essential one stop resource for academics researchers and practicing engineers      *Reconfigurable Computing* Scott Hauck, André DeHon, 2010-07-26

Reconfigurable Computing marks a revolutionary and hot topic that bridges the gap between the separate worlds of hardware and software design the key feature of reconfigurable computing is its groundbreaking ability to perform computations in hardware to increase performance while retaining the flexibility of a software solution Reconfigurable computers serve as affordable fast and accurate tools for developing designs ranging from single chip architectures to multi chip and embedded systems Scott Hauck and Andre DeHon have assembled a group of the key experts in the fields of both hardware and software computing to provide an introduction to the entire range of issues relating to reconfigurable computing FPGAs field programmable gate arrays act as the computing vehicles to implement this powerful technology Readers will be guided into adopting a completely new way of handling existing design concerns and be able to make use of the vast opportunities possible with reconfigurable logic in this rapidly evolving field Designed for both hardware and software programmers Views of reconfigurable programming beyond standard programming languages Broad set of case studies demonstrating how to use FPGAs in novel and efficient ways      *German books in print* ,2002      **Nanoelectronic Mixed-Signal System Design** Saraju Mohanty, 2015-02-20

Covering both the classical and emerging nanoelectronic technologies being used in mixed signal design this book addresses digital analog and memory components Winner of the Association of American Publishers 2016 PROSE Award in the Textbook Physical Sciences Mathematics category Nanoelectronic Mixed Signal System Design offers professionals and students a unified perspective on the science engineering and technology behind nanoelectronics system design Written by the director of the NanoSystem Design Laboratory at the University of North Texas this comprehensive guide provides a large scale picture of the design and manufacturing aspects of nanoelectronic based systems It features dual coverage of mixed signal circuit and system design rather than just digital or analog only Key topics such as process variations power dissipation and security aspects of electronic system design are discussed Top down analysis of all stages from design to manufacturing Coverage of current and developing nanoelectronic technologies not just nano CMOS Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability

BiCMOS Integrated Circuit Design IEEE Solid-State Circuits Council, 1994 This up to date book with its introductory tutorial provides extensive coverage on BiCMOS potentially the most important silicon technology of the 1990 s Many commercial products are currently utilizing BiCMOS ICs and an exponential growth is expected Elmasry provides the latest information on processing technologies circuit analysis and techniques and applications in the areas of analog digital and smart power BiCMOS Integrated Circuit Design is an important guide for engineers working in BiCMOS processing modeling characterization circuit design and applications as well as a valuable learning tool for college seniors and graduate students

**Electrical & Electronics Abstracts** ,1997      **Tutorial--gallium Arsenide Computer Design** Veljko

Milutinović, David A. Fura, 1988      Silicon, From Sand to Chips, Volume 2 Alain Vignes, 2024-06-04 Silicon is the material of the digital revolution of solar energy and of digital photography which has revolutionized both astronomy and medical imaging It is also the material of microelectromechanical systems MEMS indispensable components of smart objects The discovery of the electronic and optoelectronic properties of germanium and silicon during the Second World War followed by the invention of the transistor ushered in the digital age Although the first transistors were made from germanium silicon eventually became the preferred material for these technologies Silicon From Sand to Chips 2 traces the history of the discoveries inventions and developments in basic components and chips that these two materials enabled one after the other The book is divided into two volumes and this second volume is devoted to microelectronic and optoelectronic chips solar cells and MEMS      **Index to IEEE Publications** Institute of Electrical and Electronics Engineers, 1994

**Documentation Abstracts** ,1985      **CMOS, Circuit Design, Layout, and Simulation** R. Jacob Baker, Harry W.

Li, David E. Boyce, 1997-08-22 This exceptionally comprehensive tutorial presentation of complementary metal oxide semiconductor CMOS integrated circuits will guide you through the process of implementing a chip from the physical definition through the design and simulation of the finished chip CMOS CIRCUIT DESIGN LAYOUT AND SIMULATION provides an important contemporary view of a wide range of circuit blocks the BSIM model data converter architectures and much more Outstanding features of this text include Phase and delay locked loops mixed signal circuits and data converters More than 1 000 figures 200 examples and over 500 end of chapter problems In depth coverage of both analog and digital circuit level design techniques Real world process parameters and design rules Information on MOSIS fabrication procedures and other key topics of interest Information and directions on submitting chips of MOSIS Tutorial presentation of material suitable for self study or as a university textbook Numerous examples and homework problems For more information and links related to CMOS design go to <http://cmosedu.com> Professors To request an examination copy simply e mail collegeadoption@ieee.org Sponsored by IEEE Solid State Circuits Council Society IEEE Circuits and Systems Society

**CMOS** R. Jacob Baker, 2005 As with the first edition of CMOS Circuit Design Layout and Simulation the book provides an important contemporary view of a wide range of analog digital circuit blocks the BSIM model data converter architectures and much more      **CMOS** R. Jacob Baker, 2002-06-17 An important continuation to CMOS Circuit Design Layout and

Simulation The power of mixed signal circuit designs and perhaps the reason they are replacing analog only designs in the implementation of analog interfaces comes from the marriage of analog circuits with digital signal processing This book builds on the fundamental material in the author's previous book CMOS Circuit Design Layout and Simulation to provide a solid textbook and reference for mixed signal circuit design The coverage is both practical and in depth integrating experimental theoretical and simulation examples to drive home the why and the how of doing mixed signal circuit design

Some of the highlights of this book include A practical theoretical approach to mixed signal circuit design with an emphasis on oversampling techniques An accessible and useful alternative to hard to digest technical papers without losing technical depth Coverage of delta sigma data converters custom analog and digital filter design design with submicron CMOS processes and practical at the bench deadbug prototyping techniques Hundreds of worked examples and questions covering all areas of mixed signal circuit design A helpful companion Web site <http://cmosedu.com> provides worked solutions to textbook problems SPICE simulation netlist examples and discussions concerning mixed signal circuit design

**Mixed Design of Integrated Circuits and Systems** Andrzej Napieralski, 1998 Very fast advances in IC technologies have brought new challenges into the physical design of integrated systems The emphasis on system performance in present day applications requires timing and power constraints to be considered at each stage of physical design The size of ICs is decreasing continually and the density of power dissipated in the circuits is growing rapidly Mixed Design of Integrated Circuits and Systems presents a variety of topics in the area of microelectronics design and microsystems written by experts in technology modelling analog and digital filters design signal processing neural network design thermal design design methodologies electromagnetic compatibility sensors and actuators advanced testing methods and reliability power devices and Smart Power modules The book is divided into five parts Analog Circuit Design Power Devices and Thermal Aspects Microsystems and Neural Networks Design Methodologies Advanced Trends in Microelectronics Education Mixed Design of Integrated Circuits and Systems serves as an excellent resource for engineers involved in VLSI circuits design and may also be used as a text for advanced courses on the subject

**VLSI Memory Chip Design** Kiyoo Itoh, 2010-11-10 A systematic description of microelectronic device design Topics range from the basics to low power and ultralow voltage designs subthreshold current reduction memory subsystem designs for modern DRAMs and various on chip supply voltage conversion techniques It also covers process and device issues as well as design issues relating to systems circuits devices and processes such as signal to noise and redundancy

**Nanometer SRAM and DRAM Circuit Design** Paul S. Lazar, Yong-Bin Kim, Ban P. Wong, 2015-03-02 Low power consumption is moving higher up on the priority list of system requirements resulting in low power memory architectures and circuit implementations Circuits need to operate reliably at low voltages below 1.0V yet meet their performance targets The larger spread of process variations as the technology node shrinks makes worst case design impractical These design challenges impact also the peripheral control ancillary and i/o circuits of the SRAM and DRAM The goal of this book is to describe circuits and circuit design methodologies which overcome these challenges Examples of scripts which are used to steer the CAD tools used in the analysis of the circuits and to gather and present the results of such analysis are provided Since a variety of the emerging technologies such as CNT Carbon Nano Tube and FinFET are being developed and researched a new design method for memory cell will also be discussed based on those emerging technologies

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