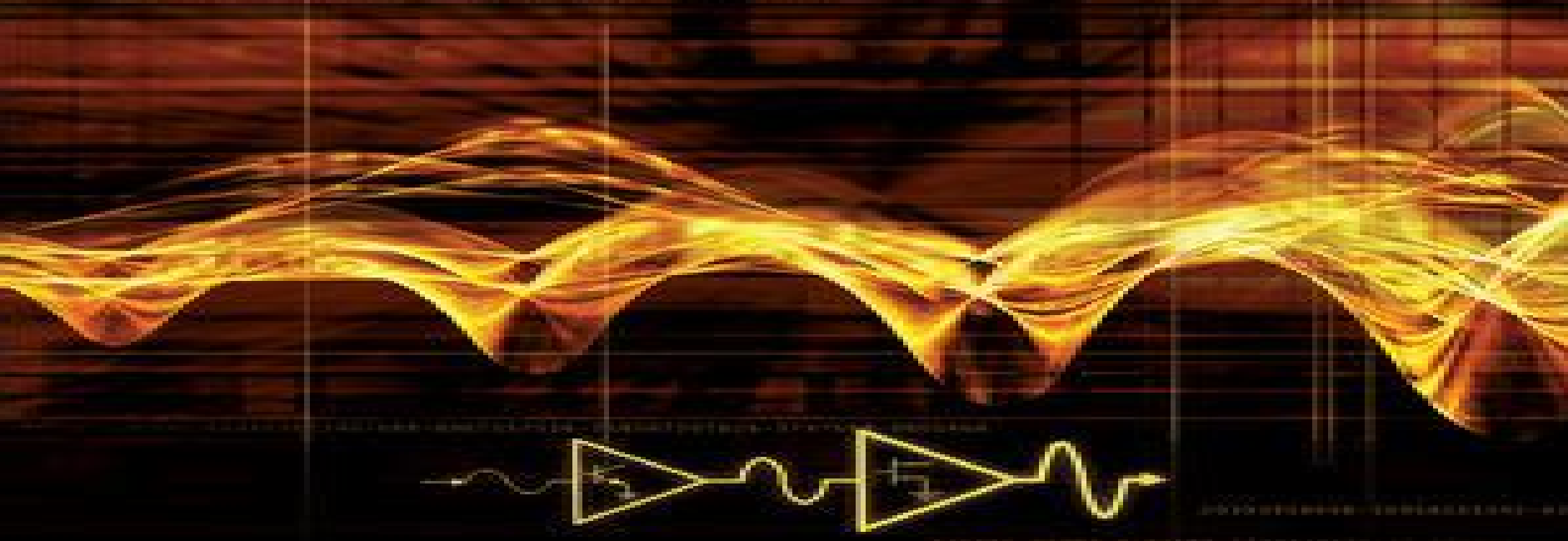


# Fundamentals of RF and Microwave Transistor Amplifiers



INDER J. BAHL

 WILEY

# Fundamentals Of Rf And Microwave Transistor Amplifiers

**Sebastian Preis**



## **Fundamentals Of Rf And Microwave Transistor Amplifiers:**

Fundamentals of RF and Microwave Transistor Amplifiers Inder Bahl, 2009-06-17 A Comprehensive and Up to Date Treatment of RF and Microwave Transistor Amplifiers This book provides state of the art coverage of RF and microwave transistor amplifiers including low noise narrowband broadband linear high power high efficiency and high voltage Topics covered include modeling analysis design packaging and thermal and fabrication considerations Through a unique integration of theory and practice readers will learn to solve amplifier related design problems ranging from matching networks to biasing and stability More than 240 problems are included to help readers test their basic amplifier and circuit design skills and more than half of the problems feature fully worked out solutions With an emphasis on theory design and everyday applications this book is geared toward students teachers scientists and practicing engineers who are interested in broadening their knowledge of RF and microwave transistor amplifier circuit design Reliable RF Power Amplifier Design Based on a Partitioning Design Approach Rui Ma, 2010 Front cover Titelseite Impressum Acknowledgments Contents List of Abbreviations and Acronyms Abstract Zusammenfassung Chapter 1 Introduction 1 1 Principle of the Partitioning Design Approach 1 2 Dissertation Organization Chapter 2 Investigation of Planar Interconnection 2 1 Active Chip Device Interconnection 2 1 1 Die Attach 2 1 2 Wire Bonding Pad To Microstrip 2 2 Microstrip to Microstrip Interconnection 2 2 1 Soldering 2 2 2 Multi Wire Bonding 2 2 3 Copper Ribbon 2 2 4 Silver Painting Chapter 3 Analysis and Modeling of Passive SMD Components 3 1 SMD Resistor 3 2 SMD Capacitor 3 3 SMD Inductor Chapter 4 Modeling of AlGaAs GaAs HEMT Chip Device 4 1 AlGaAs GaAs HEMT Chip 4 2 Modeling Approach Overview 4 3 Small Signal Modeling 4 3 1 Extrinsic Parameter Extraction 4 3 2 Intrinsic Parameter Extraction 4 4 Large Signal Modeling 4 4 1 Gate Current and Charge Models 4 4 2 Drain Current Model 4 4 3 Model Verification Chapter 5 Demonstrator Design of a Class AB Power Amplifier Following 5 1 Micro Packaged Device Characterization 5 1 1 Small Signal Performance 5 1 2 Large Signal Performance 5 2 Bias Network Design 5 2 1 Drain Bias Network 5 2 2 Gate Bias Network 5 3 Matching Network Design 5 3 1 Matching Impedance Determination 5 4 Power Amplifier Performance Evaluation 5 4 1 Small Signal Performance 5 4 2 Large Signal Performance Chapter 6 Conclusions and Outlook Appendix Appendix A THLR In Fixture Calibration Appendix B Precise Determination of Substrate Permittivity Appendix C Schematic Circuit of the Designed Power Amplifier Demonstrator Appendix D Power Amplifier Design Following the Conventional Design Approach References Back cover **Handbook of RF and Microwave Power Amplifiers** John L. B. Walker, 2012 This is a one stop guide for circuit designers and system device engineers covering everything from CAD to reliability **HF-Leistungsverstärker in modernen Silizium- und Verbindungshalbleitern-Technologien** Daniel Gruner, 2012-11-01 Kurzbeschreibung Diese Dissertation beschäftigt sich mit HF Leistungsverstärkern auf Basis moderner Transistortechnologien und verfolgt aktuelle Trends im Bereich Silizium und III V Verbindungshalbleiter Dies umfasst den Entwurf von innovativen HF Leistungstransistoren und Verstärkern Aspekte zur

Charakterisierung und Aufbautechnik sowie den Vergleich zum aktuellen Stand von Wissenschaft und Technik Im ersten Teil richtet sich der Fokus auf MMIC Leistungsverstärker für 5,6 GHz WLAN Anwendungen Hierbei wird zunächst auf den Entwurf eines Push Pull PAs in einer 0,25 µm BiCMOS Technologie eingegangen Mit einer Betriebsspannung von lediglich 1,8 V zeigt dieser Verstärker ein breitbandiges Verhalten und erreicht im Frequenzbereich 5,6–6,2 GHz eine Sättigungsleistung von 24 dBm Der maximale Wirkungsgrad liegt bei 27 % PAE 18 % mit einer Verstärkung von typischerweise 9 dB Um diese Performance weiter zu verbessern wird eine innovative Transformer Combining Topologie vorgestellt Im Vergleich zum Push Pull Verstärker kann diese eine erhöhte Verstärkung 12 dB sowie einen exzellenten PAE Maximalwert von 23 % bei sonst ähnlichen Eigenschaften erzielen Als zweite Zielstellung wird die Steigerung der HF Leistungs Performance innerhalb moderner BiCMOS Technologien verfolgt In diesem Rahmen stehen die Analyse der Entwurf und die Evaluierung von LDMOS Transistoren für Anwendungen bis in den 6 GHz Bereich im Mittelpunkt Der Einfluss von Transistorgeometrie und Geometrie sowie auch der Stabilitätsnetzwerke wird unter Verwendung HF optimierter LDMOS Strukturen untersucht Die Evaluierung der Source Load Load Pull Messungen wird mit Hilfe mehrerer hybrider Verstärkermodule durchgeführt Als eine der ersten Veröffentlichungen kann diese Arbeit eine 6 GHz LDMOS Implementation in einer Standard BiCMOS Technologie mit einer Ausgangsleistung von mehr als 1 W und einem Wirkungsgrad über 40 % demonstrieren Der dritte Teil beschäftigt sich mit GaN Doherty Verstärkern für das 6 GHz Band Die Kombination von Doherty Konzept mit Strategien zur Harmonischen Terminierung einem auf Device Referenzebene optimierten Entwurf und der entwickelten Chip Wire Aufbautechnik erlaubt die Realisierung von einem der ersten 6 GHz Doherty Verstärker mit exzellenter Wirkungsgrad Leistungs sowie Linearitäts Performance Unter Verwendung zweier GaN Chip Transistoren kann eine maximale Ausgangsleistung von 41,5 dBm mit einem maximalen Wirkungsgrad von 63 % erreicht werden Im 6 dB Backoff Punkt wird dabei ein exzellenter Wirkungsgrad von 49 % erzielt

Description This work focuses on RF power amplifiers based on modern transistor technologies and investigates current trends in silicon as well as III V semiconductors This includes the design of innovative RF power devices and amplifiers characterization and assembling techniques as well as the comparison to other state of the art achievements The first part focuses on MMIC power amplifiers for WLAN applications in the 5,6 GHz range Based on a 0,25 µm BiCMOS technology a low voltage push pull power amplifier using integrated transformers is presented At a supply voltage of 1,8 V this fully integrated amplifier achieves a wideband performance showing a maximum output power of 24 dBm across 5,6–6,2 GHz A maximum efficiency of 27 % PAE 18 % is obtained with a small signal gain of typically 9 dB To further improve this performance an innovative transformer combining topology is developed Compared to the push pull amplifier this topology achieves an increased gain of 12 dB and an enhanced maximum PAE of up to 23 % with otherwise similar characteristics showing state of the art performance The RF power enhancement of BiCMOS technologies is a second major aim of this work In this context the analysis design and evaluation of medium voltage LDMOS FETs for wireless applications up to 6 GHz

is presented Using RF optimized NLD MOS transistors power devices of different sizes were fabricated in a standard 0.25  $\mu\text{m}$  BiCMOS technology The influences of the finger geometry and the stabilization networks on the RF performance are studied Power evaluation of the LDMOS transistors is carried out by means of hybrid power amplifier modules As one of the first contributions this work shows a 6 GHz LDMOS implementation in a standard BiCMOS technology that achieves an output power of greater than 1 W with a drain efficiency of more than 40 % In the third part a harmonically tuned GaN Doherty amplifier targeted for vehicular applications at 6 GHz is presented The combination of the Doherty concept with harmonic tuning strategies an optimized design approach and the developed chip wire assembly technology results in one of the first Doherty amplifiers implemented for the 6 GHz frequency range with excellent efficiency output power and linearity performance Using two bare die GaN devices a maximum output power of 41.5 dBm with 63% peak drain efficiency is obtained At 6 dB output power back off an excellent drain efficiency of still 49 % can be achieved **Methoden und**

**Techniken zur Integration von 122 GHz Antennen in miniaturisierte Radarsensoren** Stefan Beer, 2014-05-22 Die Dissertation beschreibt Methoden und Techniken zur Realisierung eines vollintegrierten Radarsensors im Frequenzbereich oberhalb von 100 GHz Der hohe Frequenzbereich ermöglicht die Integration von Antennen und einem Radar IC in ein gemeinsames Chip Gehäuse da die Antenne durch die Wellenlänge im Millimeterbereich ähnliche Abmessungen hat wie der IC selbst Durch diese Integration resultiert ein abgeschlossenes Hochfrequenzsystem innerhalb des Gehäuses

**Hocheffiziente frequenzagile Mikrowellen-Leistungsverstärker auf Basis von Verbindungshalbleitern und Ferroelektrika** Sebastian Preis, 2019-05-28 Diese Arbeit beschäftigt sich mit dem Entwurf der Optimierung und Charakterisierung von GaN HEMT Leistungsverstärkern für Mobilfunkanwendungen Der Fokus liegt hierbei auf steuerbaren Transformationsnetzwerken auf Basis elektronisch anpassbarer Barium Strontium Titanat BST Varaktoren Dabei konnte die Leistungsverträglichkeit jener BST Varaktoren bis zu 50 W gesteigert werden verbunden mit einer guten Linearität und geringen Temperaturabhängigkeit Dies zielte darauf den Wirkungsgrad von Leistungsverstärkern bei Leistungen deutlich unterhalb der Sättigung zu verbessern Die so entwickelten steuerbaren Transformationsnetzwerke sind für gehäusintegrierte Transistormodule optimiert Auf diese Weise lassen sich platzsparende und zuerst flexibel einsetzbare Komponenten für Hochfrequenzanwendungen realisieren Die Transistormodule erreichen einen hohen Wirkungsgrad von bis zu 72% PAE in der Sättigung Sie verbessern zudem die PAE bei reduzierter Leistung um mehrere Prozentpunkte gegenüber statisch angepassten Transistoren Darüber hinaus sind sie über der Frequenz im S-Band nicht nur gut steuerbar sondern lassen sich auch durch Änderung der Steuerspannung auf optimalen Wirkungsgrad oder Linearität bei verschiedenen Leistungspegeln anpassen Auch hybride Verstärker werden vorgestellt in denen diskret aufgebaute Varaktoren verbaut sind Dank der verwendeten Modulatoren für welche ebenfalls GaN Transistoren und Dioden genutzt werden bieten die Verstärker das Potenzial zur Lastmodulation Dadurch steigt die Effizienz beim Betrieb mit modulierten Signalen Für LTE äquivalente Signale

sst sich die mittlere PAE um mehrere Prozentpunkte erhöhen ohne die Linearität in einem System mit Vorverzerrung zu belasten

**Practical RF Amplifier Design and Performance Optimization with SPICE and Load- and Source-pull Techniques** Amal Banerjee, 2021-03-01 This book explains and demonstrates with an exhaustive set of design examples how common types of radio frequency RF amplifiers classes A B AB C D E F G and H can be designed and then have their performance characteristics evaluated and optimized with SPICE The author demonstrates the transient analysis features of SPICE along with industry standard load and source pull techniques to simulate the steady state long term time domain behavior of any test RF amplifier Describes methods for designing and evaluating optimizing the performance characteristics of an RF amplifier that circumvent the issues involved with existing traditional methods and don't require expensive high end software tools Includes C language executables for each RF amplifier type eliminating errors that might creep in while computing passive component capacitor inductor resistor values for a given RF amplifier type Demonstrates industry standard load and source pull schemes that can be included easily in text SPICE netlists allowing accurate calculation of impedance matching and impedance values at the input and output ports of the test RF amplifier eliminating messy error prone S parameter based calculations

Microwave Circuit Design Using Linear and Nonlinear Techniques George D. Vendelin, Anthony M. Pavio, Ulrich L. Rohde, Matthias Rudolph, 2021-04-27 Four leaders in the field of microwave circuit design share their newest insights into the latest aspects of the technology The third edition of Microwave Circuit Design Using Linear and Nonlinear Techniques delivers an insightful and complete analysis of microwave circuit design from their intrinsic and circuit properties to circuit design techniques for maximizing performance in communication and radar systems This new edition retains what remains relevant from previous editions of this celebrated book and adds brand new content on CMOS technology GaN SiC frequency range and feedback power amplifiers in the millimeter range region The third edition contains over 200 pages of new material The distinguished engineers academics and authors emphasize the commercial applications in telecommunications and cover all aspects of transistor technology Software tools for design and microwave circuits are included as an accompaniment to the book In addition to information about small and large signal amplifier design and power amplifier design readers will benefit from the book's treatment of a wide variety of topics like An in depth discussion of the foundations of RF and microwave systems including Maxwell's equations applications of the technology analog and digital requirements and elementary definitions A treatment of lumped and distributed elements including a discussion of the parasitic effects on lumped elements Descriptions of active devices including diodes microwave transistors heterojunction bipolar transistors and microwave FET Two port networks including S Parameters from SPICE analysis and the derivation of transducer power gain Perfect for microwave integrated circuit designers the third edition of Microwave Circuit Design Using Linear and Nonlinear Techniques also has a place on the bookshelves of electrical engineering researchers and graduate students It's comprehensive take on all aspects of transistors by world renowned experts in the

field places this book at the vanguard of microwave circuit design research **CMOS Low Noise Amplifiers for Single and Multiband Applications: A Comprehensive Design Approach** Norlaili Mohd Noh, Farshad Eshghabadi, Arjuna Marzuki, 2023-10-11 This book provides comprehensive knowledge aimed at practicing integrated circuit design engineer or researcher to learn and design a low noise amplifier LNA for single and multiband applications The content is structured in a way so that even a beginner can follow the design method easily This book features the following characteristics different types of LNA designs with key building blocks are discussed and detailed analysis is given for each LNA design which covers from the fundamental and principal knowledge to the justification of the design approach Detailed design approaches are using 180 nm and 130nm CMOS technologies purposely presented in this manner to give exposure to the design of LNA under different technologies The LNAs in this book are designed for GSM WCDMA and WLAN standards but the same method can be used for other frequencies of operation Comprehensive analyses on the extreme or corner condition effects are highlighted Besides detailed derivation of equations relating to the parameters of the LNA s performance metrics help LNA designers in understanding how the performance metrics of the LNA can be optimized to meet the desired specification Electromagnetic analyses using Sonnet an electromagnetic tool able to replace the conventional post layout simulation with resistance and capacitance parasitic extraction for more accurate frequency performance prediction are presented The electromagnetic method is proposed to be used in the LNA design as it can accurately predict the LNA s performance before tape out for first pass fabrication MATLAB codes are provided to generate important S parameters and noise figure values

Wide Bandgap Based Devices Farid Medjdoub, 2021-05-26 Emerging wide bandgap WBG semiconductors hold the potential to advance the global industry in the same way that more than 50 years ago the invention of the silicon Si chip enabled the modern computer era SiC and GaN based devices are starting to become more commercially available Smaller faster and more efficient than their counterpart Si based components these WBG devices also offer greater expected reliability in tougher operating conditions Furthermore in this frame a new class of microelectronic grade semiconducting materials that have an even larger bandgap than the previously established wide bandgap semiconductors such as GaN and SiC have been created and are thus referred to as ultra wide bandgap materials These materials which include AlGaIn AlN diamond Ga<sub>2</sub>O<sub>3</sub> and BN offer theoretically superior properties including a higher critical breakdown field higher temperature operation and potentially higher radiation tolerance These attributes in turn make it possible to use revolutionary new devices for extreme environments such as high efficiency power transistors because of the improved Baliga figure of merit ultra high voltage pulsed power switches high efficiency UV LEDs and electronics This Special Issue aims to collect high quality research papers short communications and review articles that focus on wide bandgap device design fabrication and advanced characterization The Special Issue will also publish selected papers from the 43rd Workshop on Compound Semiconductor Devices and Integrated Circuits held in France WOCSDICE 2019 which brings together

scientists and engineers working in the area of III V and other compound semiconductor devices and integrated circuits In particular the following topics are addressed GaN and SiC based devices for power and optoelectronic applications Ga<sub>2</sub>O<sub>3</sub> substrate development and Ga<sub>2</sub>O<sub>3</sub> thin film growth doping and devices AlN based emerging material and devices BN epitaxial growth characterization and devices

**mm-Wave Silicon Power Amplifiers and Transmitters** Hossein Hashemi, Sanjay Raman, 2016-04-04 Build high performance spectrally clean energy efficient mm wave power amplifiers and transmitters with this cutting edge guide to designing modeling analysing implementing and testing new mm wave systems Suitable for students researchers and practicing engineers this self contained guide provides in depth coverage of state of the art semiconductor devices and technologies linear and nonlinear power amplifier technologies efficient power combining systems circuit concepts system architectures and system on a chip realizations The world's foremost experts from industry and academia cover all aspects of the design process from device technologies to system architectures Accompanied by numerous case studies highlighting practical design techniques tradeoffs and pitfalls this is a superb resource for those working with high frequency systems

**Millimeter Wave and Terahertz Devices for 5G and 6G systems** K. Vasu Babu, Sudipta Das, Gorre Naga Jyothi Sree, Varakumari Samudrala, Beulah Jackson, 2025-08-10 This book explores wireless communication elements focusing on mm and THz wave generation specifications material innovations machine learning integration and applications Computational methods like genetic algorithms and artificial neural networks optimize mm wave and THz devices The microwave spectrum is often crowded making millimeter mm wave and terahertz frequencies the preferred choice for next generation high end applications Millimeter wave mm Wave fifth generation 5G communication technology addresses reduced time delays increased data transmission speeds and minimized energy consumption crucial for diverse user devices While 5G networks advance with Multiple Input Multiple Output MIMO multiplexing and mm wave communications the THz band offers even greater spectrum availability for systems like 6G The surge in THz systems research aims to meet expanding technological demands promising unprecedented data rates THz wave technology finds applications in wireless communications remote sensing and chemical analysis For THz wave technologists this book is a valuable resource covering research trends and demands along with computational simulation methods Topics include Terahertz passive circuit modeling mm wave device simulation Terahertz metrology data transmission via mm wave and THz signals high speed channel modeling antenna design graphene applications in 6G devices THz absorbers and sensors

*Space Fostering African Societies* Annette Froehlich, 2022-01-01 This peer reviewed book provides detailed insights into how space and its applications are and can be used to support the development of the full range and diversity of African societies as encapsulated in the African Union's Agenda 2063 Following on from Part 1 and 2 which were highly acclaimed by the space community it focuses on the role of space in supporting the UN Sustainable Development Goals in Africa but covers an even more extensive array of relevant and timely topics addressing all facets of African development It demonstrates that



while there have been significant achievements in recent years in terms of economic and social development which have lifted many of Africa's people out of poverty there is still a great deal that needs to be done to fulfill the basic needs of Africa's citizens and afford them the dignity they deserve To this end space is already being employed in diverse fields of human endeavor to serve Africa's goals for its future but there is much room for further incorporation of space systems and data Providing a comprehensive overview of the role space is playing in helping Africa achieve its developmental aspirations the book will appeal to both students and professionals in fields such as space studies international relations governance and social and rural development

**Device Physics, Modeling, Technology, and Analysis for Silicon MESFET** Iraj Sadegh Amiri, Hossein Mohammadi, Mahdiar Hosseinghadiry, 2018-12-13 This book provides detailed and accurate information on the history structure operation benefits and advanced structures of silicon MESFET along with modeling and analysis of the device The authors explain the detailed physics that are important in modeling of SOI MESFETs and present the derivations of compact model expressions so that users can recognize the physical meaning of the model equations and parameters The discussion also includes advanced structures for SOI MESFET for submicron applications

*Advances in Time-Domain Computational Electromagnetic Methods* Qiang Ren, Su Yan, Atef Z. Elsherbeni, 2022-11-15 *Advances in Time Domain Computational Electromagnetic Methods* Discover state of the art time domain electromagnetic modeling and simulation algorithms *Advances in Time Domain Computational Electromagnetic Methods* delivers a thorough exploration of recent developments in time domain computational methods for solving complex electromagnetic problems The book discusses the main time domain computational electromagnetics techniques including finite difference time domain FDTD finite element time domain FETD discontinuous Galerkin time domain DGTD time domain integral equation TDIE and other methods in electromagnetic multiphysics modeling and simulation and antenna designs The book bridges the gap between academic research and real engineering applications by comprehensively surveying the full picture of current state of the art time domain electromagnetic simulation techniques Among other topics it offers readers discussions of automatic load balancing schemes for DG FETD SETD methods and convolution quadrature time domain integral equation methods for electromagnetic scattering *Advances in Time Domain Computational Electromagnetic Methods* also includes Introductions to cylindrical spherical and symplectic FDTD as well as FDTD for metasurfaces with GSTC and FDTD for nonlinear metasurfaces Explorations of FETD for dispersive and nonlinear media and SETD DDM for periodic quasi periodic arrays Discussions of TDIE including explicit marching on in time solvers for second kind time domain integral equations TD SIE DDM and convolution quadrature time domain integral equation methods for electromagnetic scattering Treatments of deep learning including time domain electromagnetic forward and inverse modeling using a differentiable programming platform Ideal for undergraduate and graduate students studying the design and development of various kinds of communication systems as well as professionals working in these fields *Advances in Time Domain Computational Electromagnetic Methods* is

also an invaluable resource for those taking advanced graduate courses in computational electromagnetic methods and simulation techniques

**Proceedings of Fifth International Conference on Computer and Communication Technologies** B. Rama Devi, Kishore Kumar, M. Raju, K. Srujan Raju, Mathini Sellathurai, 2024-02-29 This book is a compilation of high quality scientific papers presented at the 5th International Conference on Computer Communication Technologies IC3T 2023 The book covers cutting edge technologies and applications of soft computing artificial intelligence and communication In addition a variety of further topics are discussed which include data mining machine intelligence fuzzy computing sensor networks signal and image processing human computer interaction and web intelligence

*Handbook of Research on Recent Developments in Electrical and Mechanical Engineering* Zbitou, Jamal, Pruncu, Catalin Iulian, Errkik, Ahmed, 2019-09-27 Technological advancements continue to enhance the field of engineering and have led to progress in branches that include electrical and mechanical engineering These technologies have allowed for more sophisticated circuits and components while also advancing renewable energy initiatives With increased growth in these fields there is a need for a collection of research that details the variety of works being studied in our globalized world The Handbook of Research on Recent Developments in Electrical and Mechanical Engineering is a pivotal reference source that discusses the latest advancements in these engineering fields Featuring research on topics such as materials manufacturing microwave photons and wireless power transfer this book is ideally designed for graduate students researchers engineers manufacturing managers and academicians seeking coverage on the works and experiences achieved in electrical and mechanical engineering

*Handbook of Integrated Circuit Industry* Yangyuan Wang, Min-Hwa Chi, Jesse Jen-Chung Lou, Chun-Zhang Chen, 2023-11-27 Written by hundreds of experts who have made contributions to both enterprise and academic research these excellent reference books provide all necessary knowledge of the whole industrial chain of integrated circuits and cover topics related to the technology evolution trends fabrication applications new materials equipment economy investment and industrial developments of integrated circuits Especially the coverage is broad in scope and deep enough for all kinds of readers being interested in integrated circuit industry Remarkable data collection update marketing evaluation enough working knowledge of integrated circuit fabrication clear and accessible category of integrated circuit products and good equipment insight explanation etc can make general readers build up a clear overview about the whole integrated circuit industry This encyclopedia is designed as a reference book for scientists and engineers actively involved in integrated circuit research and development field In addition this book provides enough guide lines and knowledges to benefit enterprisers being interested in integrated circuit industry

**Proceeding of the International Conference on Connected Objects and Artificial Intelligence (COCIA2024)** Youssef Mejdoub, Abdelkebir Elamri, 2024-10-12 This book presents recent advances on Connected Objects Systems Telecommunications Artificial Intelligence and Electronic Engineering On the connected objects side the proceedings covered advancements in areas like sensor miniaturization and networking to enable

ever more ubiquitous and autonomous IoT deployments The AI focused contributions explored novel machine learning architectures and training techniques tailored for resource constrained edge devices Key breakthroughs included federated learning models In the telecommunications realm the proceedings examined the critical role of 5G 6G and satellite communications in providing the high bandwidth low latency connectivity required to unlock the full potential of AI powered connected systems This book is a collection of high quality research papers presented at the 2nd International Conference on Connected Objects and Artificial Intelligence COCIA 2024 held at High School of Technology Hassan II University Casablanca Morocco during 08 10 May 2024 This book features cutting edge research and insights at the intersection of the important technology domains Connected Objects Systems Telecommunications Artificial Intelligence and Electronic Engineering It is designed for researchers academicians professionals and graduates seeking to deepen their understanding and expertise at the intersection of IoT AI Telecommunications and Electronic Engineering This book includes In depth exploration of the latest advancements in connected objects and systems to enable autonomous IoT deployments Detailed examinations of cutting edge AI techniques optimized for edge computing environments including federated learning and IA model compression Insights into the critical role of 5G 6G and satellite communications in providing the high performance connectivity required to unlock the full potential of intelligent AI powered IoT applications With contributions from leading experts across academia and industry this book equips readers with the knowledge and tools to drive innovation at the forefront of the connected intelligence revolution It is an essential resource for anyone seeking to advance the state of the art in this rapidly evolving field

**Wireless Transceiver Design** Ariel Luzzatto, Motti Haridim, 2016-09-08 Building upon the success of the first edition 2007 Wireless Transceiver Design 2nd Edition is an accessible textbook that explains the concepts of wireless transceiver design in detail The architectures and the detailed design of both traditional and advanced all digital wireless transceivers are discussed in a thorough and systematic manner while carefully watching out for clarity and simplicity Many practical examples and solved problems at the end of each chapter allow students to thoroughly understand the mechanisms involved to build confidence and enable them to readily make correct and practical use of the applicable results and formulas From the instructors perspective the book will enable the reader to build courses at different levels of depth starting from the basic understanding whilst allowing them to focus on particular elements of study In addition to numerous fully solved exercises the authors include actual exemplary examination papers for instructors to use as a reference format for student evaluation The new edition has been adapted with instructors lecturers graduate undergraduate students and RF engineers in mind Non RF engineers looking to acquire a basic understanding of the main related RF subjects will also find the book invaluable

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