



# Circuit And Numerical Modeling Of Electrostatic Discharge

**Narain D. Arora**



## **Circuit And Numerical Modeling Of Electrostatic Discharge:**

ESD Protection Methodologies Marise Bafleur, Fabrice Caignet, Nicolas Nolhier, 2017-07-26 Failures caused by electrostatic discharges ESD constitute a major problem concerning the reliability and robustness of integrated circuits and electronic systems This book summarizes the many diverse methodologies aimed at ESD protection and shows through a number of concrete studies that the best approach in terms of robustness and cost effectiveness consists of implementing a global strategy of ESD protection ESD Protection Methodologies begins by exploring the various normalized test techniques that are used to qualify ESD robustness as well as characterization and defect localization methods aimed at implementing corrective measures Due to the increasing complexity of integrated circuits it is important to be able to provide a simulation in which the implemented ESD protection strategy provides the desired protection while not harming the performance levels of the circuit Therefore the main features and difficulties related to the different types of simulation finite element SPICE type and behavioral are then studied To conclude several case studies are presented which provide real life examples of the approaches explained in the previous chapters and validate a number of the strategies from component to system level Provides a global ESD protection approach from component to system including both the proposal of investigation techniques and predictive simulation methodologies Addresses circuit and system designers as well as failure analysis engineers Provides the description of specifically developed investigation techniques and the application of the proposed methodologies to real case studies

**System Level ESD Co-Design** Charvaka Duvvury, Harald Gossner, 2017-05-05 An effective and cost efficient protection of electronic system against ESD stress pulses specified by IEC 61000 4 2 is paramount for any system design This pioneering book presents the collective knowledge of system designers and system testing experts and state of the art techniques for achieving efficient system level ESD protection with minimum impact on the system performance All categories of system failures ranging from hard to soft types are considered to review simulation and tool applications that can be used The principal focus of System Level ESD Co Design is defining and establishing the importance of co design efforts from both IC supplier and system builder perspectives ESD designers often face challenges in meeting customers system level ESD requirements and therefore a clear understanding of the techniques presented here will facilitate effective simulation approaches leading to better solutions without compromising system performance With contributions from Robert Ashton Jeffrey Dunnihoo Micheal Hopkins Pratik Maheshwari David Pomerence Wolfgang Reinprecht and Matti Usumaki readers benefit from hands on experience and in depth knowledge in topics ranging from ESD design and the physics of system ESD phenomena to tools and techniques to address soft failures and strategies to design ESD robust systems that include mobile and automotive applications The first dedicated resource to system level ESD co design this is an essential reference for industry ESD designers system builders IC suppliers and customers and also Original Equipment Manufacturers OEMs Key features Clarifies the concept of system level ESD protection Introduces a co design approach for

ESD robust systems Details soft and hard ESD fail mechanisms Detailed protection strategies for both mobile and automotive applications Explains simulation tools and methodology for system level ESD co design and overviews available test methods and standards Highlights economic benefits of system ESD co design      **Signal Integrity and Radiated Emission of**

**High-Speed Digital Systems** Spartaco Caniggia,Francescaromana Maradei,2008-11-20 Before putting digital systems for information technology or telecommunication applications on the market an essential requirement is to perform tests in order to comply with the limits of radiated emission imposed by the standards This book provides an investigation into signal integrity SI and electromagnetic interference EMI problems Topics such as reflections crosstalk switching noise and radiated emission RE in high speed digital systems are covered which are essential for IT and telecoms applications The highly important topic of modelling is covered which can reduce costs by enabling simulation data to demonstrate that a product meets design specifications and regulatory limits According to the new European EMC directive this can help to avoid the expensive use of large semi anechoic chambers or open area test sites for radiated emission assessments Following a short introduction to signalling and radiated interference in digital systems the book provides a detailed characterization of logic families in terms of static and dynamic characteristic useful for modelling techniques Crosstalk in multi coupled line structures are investigated by analytical graphical and circuit based methods and techniques to mitigate these phenomena are provided Grounding filtering and shielding with multilayer PCBs are also examined and design rules given Written by authors with extensive experience in industry and academia Explains basic conceptual problems from a theoretical and practical point of view by using numerous measurements and simulations Presents models for mathematical and SPICE like circuit simulators Provides examples of using full wave codes for SI and RE investigations Companion website containing lists of codes and sample material Signal Integrity and Radiated Emission of High Speed Digital Systems is a valuable resource to industrial designers of information technology telecommunication equipment and automation equipment as well as to development engineers It will also be of interest to managers and designers of consumer electronics and researchers in electronics      Electrostatic Discharge Protection Juin J. Liou,2017-12-19 Electrostatic discharge ESD is one of the most prevalent threats to electronic components In an ESD event a finite amount of charge is transferred from one object i e

human body to another i e microchip This process can result in a very high current passing through the microchip within a very short period of time Thus more than 35 percent of single event chip damages can be attributed to ESD events and designing ESD structures to protect integrated circuits against the ESD stresses is a high priority in the semiconductor industry Electrostatic Discharge Protection Advances and Applications delivers timely coverage of component and system level ESD protection for semiconductor devices and integrated circuits Bringing together contributions from internationally respected researchers and engineers with expertise in ESD design optimization modeling simulation and characterization this book bridges the gap between theory and practice to offer valuable insight into the state of the art of ESD protection

Amplified with tables, figures, and case studies, the text instills a deeper understanding of ESD events and ESD protection design principles. Examines vital processes including Si CMOS, Si BCD, Si SOI, and GaN technologies. Addresses important aspects pertinent to the modeling and simulation of ESD protection solutions. Electrostatic Discharge Protection: Advances and Applications provides a single source for cutting edge information vital to the research and development of effective, robust ESD protection solutions for semiconductor devices and integrated circuits.

*ESD — The Scourge of Electronics* Sten Hellström, 2012-12-06 This book on electrostatic discharge phenomena is essentially a translation and update of a Swedish edition from 1992. The book is intended for people working with electronic circuits and equipments in application and development. All personnel should be aware of the ESD hazards, especially those responsible for quality. ESD prevention is a part of TQM (Total Quality Management). The book is also usable for courses on the subject. Background: It was soon realized that the MOS circuits (MOS Metal Oxide Semiconductor) which appeared in the beginning of the 1960s were sensitive to electrostatic discharges. But a severe accident accelerated the search for materials that do not generate electric charges. In April 1964, three people were working inside a satellite at Cape Kennedy Space Center. They suddenly screamed: we are burning. They died. The satellite encapsulation was covered with untreated plastics to protect against dust. When the plastics were pulled off, both this and the metal encapsulating got charged. A discharge from the metal ignited inflammable parts of the satellite. Eleven more people were injured, and the cost of the accident amounted to about 55 billions USD.

**The Proceedings of the 19th Annual Conference of China Electrotechnical Society** Qingxin Yang, Zhaohong Bie, Xu Yang, 2025-04-26 This book compiles exceptional papers presented at the 19th Annual Conference of the China Electrotechnical Society (CES) held in Xi'an, China, from September 20 to 22, 2024. It encompasses a wide range of topics including electrical technology, power systems, electromagnetic emission technology, and electrical equipment. The book highlights innovative solutions that integrate concepts from various disciplines, making it a valuable resource for researchers, engineers, practitioners, research students, and interested readers.

*Topical Drifts in Intelligent Computing* Jyotsna Kumar Mandal, Pao-Ann Hsiung, Rudra Sankar Dhar, 2022-04-23 This book gathers a collection of high quality peer reviewed research papers presented at International Conference on Computational Techniques and Applications (ICCTA 2021) organized by the Electronics and Telecommunication Engineers (IETE) Kolkata Center, India, during 8-9 October 2021. This includes research in the areas of intelligent computing and communication systems including computing electronics, green energy design, communications, computers to interact and disseminate information on latest developments both academically and industrially for computational drifts. The three main tracks are: i) computing in network security, AI and data science; ii) contemporary issues in electronics and communication technology; and iii) intelligent computing in electrical power control systems and energy technology.

**Practical ESD Protection Design** Albert Wang, 2022-01-06 An authoritative single volume reference on the design and analysis of ESD protection for ICs. Electrostatic discharge (ESD) is a major reliability

challenge to semiconductors integrated circuits ICs and microelectronic systems On chip ESD protection is a vital to any electronic products such as smartphones laptops tablets and other electronic devices Practical ESD Protection Design provides comprehensive and systematic guidance on all major aspects of designs of on chip ESD protection for integrated circuits ICs Written for students and practicing engineers alike this one stop resource covers essential theories hands on design skills computer aided design CAD methods characterization and analysis techniques and more on ESD protection designs Detailed chapters examine an array of topics ranging from fundamental to advanced including ESD phenomena ESD failure analysis ESD testing models ESD protection devices and circuits ESD design layout and technology effects ESD design flows and co design methods ESD modelling and CAD techniques and future ESD protection concepts Based on the author s decades of design research and teaching experiences Practical ESD Protection Design Features numerous real world ESD protection design examples Emphasizes on ESD protection design techniques and procedures Describes ESD IC co design methodology for high performance mixed signal ICs and broadband radio frequency RF ICs Discusses CAD based ESD protection design optimization and prediction using both Technology and Electrical Computer Aided Design TCAD ECAD simulation Addresses new ESD CAD algorithms and tools for full chip ESD physical design verification Explores the disruptive future outlook of ESD protection Practical ESD Protection Design is a valuable reference for industrial engineers and academic researchers in the field and an excellent textbook for electronic engineering courses in semiconductor microelectronics and integrated circuit designs

**Computer Applications in Engineering and Management** Parveen Berwal,Jagjit Singh Dhatteval,Kuldeep Singh Kaswan,Shashi Kant,2022-04-08 The book Computer Applications in Engineering and Management is about computer applications in management electrical engineering electronics engineering and civil engineering It covers the software tools for office automation introduces the basic concepts of database management and provides an overview about the concepts of data communication internet and e commerce Additionally the book explains the principles of computing management used in construction of buildings in civil engineering and the role of computers in power grid automation in electronics engineering Features Provides an insight to prospective research and application areas related to industry and technology Includes industry based inputs Provides a hands on approach for readers of the book to practice and assimilate learning This book is primarily aimed at undergraduates and graduates in computer science information technology civil engineering electronics and electrical engineering management academicians and research scholars *Electrical Overstress/Electrostatic Discharge Symposium Proceedings ,2004* Mikroelektronik 89 A.

Lechner,M. Schrödl,R. Weiß,E. Brenner,P. Seifter,W. Kosta,G. Raimann,F. Furtner,D. Kirchner,H. Bühler,B. Nemsic,W. Jerono,G. Horak,R. Messaros,F. Seifert,P. Fey,A. Goßlau,A. Gottwald,W. Gáspár-Ruppert,P. Kreuzgruber,A. L. Scholtz,W. Smutny,P. Koschnick,W. Schulz,R. Schwarze,Peter W. Fröhling,Robert Casari,E. Fugger,P. Spinadel,J. Niwinski,L. Prager,E. Schauer,G. Rigler,K. Moshhammer,H. Arnold,W. Pribyl,R. Röhrer,W. A. Halang,K. Barbier,F. Schiestl,C. Blind,H. Kamper,A.

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*ESD* Steven H. Voldman, 2006-11-02 With the growth of high speed telecommunications and wireless technology it is becoming increasingly important for engineers to understand radio frequency RF applications and their sensitivity to electrostatic discharge ESD phenomena This enables the development of ESD design methods for RF technology leading to increased protection against electrical overstress EOS and ESD ESD RF Technology and Circuits Presents methods for synthesizing ESD networks for RF applications to achieve improved performance and ESD protection of semiconductor chips discusses RF ESD design methods of capacitance load transformation matching network capacitance shunts inductive shunts impedance isolation load cancellation methods distributed loads emitter degeneration buffering and ballasting examines ESD protection and design of active and passive elements in RF complementary metal oxide semiconductor CMOS RF laterally diffused metal oxide semiconductor LDMOS RF BiCMOS Silicon Germanium SiGe RF BiCMOS Silicon Germanium Carbon SiGeC and Gallium Arsenide technology gives information on RF ESD testing methodologies RF degradation effects and failure mechanisms for devices circuits and systems highlights RF ESD mixed signal design integration of digital analog and RF circuitry sets out examples of RF ESD design computer aided design methodologies covers state of the art RF ESD input circuits as well as voltage triggered to RC triggered ESD power clamps networks in RF technologies as well as off chip protection concepts Following the authors series of books on ESD this

book will be a thorough overview of ESD in RF technology for RF semiconductor chip and ESD engineers Device and circuit engineers working in the RF domain and quality reliability and failure analysis engineers will also find it a valuable reference in the rapidly growing area of RF ESD design In addition it will appeal to graduate students in RF microwave technology and RF circuit design Scientific and Technical Aerospace Reports ,1995 Lists citations with abstracts for aerospace related reports obtained from world wide sources and announces documents that have recently been entered into the NASA

Scientific and Technical Information Database **Principles and Techniques of Electromagnetic Compatibility** Christos Christopoulos,2022-07-14 This book provides a sound grasp of the fundamental concepts applications and practice of EMC Developments in recent years have resulted in further increases in electrical component density wider penetration of wireless technologies and a significant increase in complexity of electrical and electronic equipment New materials which can be customized to meet EMC needs have been introduced Considerable progress has been made in developing numerical tools for complete system EMC simulation EMC is now a central consideration in all industrial sectors Maintaining the holistic approach of the previous edition of Principles and Techniques of Electromagnetic Compatibility the Third Edition updates coverage of EMC to reflect recent important developments What is new in the Third Edition A comprehensive treatment of new materials meta and nano and their impact on EMC Numerical modelling of complex systems and complexity reduction methods Impact of wireless technologies and the Internet of Things IoT on EMC Testing in reverberation chambers and in the time domain A comprehensive treatment of the scope and development of stochastic models for EMC EMC issues encountered in automotive railway aerospace and marine applications Impact of EMC and Intentional EMI IEMI on infrastructure and risk assessment In addition to updating material new references examples and appendices were added to offer further support to readers interested in exploring further As in previous editions the emphasis is on building a sound theoretical framework and demonstrating how it can be turned to practical use in challenging applications The expectation is that this approach will serve EMC engineers through the inevitable future technological shifts and developments

**Introduction to Electromagnetic Compatibility** Clayton R. Paul,Robert C. Scully,Mark A. Steffka,2022-11-01 INTRODUCTION TO ELECTROMAGNETIC COMPATIBILITY The revised new edition of the classic textbook is an essential resource for anyone working with today's advancements in both digital and analog devices communications systems as well as power energy generation and distribution Introduction to Electromagnetic Compatibility provides thorough coverage of the techniques and methodologies used to design and analyze electronic systems that function acceptably in their electromagnetic environment Assuming no prior familiarity with electromagnetic compatibility this user friendly textbook first explains fundamental EMC concepts and technologies before moving on to more advanced topics in EMC system design This third edition reflects the results of an extensive detailed review of the entire second edition embracing and maintaining the content that has stood the test of time such as from the theory of electromagnetic phenomena and associated



mathematics to the practical background information on U S and international regulatory requirements In addition to converting Dr Paul s original SPICE exercises to contemporary utilization of LTSPICE there is new chapter material on antenna modeling and simulation This edition will continue to provide invaluable information on computer modeling for EMC circuit board and system level EMC design EMC test practices EMC measurement procedures and equipment and more such as Features fully worked examples topic reviews self assessment questions end of chapter exercises and numerous high quality images and illustrations Contains useful appendices of phasor analysis methods electromagnetic field equations and waves The ideal textbook for university courses on EMC Introduction to Electromagnetic Compatibility Third Edition is also an invaluable reference for practicing electrical engineers dealing with interference issues or those wanting to learn more about electromagnetic compatibility to become better product designers

### **Simulation Methods for ESD Protection**

**Development** Harald Gossner,Kai Esmark,Wolfgang Stadler,2003-10-16 Simulation Methods for ESD Protection

Development looks at the integration of new techniques into a comprehensive development flow which is now available due advances made in the field during the recent years These findings allow for an early stable ESD concept at a very early stage of the technology development which is essential now development cycles have been reduced The book also offers ways of increasing the optimization and control of the technology concerning performance thus making the process more cost effective and increasingly efficient This title provides a guide through the latest research and technology presenting the ESD protection development methodology This is based on a combination of process device and circuit stimulation and addresses the optimization of the industry critical issue reduced development cycles Written to address the needs of the ESD engineer this text is required reading by all industry practitioners and researchers and students within this field The FIRST Extensive overview on the subject of ESD simulation Addresses the industry critical issue of reduced development cycles and provides solutions Presents the latest research in the field with high practical relevance and its results

### **The Modelling and**

**Characterization of Dielectric Barrier Discharge-Based Cold Plasma Jets** G Divya Deepak,2020-01-15 Non equilibrium atmospheric pressure plasma jets APPJs are of intense interest in current low temperature plasma research because of their immense potential for material processing and biomedical applications Depending on the jet configuration and the electrical excitation plasma characteristics including heat charged particle electric field and chemically active species may differ significantly Other important parameters of importance in these studies are the kind of utilized working gas and gas flow rate This book presents the electrical characterization of DBD based APPJs for three electrode arrangements ring electrode pin electrode and floating helix electrode configurations The analysis presented here will serve to help in establishing an optimum range of operation for a cold plasma jet without arcing and without any physical damage to the electrodes Furthermore the experimental results provided in the book establish the significance of the type of working gas on the power consumption and on the jet length obtained These developed cold DBD based APPJs of larger lengths may be useful for

diverse biological applications and surface treatments      MOSFET Models for VLSI Circuit Simulation Narain D. Arora, 2012-12-06 Metal Oxide Semiconductor MOS transistors are the basic building block of MOS integrated circuits I C Very Large Scale Integrated VLSI circuits using MOS technology have emerged as the dominant technology in the semiconductor industry Over the past decade the complexity of MOS IC s has increased at an astonishing rate This is realized mainly through the reduction of MOS transistor dimensions in addition to the improvements in processing Today VLSI circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0.5 microns are in volume production Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator This problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models There is extensive literature available on modeling these short channel devices However there is a lot of confusion too Often it is not clear what model to use and which model parameter values are important and how to determine them After working over 15 years in the field of semiconductor device modeling I have felt the need for a book which can fill the gap between the theory and the practice of MOS transistor modeling This book is an attempt in that direction      ULSI Science and Technology/1997 Hisham Z. Massoud, 1997      **Applied Mechanics, Mechatronics And Intelligent Systems - Proceedings Of The 2015 International Conference (Ammis2015)** Shihong Qin, Xiaolong Li, 2015-12-08 This book consists of one hundred and twenty five selected papers presented at the 2015 International Conference on Applied Mechanics Mechatronics and Intelligent Systems AMMIS2015 which was held in Nanjing China during June 19-20 2015 AMMIS2015 focuses on seven main areas namely applied mechanics control and automation intelligent systems computer technology electronics engineering electrical engineering and materials science and technology Experts in this field from all over the world contributed to the collection of research results and development activities AMMIS2015 provides an excellent international exchange platform for researchers to share their development works and results in these areas All papers selected for this proceeding were subjected to a rigorous peer review process

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### Circuit And Numerical Modeling Of Electrostatic Discharge Introduction

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