

Fei Yuan

CMOS Active Inductors and Transformers

Principle, Implementation, and Applications

Cmos Active Inductors And Transformers Principle Implementation And Applications 1st Edition

University of Colorado Boulder



Cmos Active Inductors And Transformers Principle Implementation And Applications 1st Edition:

CMOS Active Inductors and Transformers Fei Yuan, 2008-06-17 Many new topologies and circuit design techniques have emerged recently to improve the performance of active inductors but a comprehensive treatment of the theory topology characteristics and design constraint of CMOS active inductors and transformers and a detailed examination of their emerging applications in high speed analog signal processing and data communications over wire and wireless channels is not available This book is an attempt to provide an in depth examination and a systematic presentation of the operation principles and implementation details of CMOS active inductors and transformers and a detailed examination of their emerging applications in high speed analog signal processing and data communications over wire and wireless channels The content of the book is drawn from recently published research papers and are not available in a single cohesive book Equal emphasis is given to the theory of CMOS active inductors and transformers and their emerging applications Major subjects to be covered in the book include inductive characteristics in high speed analog signal processing and data communications spiral inductors and transformers modeling and limitations a historical perspective of device synthesis the topology characterization and implementation of CMOS active inductors and transformers and the application of CMOS active inductors and transformers in high speed analog and digital signal processing and data communications [Index to IEEE Publications](#) Institute of Electrical and Electronics Engineers, 1990 Issues for 1973 cover the entire IEEE technical literature

Dissertation Abstracts International ,2004 **Catalog** University of Colorado Boulder, 2004 **Proceedings** ,2004 **Index to IEEE Periodicals** Institute of Electrical and Electronics Engineers, 1972 **Electrical & Electronics Abstracts** ,1997 [Design, Simulation and Applications of Inductors and Transformers for Si RF ICs](#) Ali M. Niknejad, Robert G. Meyer, 2005-12-15 The modern wireless communication industry has put great demands on circuit designers for smaller cheaper transceivers in the gigahertz frequency range One tool which has assisted designers in satisfying these requirements is the use of on chip inductive elements inductors and transformers in silicon Si radio frequency RF integrated circuits ICs These elements allow greatly improved levels of performance in Si monolithic low noise amplifiers power amplifiers up conversion and down conversion mixers and local oscillators Inductors can be used to improve the intermodulation distortion performance and noise figure of small signal amplifiers and mixers In addition the gain of amplifier stages can be enhanced and the realization of low cost on chip local oscillators with good phase noise characteristics is made feasible In order to reap these benefits it is essential that the IC designer be able to predict and optimize the characteristics of on chip inductive elements Accurate knowledge of inductance values quality factor Q and the influence of adjacent elements on chip proximity effects and substrate losses is essential In this book the analysis modeling and application of on chip inductive elements is considered Using analyses based on Maxwells equations an accurate and efficient technique is developed to model these elements over a wide frequency range Energy loss to the conductive substrate is modeled through several

mechanisms including electrically induced displacement and conductive currents and by magnetically induced eddy currents. These techniques have been compiled in a user friendly software tool ASITIC Analysis and Simulation of Inductors and Transformers for Integrated Circuits. **Improved Control System for Process, Voltage, and Temperature**

Compensation of CMOS Active Inductors Reinhold Frederick William Hollender (III.), 2011 Wireless communications play an increasingly large role in today's society. Today many wireless functions are necessarily integrated into chips and other small packages to support miniaturized wireless devices such as cell phones, laptops, netbooks, etc. These Radio Frequency Integrated Circuits (RFICs) often require inductances to perform their function. Most RFICs utilize spiral inductors. Spiral inductors have their properties defined by their physical dimensions, often taking up large areas of IC real estate. There has been research into replacing these spiral inductors with active elements to reduce their size. However, these active inductors are based on parasitic elements that can vary significantly over temperature, supply voltage, and processing variations of the wafers themselves. This professional paper documents an improved control scheme to maintain correct active inductor behavior over process voltage and temperature variations in applications where the active inductor is used in a Wilkinson power divider. **Integrated Inductors and Transformers** Egidio Ragonese, Angelo Scuderi, Tonio Biondi, Giuseppe Palmisano, 2010-11-15 With the ability to improve performance, reduce fabrication costs, and increase integration levels of both RX and TX sections of the RF/mm-wave front end, passive inductive components have experienced extraordinary growth in ICs. Therefore, a fundamental understanding of monolithic inductors and transformers has become essential for all process engineers. *The Study of CMOS Based VCO with Active Inductor and Its Design Methodology* Sy-Min Chueng, 2012 Active Inductors are useful in reducing the large chip area typically consumed by spiral inductors, as well as providing larger inductance values and higher quality factors that otherwise cannot be achieved by spiral inductors. Integrated inductors find application in many radio frequency (RF) front end integrated circuits, including impedance matching, filtering, biasing, and in oscillator circuits. Nonetheless, because of the interdependent relationship of the self resonant frequency and quality factor, it is often difficult to meet desired circuit requirements. Additionally, active devices pose problems of higher power consumption, noise figure, and potential instability. This thesis begins with the study of active inductors, the Wu active inductor in particular, and considers tuning methods based on the Wu active inductor topology. Starting with the small signal model, the emulated inductance and quality factor expressions are derived. Next, the operation of active inductors under large signal is closely examined. Comparisons between a passive and an active VCO are made. The active inductor based voltage controlled oscillator (Active VCO) is studied extensively, and the methods of improving the performance under large signal behavior are discussed. Then, a design procedure based on gm/ID methodology is proposed. A Matlab script that can be applied to gyrator C based active inductors is developed to determine the sizing of the transistors for a desired inductance and resonant frequency. Cadence Virtuoso is used for simulations, and extraction is based on an IBM 8RF technology file. Finally, a low power

active VCO is designed simulated and laid out

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