



Council for Technical Education and Vocational Training

Office of the Controller of Examinations

Sanothimi, Bhaktapur

Regular/Back Exam – 2081 Bhadra/Ashwin

Program:	Diploma in Computer Engineering/ Information Technology	Full Marks: 80
Year/Part:	II/II (2022) © Arjun	Pass Marks: 32
Subject:	Microprocessor and Computer Architecture	Time: 3 hrs.

*Candidates are required to give their answers in their own words as far as practicable. The figures in the margin indicate full marks.*

Attempt any **TEN** questions.



[www.arjun00.com.np](http://www.arjun00.com.np)

1. Define bus. What are the different types of bus in microprocessor? Explain them. [2+6]
2. What is addressing mode? Explain the addressing modes of 8085 with suitable example. [2+6]
3.
  - a. WAP to add the contents of memory location 4500H and 4501H and store the sum at memory location 4502H. [4]
  - b. WAP to find the 2's complement of the number stored at memory location 4200H and store the complemented number at memory location 4300H. [4]
4. Write an assembly language to multiply two 8 bit numbers 8EH and 2FH. Store the result in memory location C000H and C001H. Also, exchange the result to register C to register B from the accumulator. [8]
5. What are the limitations of 8085-microprocessor? Draw the timing diagram of 8085 OP code fetch cycle. [4+4]
6. Define computer architecture and computer organization. Explain memory hierarchy with a diagram. [8]
7. Differentiate between RISC and CISC. [8]
8. What are different modes of data transfer in computer architecture? Explain. [8]

*Cont. ....*

# Diploma 4th Sem Exam Papers Of Microprocessor

**Ian Pickup**



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


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