Miloš ERCEGOVAC | Tomás LANG

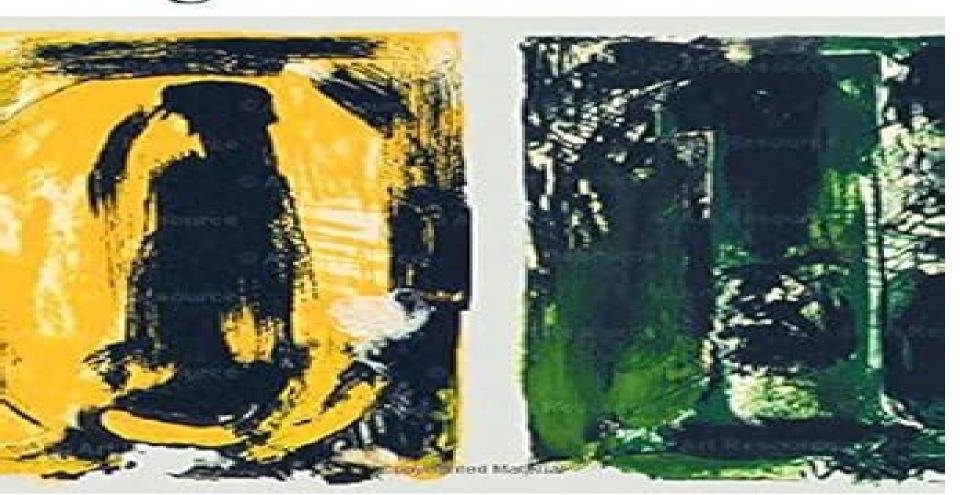# Digital Arithmetic

Digital Arithmetic Ercegovac

Jean-Pierre Deschamps,Gery J.A. Bioul,Gustavo D. Sutter

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Digital Arithmetic Miloš D. Ercegovac,Tomás Lang,2003-09-15 Digital arithmetic plays an important role in the design of general purpose digital processors and of embedded systems for signal processing graphics and communications In spite of a mature body of knowledge in digital arithmetic each new generation of processors or digital systems creates new arithmetic design problems Designers researchers and graduate students will find solid solutions to these problems in this comprehensive state of the art exposition of digital arithmetic Ercegovac and Lang two of the field s leading experts deliver a unified treatment of digital arithmetic tying underlying theory to design practice in a technology independent manner They consistently use an algorithmic approach in defining arithmetic operations illustrate concepts with examples of designs at the logic level and discuss cost performance characteristics throughout Students and practicing designers alike will find Digital Arithmetic a definitive reference and a consistent teaching tool for developing a deep understanding of the arithmetic style of algorithms and designs Guides readers to develop sound solutions avoid known mistakes and repeat successful design decisions Presents comprehensive coverage3 4from fundamental theories to current research trends Written in a clear and engaging style by two masters of the field Concludes each chapter with in depth discussions of the key literature Includes a full set of over 250 exercises   Synthesis of Arithmetic Circuits Jean-Pierre Deschamps,Gery J.A. Bioul,Gustavo D. Sutter,2006-03-31 A new approach to the study of arithmetic circuits In Synthesis of Arithmetic Circuits FPGA ASIC and Embedded Systems the authors take a novel approach of presenting methods and examples for the synthesis of arithmetic circuits that better reflects the needs of today s computer system designers and engineers Unlike other publications that limit discussion to arithmetic units for general purpose computers this text features a practical focus on embedded systems Following an introductory chapter the publication is divided into two parts The first part Mathematical Aspects and Algorithms includes mathematical background number representation addition and subtraction multiplication division other arithmetic operations and operations in finite fields The second part Synthesis of Arithmetic Circuits includes hardware platforms general principles of synthesis adders and subtractors multipliers dividers and other arithmetic primitives In addition the publication distinguishes itself with A separate treatment of algorithms and circuits a more useful presentation for both software and hardware implementations Complete executable and synthesizable VHDL models available on the book s companion Web site allowing readers to generate synthesizable descriptions Proposed FPGA implementation examples namely synthesizable low level VHDL models for the Spartan II and Virtex families Two chapters dedicated to finite field operations This publication is a must have resource for students in computer science and embedded system designers engineers and researchers in the field of hardware and software computer system design and development An Instructor Support FTP site is available from the Wiley editorial department   **Handbook of Floating-Point Arithmetic** Jean-Michel Muller,Nicolas Brisebarre,Florent de Dinechin,Claude-Pierre Jeannerod,Vincent Lefèvre,Guillaume Melquiond,Nathalie

Revol,Damien Stehlé,Serge Torres,2009-11-11 Floating point arithmetic is the most widely used way of implementing real number arithmetic on modern computers However making such an arithmetic reliable and portable yet fast is a very difficult task As a result floating point arithmetic is far from being exploited to its full potential This handbook aims to provide a complete overview of modern floating point arithmetic So that the techniques presented can be put directly into practice in actual coding or design they are illustrated whenever possible by a corresponding program The handbook is designed for programmers of numerical applications compiler designers programmers of floating point algorithms designers of arithmetic operators and more generally students and researchers in numerical analysis who wish to better understand a tool used in their daily work and research **Application-Specific Arithmetic** Florent de Dinechin,Martin Kumm,2024-03-12 Written by two experts of the domain this book presents the most recent advances in computer arithmetic hardware with a focus on application specific arithmetic beyond the classic operators and the standard precisions It targets silicon designers who have to do better with less in the post Moore era and FPGA developers who want to exploit the full possibilities of reconfigurable computing platforms , Computing Technologies and Applications Latesh Malik,Sandhya Arora,Urmila Shrawankar,Maya Ingle,Indu Bhagat,2021-11-09 Making use of digital technology for social care is a major responsibility of the computing domain Social care services require attention for ease in social systems e farming and automation etc Thus the book focuses on suggesting software solutions for supporting social issues such as health care learning about and monitoring for disabilities and providing technical solutions for better living Technology is enabling people to have access to advances so that they can have better health To undergo the digital transformation the current processes need to be completely re engineered to make use of technologies like the Internet of Things IoT big data analytics artificial intelligence and others Furthermore it is also important to consider digital initiatives in tandem with their cloud strategy instead of treating them in isolation At present the world is going through another possibly even stronger revolution the use of recent computing models to perform complex cognitive tasks to solve social problems in ways that were previously either highly complicated or extremely resource intensive This book not only focuses the computing technologies basic theories challenges and implementation but also covers case studies It focuses on core theories architectures and technologies necessary to develop and understand the computing models and their applications The book also has a high potential to be used as a recommended textbook for research scholars and post graduate programs The book deals with a problem solving approach using recent tools and technology for problems in health care social care etc Interdisciplinary studies are emerging as both necessary and practical in universities This book helps to improve computational thinking to understand and change the world It will be a link between computing and a variety of other fields Case studies on social aspects of modern societies and smart cities add to the contents of the book to enhance book adoption potential This book will be useful to undergraduates postgraduates researchers and industry professionals Every chapter covers one possible solution in detail along with results

Embedded Software and Systems Yann-Hang Lee,Heung-Nam Kim,Jong Kim,Yongwan Park,Laurence T. Yang,Sung Won Kim,2007-06-30 This book constitutes the refereed proceedings of the Third International Conference on Embedded Software and Systems ICESS 2007 held in Daegu Korea May 2007 The 75 revised full papers cover embedded architecture embedded hardware embedded software HW SW co design and SoC multimedia and HCI pervasive ubiquitous computing and sensor network power aware computing real time systems security and dependability and wireless communication

Computational Science -- ICCS 2005 V.S. Sunderam,2005-05-12 The three volume set LNCS 3514 3516 constitutes the refereed proceedings of the 5th International Conference on Computational Science ICCS 2005 held in Atlanta GA USA in May 2005 The 464 papers presented were carefully reviewed and selected from a total of 834 submissions for the main conference and its 21 topical workshops The papers span the whole range of computational science ranging from numerical methods algorithms and computational kernels to programming environments grids networking and tools These fundamental contributions dealing with computer science methodologies and techniques are complemented by papers discussing computational applications and needs in virtually all scientific disciplines applying advanced computational methods and tools to achieve new discoveries with greater accuracy and speed Handbook of Signal Processing Systems Shuvra S. Bhattacharyya,Ed F. Deprettere,Rainer Leupers,Jarmo Takala,2018-10-13 In this new edition of the Handbook of Signal Processing Systems many of the chapters from the previous editions have been updated and several new chapters have been added The new contributions include chapters on signal processing methods for light field displays throughput analysis of dataflow graphs modeling for reconfigurable signal processing systems fast Fourier transform architectures deep neural networks programmable architectures for histogram of oriented gradients processing high dynamic range video coding system on chip architectures for data analytics analysis of finite word length effects in fixed point systems and models of architecture There are more than 700 tables and illustrations in this edition over 300 are in color This new edition of the handbook is organized in three parts Part I motivates representative applications that drive and apply state of the art methods for design and implementation of signal processing systems Part II discusses architectures for implementing these applications and Part III focuses on compilers as well as models of computation and their associated design tools and methodologies **Division and Square root Mobile and Scientific computing markets.** Vijaykumar Holimath,2008

Finite Precision Number Systems and Arithmetic Peter Kornerup,David W. Matula,2010-09-30 Fundamental arithmetic operations support virtually all of the engineering scientific and financial computations required for practical applications from cryptography to financial planning to rocket science This comprehensive reference provides researchers with the thorough understanding of number representations that is a necessary foundation for designing efficient arithmetic algorithms Using the elementary foundations of radix number systems as a basis for arithmetic the authors develop and compare alternative algorithms for the fundamental operations of addition multiplication division and square root with

precisely defined roundings Various finite precision number systems are investigated with the focus on comparative analysis of practically efficient algorithms for closed arithmetic operations over these systems Each chapter begins with an introduction to its contents and ends with bibliographic notes and an extensive bibliography The book may also be used for graduate teaching problems and exercises are scattered throughout the text and a solutions manual is available for instructors   *RTL Hardware Design Using VHDL* Pong P. Chu,2006-04-20 The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient portable and scalable Register Transfer Level RTL digital circuits using the VHDL hardware description language and synthesis software Focusing on the module level design which is composed of functional units routing circuit and storage the book illustrates the relationship between the VHDL constructs and the underlying hardware components and shows how to develop codes that faithfully reflect the module level design and can be synthesized into efficient gate level implementation Several unique features distinguish the book Coding style that shows a clear relationship between VHDL constructs and hardware components Conceptual diagrams that illustrate the realization of VHDL codes Emphasis on the code reuse Practical examples that demonstrate and reinforce design concepts procedures and techniques Two chapters on realizing sequential algorithms in hardware Two chapters on scalable and parameterized designs and coding One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis it also examines the synthesis task from the perspective of the overall development process Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation verification and testing needs and can be easily incorporated into a larger system or reused Discussion is independent of technology and can be applied to both ASIC and FPGA devices With a balanced presentation of fundamentals and practical examples this is an excellent textbook for upper level undergraduate or graduate courses in advanced digital logic Engineers who need to make effective use of today s synthesis software and FPGA devices should also refer to this book   Proceedings of the International Conference on ISMAC in Computational Vision and Bio-Engineering 2018 (ISMAC-CVB) Durai Pandian,Xavier Fernando,Zubair Baig,Fuqian Shi,2019-01-01 These are the proceedings of the International Conference on ISMAC CVB held in Palladam India in May 2018 The book focuses on research to design new analysis paradigms and computational solutions for quantification of information provided by object recognition scene understanding of computer vision and different algorithms like convolutional neural networks to allow computers to recognize and detect objects in images with unprecedented accuracy and to even understand the relationships between them The proceedings treat the convergence of ISMAC in Computational Vision and Bioengineering technology and includes ideas and techniques like 3D sensing human visual perception scene understanding human motion detection and analysis visualization and graphical data presentation and a very wide range of sensor modalities in terms of surveillance wearable applications home automation etc ISMAC CVB is a forum for leading academic scientists researchers and research scholars

to exchange and share their experiences and research results about all aspects of computational vision and bioengineering

Encyclopedia of Computer Science and Technology, Second Edition (Set) Phillip A. Laplante,2017-10-02 With breadth and depth of coverage the Encyclopedia of Computer Science and Technology Second Edition has a multi disciplinary scope drawing together comprehensive coverage of the inter related aspects of computer science and technology The topics covered in this encyclopedia include General and reference Hardware Computer systems organization Networks Software and its engineering Theory of computation Mathematics of computing Information systems Security and privacy Human centered computing Computing methodologies Applied computing Professional issues Leading figures in the history of computer science The encyclopedia is structured according to the ACM Computing Classification System CCS first published in 1988 but subsequently revised in 2012 This classification system is the most comprehensive and is considered the de facto ontological framework for the computing field The encyclopedia brings together the information and historical context that students practicing professionals researchers and academicians need to have a strong and solid foundation in all aspects of computer science and technology   **Secure System Design and Trustable Computing** Chip-Hong Chang,Miodrag Potkonjak,2015-09-17 This book provides the foundations for understanding hardware security and trust which have become major concerns for national security over the past decade Coverage includes issues related to security and trust in a variety of electronic devices and systems related to the security of hardware firmware and software spanning system applications online transactions and networking services This serves as an invaluable reference to the state of the art research that is of critical significance to the security of and trust in modern society s microelectronic supported infrastructures   Computation and Logic in the Real World S. Barry Cooper,2007-06-11 This book constitutes the refereed proceedings of the Third International Conference on Computability in Europe CiE 2007 held in Sienna Italy in June 2007 The 50 revised full papers presented together with 36 invited papers were carefully reviewed and selected from 167 submissions

Elementary Functions: Jean-Michel Muller,2013-03-14 The elementary functions sine cosine exponentials logarithms are the most commonly used mathematical functions Computing them quickly and accurately is a major goal in computer arithmetic This book gives the theoretical background necessary to understand and or build algorithms for computing these functions presents algorithms hardware oriented as well as software oriented and discusses issues related to the accurate floating point implementation of these functions My purpose was not to give cooking recipes that allow to implement some given functions on some given floating point systems but to provide the reader with the knowledge that is necessary to build or adapt algorithms to his or her computing environment When writing this book I have had in mind two different audiences specialists who will have to design floating point systems hardware or soft ware parts orto do research on algorithms and inquiring minds who just want to know what kind of methods are used to compute the math functions in current computers or pocket calculators Because of this the book is in tended to be helpful as well for postgraduate and advanced

undergraduate students in computer science or applied mathematics as for professionals engaged in the design of algorithms programs or circuits that implement floating point arithmetic or simply for engineers or scientists who want to improve their culture in that domain   Advanced Signal-processing Algorithms, Architectures, and Implementations ,1991

    Computer-hardware Evaluation Of Mathematical Functions Amos R Omondi,2015-10-22 Computer Hardware Evaluation of Mathematical Functions provides a thorough up to date understanding of the methods used in computer hardware for the evaluation of mathematical functions reciprocals square roots exponentials logarithms trigonometric functions hyperbolic functions etc It discusses how the methods are derived how they work and how well they work The methods are divided into four core themes CORDIC normalization table look up and polynomial approximations In each case the author carefully considers the mathematical derivation and basis of the relevant methods how effective they are including mathematical errors analysis and how they can be implemented in hardware This book is an excellent resource for any student or researcher seeking a comprehensive yet easily understandable explanation of how computer chips evaluate mathematical functions   **Arm Assembly Language - An Introduction (Second Edition)** J. R. Gibson,2011 An introductory text describing the ARM assembly language and its use for simple programming tasks

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