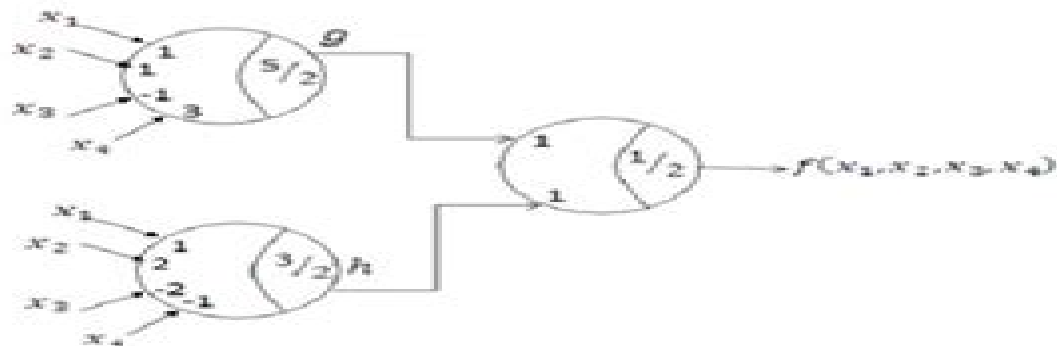


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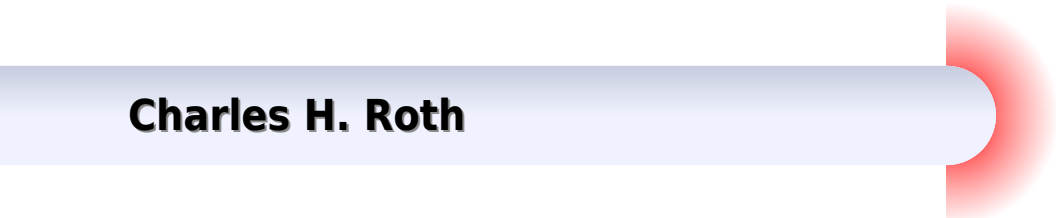
CHAPTER1: Threshold Logic

- Q1. Is it possible to design EX-OR gate with a single threshold element? Justify your answer.
- Q2. Let $f(x_1, x_2, x_3) = \sum(0,1,3)$. Realize it using a threshold element.
- Q3. What are the various capabilities of threshold logic?
- Q5. Determine whether the function $f(x_1, x_2, x_3, x_4) = \sum(0,1,3,4,5,6,7,12,13)$ is a threshold function, and, if it is, find a weight-threshold vector.
- Q6. By examining the linear inequalities, determine which of the following functions is a threshold function, and for each one that is, find the corresponding weight-threshold vector.
- a) $f_1(x_1, x_2, x_3) = \sum(1,2,3,7)$
 - b) $f_1(x_1, x_2, x_3) = \sum(0,2,4,5,6)$
 - c) $f_1(x_1, x_2, x_3) = \sum(0,3,5,6)$
- Q7. Determine the function $f(x_1, x_2, x_3, x_4)$ realized by the network as shown in fig below.



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Kandel, Gideon Langholz, Joe L Mott, 1998-08-11 This text is intended for a first course in digital logic design at the sophomore or junior level for electrical engineering computer engineering and computer science programs as well as for a number of other disciplines such as physics and mathematics The book can also be used for self study or for review by practicing engineers and computer scientists not intimately familiar with the subject After completing this text the student should be prepared for a second advanced course in digital design switching and automata theory microprocessors or computer organization

DIGITAL LOGIC DESIGN Sonali Singh, 2018-06-01 Description The book is an attempt to make Digital Logic Design easy and simple to understand The book covers various features of Logic Design using lots of examples and relevant diagrams The complete text is reviewed for its correctness This book is an outcome of sincere effort and hard work to bring concepts of Digital Logic Design close to the audience of this book The salient features of the book Easy explanation of Digital System and Binary Numbers with lots of solved examples Detailed covering of Boolean Algebra and Gate Level Minimization with proper examples and diagrammatic representation Detailed analysis of different Combinational Logic Circuits Complete Synchronous sequential Logic understanding Deep understanding of Memory and Programmable Logic Detailed analysis of different Asynchronous Sequential Logic Table Of Contents Unit 1 Digital System and Binary Numbers

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Digital Logic Testing and Simulation Alexander Miczo, 1985-12 The new standard in the field presenting the latest design and testing methods for logic circuits and the development of a BASIC based simulation Offers designers and test engineers unique coverage of circuit design for testability stressing the incorporation of hardware into designs that facilitate testing and diagnosis by allowing greater access to internal circuits Examines various ways of representing a design as well as external testing methods that apply this information Digital Logic and State Machine Design David J. Comer, 1984

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