A logic gate circuit with four inputs A, B, C and D and output F is shown below in Figure 1.

(a) Determine the output (in terms of Boolean expressions) of all four logic gates in the circuit.

(b) Complete a truth table for the function F.

(c) From the truth table, write out a 'sum of products' expression for the function F.

(d) Plot the sum of products function F onto a Kamaugh Map.

(e) Directly from the Kamaugh map, write out the minimised function for F.

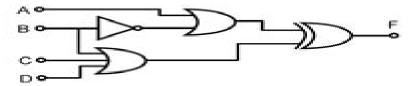


Figure 1

- Simplify the Boolean expression F = A.B + C.(B+A.B) as far as
  possible by first finding an equivalent 'sum of products expression' and
  subsequently utilising a Kamaugh Map.
- (a) Determine the output (in terms of Boolean expressions) of all five logic gates in the circuit shown in Figure 2.

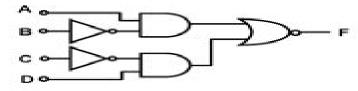


Figure 2

(b) By utilising Boolean algebra, show that the function F obtained in part (a) above, is also equivalent to  $(\overline{A} + B) \cdot (C + \overline{D})$ .

# **Digital Electronics Lab Exam Questions With Answers**

**Christian Drosten** 

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