

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
**B.Tech II Year II Semester Examinations, July/August - 2021**  
**STRUCTURED DIGITAL SYSTEM DESIGN**  
 (Electronics and Instrumentation Engineering)

Time: 3 hours

Max. Marks: 75

Answer any five questions  
 All questions carry equal marks

- 1.a) Design a BCD adder circuit. [7+8]
- b) Design a 4-bit magnitude comparator circuit using above BCD adder.
- 2.a) Explain the terms fan-out, fan-in.
- b) What is the worst case propagation delay of given network shown in figure 1?  
 $At_1 = 10 \text{ nSec}$ ,  $At_2 = 20 \text{ nSec}$ ,  $At_3 = 25 \text{ nSec}$ ,  $At_4 = 10 \text{ nSec}$ . [7+8]



Figure: 1

- 3.a) Differentiate Moore machine from mealy machine.
- b) Convert an edge triggered D - Flip flop to an edge triggered JK flip flop. [7+8]
4. Using the state diagram make state assignment for the given state figure 2. [15]

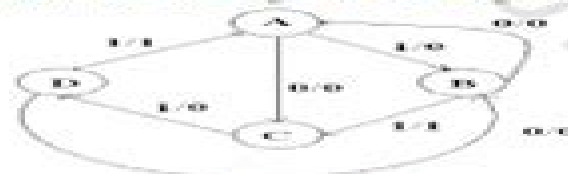


Figure: 2

5. Design a modulo - 6 Gray code counter. Make your code reflective with respect to the 2<sup>nd</sup> MSB. [15]
6. Design a controller that controls the drive circuit of stepper motor. Assume required control word format. [15]
- 7.a) Design a binary multiplier using parallel adder and shift register.
- b) Explain the concept of state assignment with examples. [9+6]
8. Write a short note on:
  - a) FPLA architecture.
  - b) Configurable memories. [7+8]

# Digital System Design Solved Question Papers

**Enrico Macii, Vassilis  
Paliouras, Odysseas Koufopavlou**



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**Integrated Circuit and System Design** Enrico Macii, Vassilis Paliouras, Odysseas Koufopavlou, 2004-08-24 Welcometothe proceedings of PATMOS 2004 the fourteenth in a series of international workshops PATMOS 2004 was organized by the University of Patras with technical co sponsorship from the IEEE Circuits and Systems Society Over the years the PATMOS meeting has evolved into an important European event where industry and academia meet to discuss power and timing aspects in modern integrated circuit and system design PATMOS provides a forum for researchers to discuss and investigate the emerging challenges in sign methodologies and tools required to develop the upcoming generations of integrated circuits and systems We realized this vision this year by providing a technical program that contained state of the art technical contributions a keynote speech three invited talks and two embedded tutorials The technical program focused on timing performance and power consumption as well as architectural aspects with particular emphasis on modelling design characterization analysis and optimization in the nanometer era This year a record 152 contributions were received to be considered for possible presentation at PATMOS Despite the choice for an intense three day meeting only 51 lecture papers and 34 poster papers could be accommodated in the single track technical program The Technical Program Committee with the assistance of additional expert reviewers selected the 85 papers to be presented at

PATMOS and organized them into 13 technical sessions As was the case with the PATMOS workshops the review process was anonymous full papers were required and several reviews were received per manuscript

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performance incentives

**Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation** Vassilis Paliouras, Johan Vounckx, Diederik Verkest, 2005-08-25 Welcome to the proceedings of PATMOS 2005 the 15th in a series of international workshops PATMOS2005 was organized by IMEC with technical co sponsorship from the IEEE Circuits and Systems Society Over the years PATMOS has evolved into an important European event where searchers from both industry and academia discuss and investigate the emerging challenges in future and contemporary applications design methodologies and tools required for the development of upcoming generations of integrated circuits and systems The technical program of PATMOS 2005 contained state of the art technical contributions three invited talks a special session on hearing aid design and an embedded tutorial The technical program focused on timing performance and power consumption as well as architectural aspects with particular emphasis on modeling design characterization analysis and optimization in the nanometer era The Technical Program Committee with the assistance of additional expert reviewers selected the 74 papers to be presented at PATMOS The papers were divided into 11 technical sessions and 3 poster sessions As is always the case with the PATMOS workshops the review process was anonymous full papers were required and several reviews were carried out per paper Beyond the presentations of the papers the PATMOS technical program was enriched by a series of speeches offered by world class experts on important emerging research issues of industrial relevance Prof Jan Rabaey Berkeley USA gave a talk on Traveling the Wild Frontier of Ultra Low Power Design Dr Sung Bae Park Sung gave a presentation on DVL Deep Low Voltage Circuits and Devices Prof *Computer Aided Design of Control Systems* M. A. Cuenod, 2014-05-19 *Computer Aided Design of Control Systems* focuses on the use of computers to analyze and design the control of various processes as well as the development of program packages with different algorithms for digital computers The selection first takes a look at the computer aided design of minimal order controllers including design of interacting and noninteracting dynamic controllers of minimal order and basic algorithm The book then discusses an accelerated Newton process to solve Riccati equation through matrix sign function suboptimal direct digital control of a trickle bed absorption column and structural design of large systems employing a geometric approach The text underscores the computer as an aid for the implementation of advanced control algorithms on physical processes and analysis of direct control algorithms and their parallel realization Topics include hardware influences on the control process influence and interactive structure design of direct control systems The book also takes a look at the optimal control of randomly sampled linear stochastic systems computer aided design of suboptimal test signals for system identification and computer aided design of multi level systems with prescribed structure and control constraints The selection is a dependable source of data for readers interested in the uses of computers

**Frontiers in Computer Education** Yuanzhi Wang, 2015-07-28 This proceedings volume contains selected papers presented at the 2014 International Conference on Frontiers in Computer Education ICFCE 2014 which was held December 24-25 2014 in Wuhan China The objective of this conference was to provide a forum for different researchers in different

fields especially Computer Education as well as Information Technology to exchange their various findings The papers have been grouped under the following overarching themes Computer Science Computer Education Education and Teaching Reform and Communication and Intelligent Control Information and Communication Technologies for Development. Strengthening Southern-Driven Cooperation as a Catalyst for ICT4D Petter Nielsen, Honest Christopher Kimaro, 2019-04-26 The two volumes IFIP AICT 551 and 552 constitute the refereed proceedings of the 15th IFIP WG 9.4 International Conference on Social Implications of Computers in Developing Countries ICT4D 2019 held in Dar es Salaam Tanzania in May 2019 The 97 revised full papers and 2 short papers presented were carefully reviewed and selected from 185 submissions The papers present a wide range of perspectives and disciplines including but not limited to public administration entrepreneurship business administration information technology for development information management systems organization studies philosophy and management They are organized in the following topical sections communities ICT enabled networks and development digital platforms for development ICT for displaced population and refugees How it helps How it hurts ICT4D for the indigenous by the indigenous and of the indigenous local technical papers pushing the boundaries new research methods theory and philosophy in ICT4D southern driven human computer interaction sustainable ICT informatics education and learning in a turbulent world doing the safari way

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**Blockchain - ICBC 2018** Shiping Chen,Harry Wang,Liang-Jie Zhang,2018-06-21 This book constitutes the refereed proceedings of the First International Conference on Blockchain ICBC 2018 held as part of the Services Conference Federation SCF 2018 in Seattle USA in June 2018 The 16 full papers and 7 short papers presented were carefully reviewed and selected from 36 submissions The papers cover a wide range of topics in blockchain technologies platforms solutions and business models such as new blockchain architecture platform constructions blockchain development and blockchain services technologies as well as standards and blockchain services innovation lifecycle including enterprise modeling business consulting solution creation services orchestration services optimization services management services marketing business process integration and management

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**Computer Aided Design in Control Systems 1988** Zhen-Yu Chen,2017-05-03 This volume contains 73 papers presenting the state of the art in computer aided design in control systems CADCS The latest information and exchange of ideas presented at the Symposium illustrates the development of computer aided design science and technology within control systems The Proceedings contain six plenary papers and six special invited papers and the remainder are divided into five themes CADCS packages CADCS software and hardware systems design methods CADCS expert systems CADCS applications with finally a discussion on CADCS in education and research

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