

DIGITAL DESIGN

With An Introduction to the Verilog HDL

FIFTH EDITION

M. MORRIS MANO | MICHAEL D. CILETTI



Digital Design With An Introduction To The Verilog Hdl 5th Ed By Morris Mano International Economy Edition

M. Morris Mano, Michael D. Ciletti



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FPGAs *Verilog HDL* Joseph Cavanagh, 2017-12-19 Emphasizing the detailed design of various Verilog projects Verilog HDL Digital Design and Modeling offers students a firm foundation on the subject matter The textbook presents the complete Verilog language by describing different modeling constructs supported by Verilog and by providing numerous design examples and problems in each chapter Examples include counters of different moduli half adders full adders a carry lookahead adder array multipliers different types of Moore and Mealy machines and much more The text also contains information on synchronous and asynchronous sequential machines including pulse mode asynchronous sequential machines In addition it provides descriptions of the design module the test bench module the outputs obtained from the simulator and the waveforms obtained from the simulator illustrating the complete functional operation of the design Where applicable a detailed review of the topic s theory is presented together with logic design principles including state diagrams Karnaugh maps equations and the logic diagram Verilog HDL Digital Design and Modeling is a comprehensive self contained and inclusive textbook that carries all designs through to completion preparing students to thoroughly understand this popular hardware description language **Digital Design and Verilog HDL Fundamentals** Joseph Cavanagh, 2017-12-19 Comprehensive and self contained this tutorial covers the design of a plethora of combinational and sequential logic circuits using conventional logic design and Verilog HDL Number systems and number representations are presented along with various binary codes Several advanced topics are covered including functional decomposition and iterative networks A variety of examples are provided for combinational and sequential logic computer arithmetic and advanced topics such as Hamming code error correction Constructs supported by Verilog are described in detail All designs are continued to completion Each chapter includes numerous design issues of varying complexity to be resolved by the reader Digital Logic Design Ming-Bo Lin, 2021-01-11 Digital Logic Design is a comprehensive textbook which aims to provide entry level readers a quick start to the field of digital logic design so as to facilitate them with the capability suitable for the versatility of social change and interdisciplinary learning This textbook can be used as a textbook for classroom use in the fields of electronics electrical computer science information engineering mechanical and soon The salient features of this textbook are as follows 1 Introduce incrementally the principles of digital logic design and exemplify each basic theme and concept with abundant illustrations 2 Detail design principles of various combinational modules including decoders encoders multiplexers demultiplexers arithmetic circuits and so on 3 Introduce design principles of various sequential modules including counters registers shift registers sequence generators etc 4 Address the structures features and applications of PLD FPGA devices 5 Exemplify applications of CPLD FPGA devices with Verilog HDL modules 6 Provide 20 basic and application experiments of digital logic to help readers verify the consistence of digital logic between principles and practice 7 Include an abundance of review questions in each section to help readers evaluate their understandings about the section 8 Deal with Verilog HDL concisely in relevant sections so as to make the reader understand how to describe a logic circuit in

Verilog HDL precisely Digital Logic Design is an ideal textbook for the digital logic design course in the fields of electronics electrical computer science information engineering mechanical etc or serves as a valuable reference book for self study

Digital Design using Verilog HDL Atul P. Godse, Dr. Deepali A. Godse, 2020-12-01 This book begins with an introduction to Verilog HDL. It describes basic concepts in Verilog HDL language constructs and conventions and modeling styles: gate level modeling, data flow level modeling, behavioral modeling and switch level modeling. It also describes sequential models, basic memory components, functional register static machine coding and sequential synthesis. The last section of the book focuses on component testing and verification. It includes combinational circuits testing, sequential circuit testing, test bench techniques, design verification and assertion verification. Verilog HDL Samir Palnitkar, 2003 VERILOG HDL Second Edition by Samir Palnitkar With a Foreword by Prabhu Goel Written for both experienced and new users this book gives you broad coverage of Verilog HDL. The book stresses the practical design and verification perspective of Verilog rather than emphasizing only the language aspects. The information presented is fully compliant with the IEEE 1364 2001 Verilog HDL standard. Among its many features this edition: Describes state of the art verification methodologies Provides full coverage of gate dataflow RTL behavioral and switch modeling Introduces you to the Programming Language Interface PLI Describes logic synthesis methodologies Explains timing and delay simulation Discusses user defined primitives Offers many practical modeling tips Includes over 300 illustrations examples and exercises and a Verilog resource list Learning objectives and summaries are provided for each chapter About the CD ROM The CD ROM contains a Verilog simulator with a graphical user interface and the source code for the examples in the book What people are saying about Verilog HDL Mr Palnitkar illustrates how and why Verilog HDL is used to develop today's most complex digital designs This book is valuable to both the novice and the experienced Verilog user I highly recommend it to anyone exploring Verilog based design Rajeev Madhavan Chairman and CEO Magma Design Automation This book is unique in its breadth of information on Verilog and Verilog related topics It is fully compliant with the IEEE 1364 2001 standard contains all the information that you need on the basics and devotes several chapters to advanced topics such as verification PLI synthesis and modeling techniques Michael McNamara Chair IEEE 1364 2001 Verilog Standards Organization This has been my favorite Verilog book since I picked it up in college It is the only book that covers practical Verilog A must have for beginners and experts Berend Ozceri Design Engineer Cisco Systems Inc Simple logical and well organized material with plenty of illustrations makes this an ideal textbook Arun K Somani Jerry R Junkins Chair Professor Department of Electrical and Computer Engineering Iowa State University Ames PRENTICE HALL Professional Technical Reference Upper Saddle River NJ 07458 www.phptr.com ISBN 0 13 044911 3 **FSM-based Digital Design using Verilog HDL** Peter Minns, Ian Elliott, 2008-04-30 As digital circuit elements decrease in physical size resulting in increasingly complex systems a basic logic model that can be used in the control and design of a range of semiconductor devices is vital Finite State Machines FSM have

numerous advantages they can be applied to many areas including motor control and signal and serial data identification to name a few and they use less logic than their alternatives leading to the development of faster digital hardware systems This clear and logical book presents a range of novel techniques for the rapid and reliable design of digital systems using FSMs detailing exactly how and where they can be implemented With a practical approach it covers synchronous and asynchronous FSMs in the design of both simple and complex systems and Petri Net design techniques for sequential parallel control systems Chapters on Hardware Description Language cover the widely used and powerful Verilog HDL in sufficient detail to facilitate the description and verification of FSMs and FSM based systems at both the gate and behavioural levels Throughout the text incorporates many real world examples that demonstrate designs such as data acquisition a memory tester and passive serial data monitoring and detection among others A useful accompanying CD offers working Verilog software tools for the capture and simulation of design solutions With a linear programmed learning format this book works as a concise guide for the practising digital designer This book will also be of importance to senior students and postgraduates of electronic engineering who require design skills for the embedded systems market Principles of Verilog Digital Design Wen-Long Chin,2022-02-27 Covering both the fundamentals and the in depth topics related to Verilog digital design both students and experts can benefit from reading this book by gaining a comprehensive understanding of how modern electronic products are designed and implemented Principles of Verilog Digital Design contains many hands on examples accompanied by RTL codes that together can bring a beginner into the digital design realm without needing too much background in the subject area This book has a particular focus on how to transform design concepts into physical implementations using architecture and timing diagrams Common mistakes a beginner or even an experienced engineer can make are summarized and addressed as well Beyond the legal details of Verilog codes the book additionally presents what uses Verilog codes have through some pertinent design principles Moreover students reading this book will gain knowledge about system level design concepts Several ASIC designs are illustrated in detail as well In addition to design principles and skills modern design methodology and how it is carried out in practice today are explored in depth as well Digital Design John F. Wakerly,2010 Active HDL 6 3 CD offers a completely integrated design entry and verification environment for HDL C and C design It includes sample designs with training exercises to assist students in learning more about the latest design entry and simulation techniques for the Electronic Design Automation EDA industry Also included are self paced VHDL and Verilog tutorials **Hardware Description Language Demystified** Dr. Cherry Bhargava,Dr. Rajkumar Sarma,2020-08-27 Get familiar and work with the basic and advanced Modeling types in Verilog HDL Key Features _ Learn about the step wise process to use Verilog design tools such as Xilinx Vivado Cadence NC SIM _ Explore the various types of HDL and its need _ Learn Verilog HDL modeling types using examples _ Learn advanced concept such as UDP Switch level modeling _ Learn about FPGA based prototyping of the digital system Description Hardware Description Language HDL allows analysis and

simulation of digital logic and circuits The HDL is an integral part of the EDA electronic design automation tool for PLDs microprocessors and ASICs So HDL is used to describe a Digital System The combinational and sequential logic circuits can be described easily using HDL Verilog HDL standardized as IEEE 1364 is a hardware description language used to model electronic systems This book is a comprehensive guide about the digital system and its design using various VLSI design tools as well as Verilog HDL The step wise procedure to use various VLSI tools such as Xilinx Vivado Cadence NC SIM is covered in this book It also explains the advanced concept such as User Define Primitives UDP switch level modeling reconfigurable computing etc Finally this book ends with FPGA based prototyping of the digital system By the end of this book you will understand everything related to digital system design What will you learn _ Implement Adder Subtractor Adder Cum Subtractor using Verilog HDL _ Explore the various Modeling styles in Verilog HDL _ Implement Switch level modeling using Verilog HDL _ Get familiar with advanced modeling techniques in Verilog HDL _ Get to know more about FPGA based prototyping using Verilog HDL Who this book is for Anyone interested in Electronics and VLSI design and want to learn Digital System Design with Verilog HDL will find this book useful IC developers can also use this book as a quick reference for Verilog HDL fundamentals features Table of Contents 1 An Introduction to VLSI Design Tools 2 Need of Hardware Description Language HDL 3 Logic Gate Implementation in Verilog HDL 4 Adder Subtractor Implementation Using Verilog HDL 5 Multiplexer Demultiplexer Implementation in Verilog HDL 6 Encoder Decoder Implementation Using Verilog HDL 7 Magnitude Comparator Implementation Using Verilog HDL 8 Flip Flop Implementation Using Verilog HDL 9 Shift Registers Implementation Using Verilog HDL 10 Counter Implementation Using Verilog HDL 11 Shift Register Counter Implementation Using Verilog HDL 12 Advanced Modeling Techniques 13 Switch Level Modeling 14 FPGA Prototyping in Verilog HDL

HDL with Digital Design Nazeih Botros,2015-06-25 No detailed description available for HDL with Digital Design

Verilog for Digital Design Frank Vahid,Roman Lysecky,2007-07-09 Ideal as either a standalone introductory guide or in tandem with Vahid s Digital Design to allow for greater language coverage this is an accessible introductory guide to hardware description language Verilog is a hardware description language used to model electronic systems sometimes called Verilog HDL and this book is helpful for anyone who is starting out and learning the language Focuses on application and use of the language rather than just teaching the basics of the language

Sequential Logic and Verilog HDL Fundamentals Joseph Cavanagh,2017-12-19 Sequential Logic and Verilog HDL Fundamentals discusses the analysis and synthesis of synchronous and asynchronous sequential machines These machines are implemented using Verilog Hardware Description Language HDL in accordance with the Institute of Electrical and Electronics Engineers IEEE Standard 1364 1995 The book concentrates on sequential logic design with a focus on the design of various Verilog HDL projects Emphasis is placed on structured and rigorous design principles that can be applied to practical applications Each step of the analysis and synthesis procedures is clearly delineated Each method that is presented is expounded in sufficient detail with

accompanying examples Many analysis and synthesis examples use mixed logic symbols incorporating both positive and negative input logic gates for NAND not AND and NOR not OR logic while other examples utilize only positive input logic gates The use of mixed logic parallels the use of these symbols in the industry The book is intended to be a tutorial and as such is comprehensive and self contained All designs are carried through to completion nothing is left unfinished or partially designed Each chapter contains numerous problems of varying complexity to be designed by the reader using Verilog HDL design techniques The Verilog HDL designs include the design module the test bench module that tests the design for correct functionality the outputs obtained from the test bench and the waveforms obtained from the test bench Sequential Logic and Verilog HDL Fundamentals presents Verilog HDL with numerous design examples to help the reader thoroughly understand this popular hardware description language The book is designed for practicing electrical engineers computer engineers and computer scientists for graduate students in electrical engineering computer engineering and computer science and for senior level undergraduate students

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