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Basic VLSI Design Douglas A. Pucknell, Kamran Eshraghian, 1985 Basic VLSI Design Douglas A. Pucknell, Kamran Eshraghian, 1994 *AN INTRODUCTION TO VLSI DESIGN AND TESTING* P. Brundavani ,B. Abdul Raheem ,Rachapudi Prabahakar ,2025-05-20 VLSI Design and Testing provides a concise yet comprehensive guide to the design analysis and testing of integrated circuits Covering key topics such as IC types Moore s Law MOSFET and CMOS fabrication and SOI technology the book builds a strong foundation in VLSI principles It explores the design flow CMOS logic gates layout techniques and both static and dynamic logic circuits Readers will also learn about circuit performance parameters scaling theory and subsystem design including adders shifters and comparators The book concludes with essential concepts in VLSI design styles FPGA gate array full custom and CMOS testing including fault models ATPG and BIST Ideal for students and professionals it blends theory with practical design strategies in modern VLSI systems Visit garuda publishers com **VLSI CAD** CHIPLUNKAR, NIRANJAN N.,KOTARI, MANJUNATH,2011-04-01 This well organised book presents the basics of VLSI along with important algorithms used by CAD tool designers It discusses general VLSI design styles layout design rules technology mapping in FPGAs and 3D FPGAs In addition the text describes three important steps in high level synthesis of VLSI namely partitioning scheduling and data path allocation besides logic synthesis which determines the gate level structure of circuits Finally the book gives a detailed account of physical synthesis where steps such as floorplanning placement routing and compaction are explained with necessary algorithms This book is intended as a text for the undergraduate and postgraduate students of engineering Electrical and Electronics Engineering Electronics and Communication Engineering Computer Science and Engineering besides Instrumentation for their course on VLSI CAD In addition the book would also be extremely useful for professionals in this field **KEY FEATURES** Presents a variety of chip design tools Includes a fairly large number of algorithms Discusses VHDL and graph theory essential for VLSI CAD tool design Provides 100 questions selected from various university examination papers **Proceedings of 2nd International Conference on Micro-Electronics, Electromagnetics and Telecommunications** Suresh Chandra Satapathy,Vikrant Bhateja,P. Satish Rama Chowdary,V.V.S.S. Sameer Chakravarthy,Jaume Anguera,2017-09-06 The book is a collection of best papers presented in the Second International Conference on Microelectronics Electromagnetics and Telecommunication ICMEET 2016 an international colloquium which aims to bring together academic scientists researchers and research scholars to discuss the recent developments and future trends in the fields of microelectronics electromagnetics and telecommunication Microelectronics research investigates semiconductor materials and device physics for developing electronic devices and integrated circuits with data energy efficient performance in terms of speed power consumption and functionality The book discusses various topics like analog digital and mixed signal circuits bio medical circuits and systems RF circuit design microwave and millimeter wave circuits green circuits and systems analog and digital signal processing

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Micro and Nanoelectronics Devices, Circuits and Systems Trupti Ranjan Lenka, Samar K. Saha, Lan Fu, 2023-09-02 This book presents select proceedings of the International Conference on Micro and Nanoelectronics Devices Circuits and Systems MNDCS 2023 The book includes cutting edge research papers in the emerging fields of micro and nanoelectronics devices circuits and systems from experts working in these fields over the last decade The book is a unique collection of chapters from different areas with a common theme and is immensely useful to academic researchers and practitioners in the industry who work in this field

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