

A high-magnification scanning electron micrograph (SEM) of a semiconductor device, likely a microprocessor or memory array. The image shows a complex network of gold-colored metal interconnects and pads on a dark substrate. A black rectangular title box is overlaid in the upper right quadrant, containing the text "Fundamentals of Semiconductor Devices" in a gold, serif font. Below the title box, the authors' names "Betty Lise Anderson • Richard L. Anderson" are printed in a smaller, gold, sans-serif font. The background features a dense grid of rectangular pads and intricate wiring patterns, characteristic of integrated circuit manufacturing.

Fundamentals of Semiconductor Devices

Betty Lise Anderson • Richard L. Anderson

Fundamentals Of Semiconductor Devices

Kevin F. Brennan



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